



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Features

- $BV_{CEO} > 60V$
- $I_C = 6A$ High Continuous Current
- $I_{CM} = 12A$ Peak Pulse Current
- Low Saturation Voltage $V_{CE(sat)} < 60mV @ 1A$
- Complementary PNP Type: DIODES™ NK-DSS60600MZ4

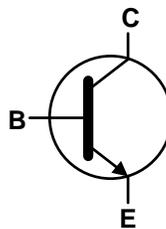
Mechanical Data

- Package: SOT223
- Package Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.115 grams (Approximate)

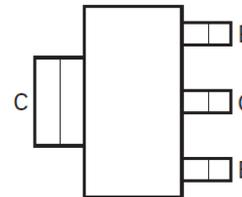
SOT223



Top View



Device Symbol



Top View
Pin-Out

Absolute Maximum Ratings

 (@ $T_A = +25^{\circ}\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CB0}	100	V
Collector-Emitter Voltage	V_{CEO}	60	V
Emitter-Base Voltage	V_{EBO}	6	V
Continuous Collector Current	I_C	6	A
Peak Pulse Collector Current	I_{CM}	12	A

Thermal Characteristics

 (@ $T_A = +25^{\circ}\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation	P_D	(Note 5) 3	W
		(Note 6) 2	
		(Note 7) 1.2	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	(Note 5) 41.7	$^{\circ}\text{C/W}$
		(Note 6) 62.5	
		(Note 7) 104	
Thermal Resistance, Junction to Leads (Note 8)	$R_{\theta JL}$	12.9	$^{\circ}\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^{\circ}\text{C}$

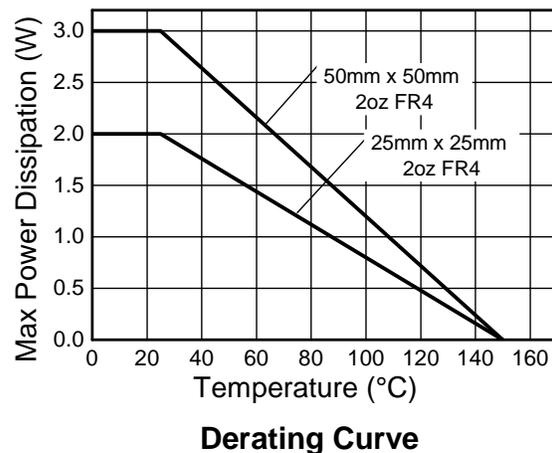
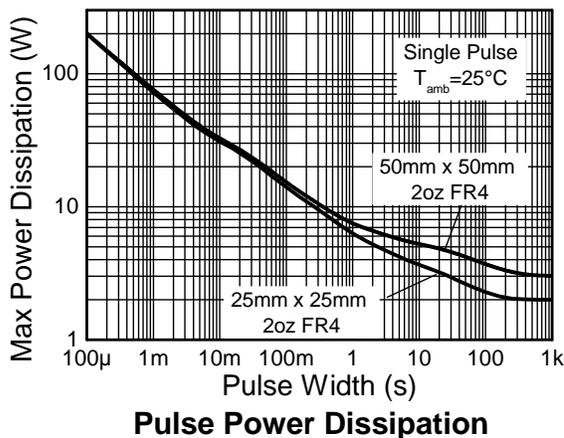
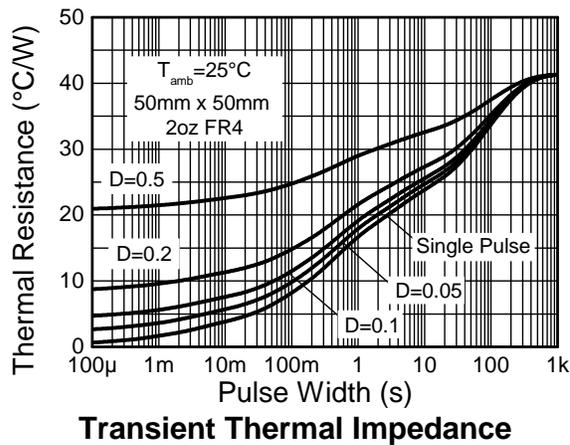
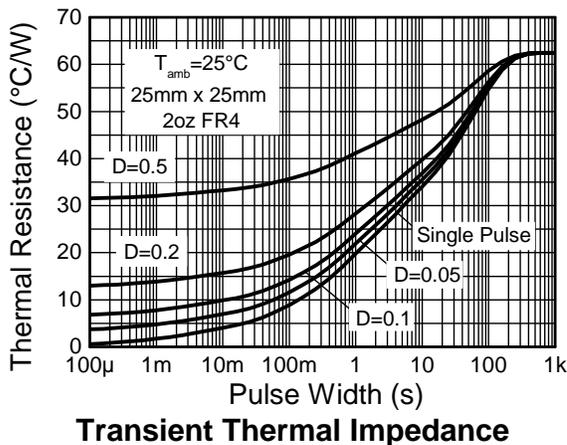
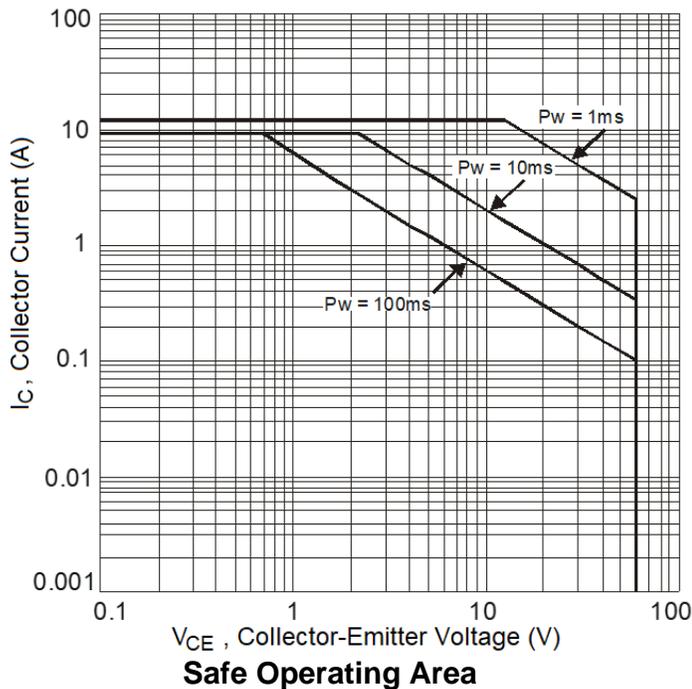
ESD Ratings

 (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	C

- Notes:
5. For a device mounted with the collector lead on 50mm x 50mm 2oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.
 6. Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
 7. Same as Note 5, except the device is mounted on minimum recommended pad (MRP) layout.
 8. Thermal resistance from junction to solder-point (at the end of the collector lead).
 9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information

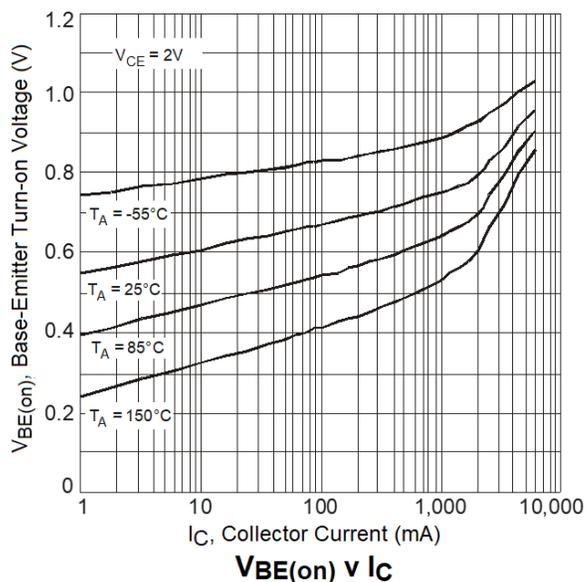
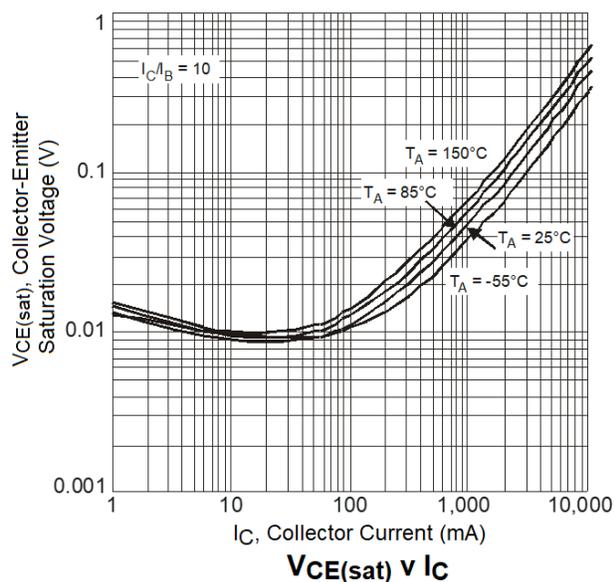
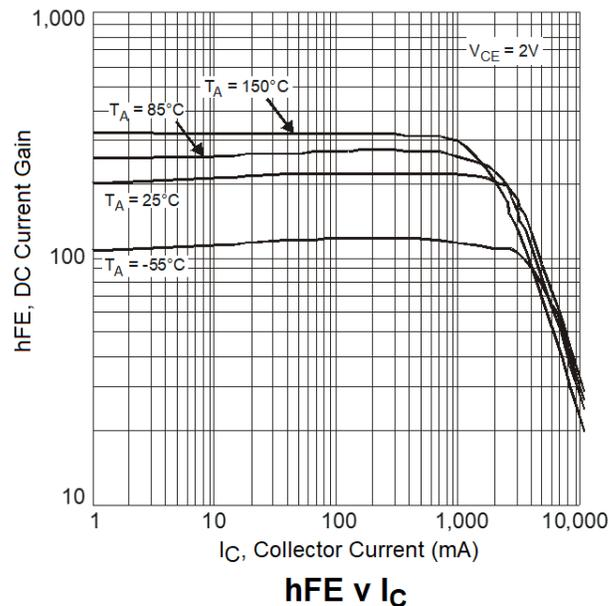
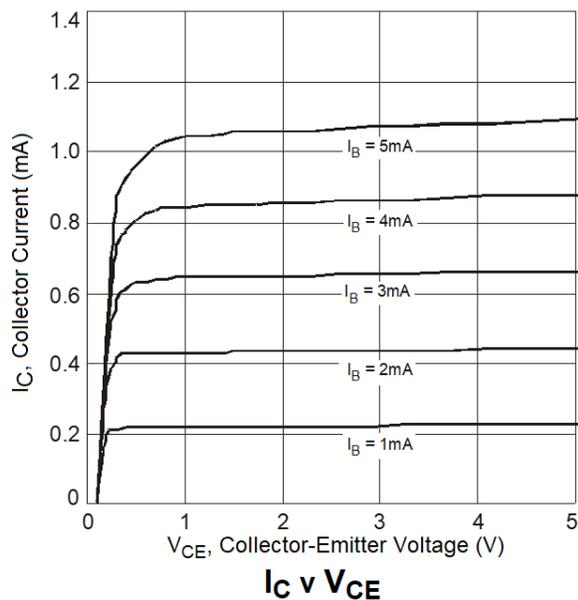


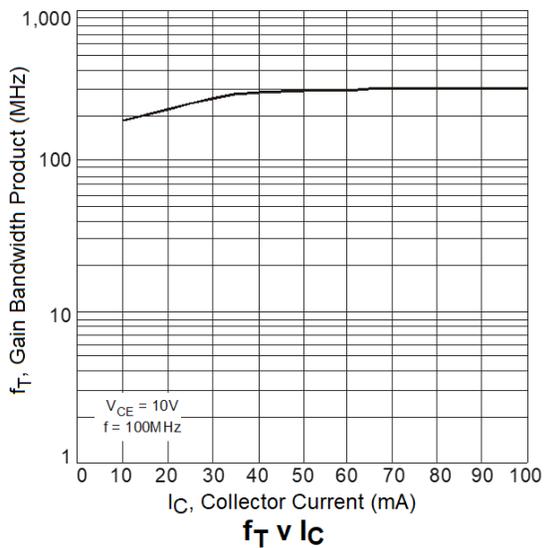
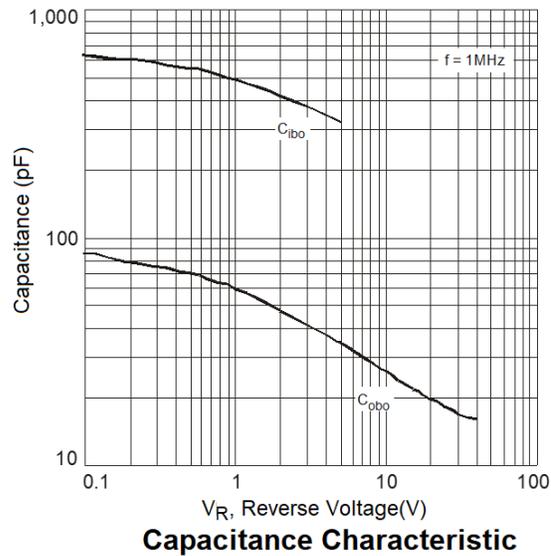
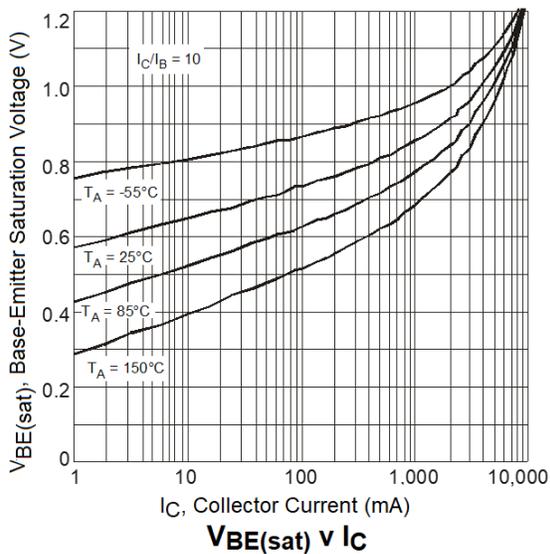
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV _{CBO}	100	—	—	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 10)	BV _{CEO}	60	—	—	V	I _C = 10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	6	—	—	V	I _E = 100μA
Collector-Base Cutoff Current	I _{CBO}	—	—	100	nA	V _{CB} = 40V, I _E = 0
		—	—	50	μA	V _{CB} = 40V, I _E = 0, T _J = +150°C
Emitter-Base Cutoff Current	I _{EBO}	—	—	100	nA	V _{EB} = 6V, I _C = 0
ON CHARACTERISTICS (Note 10)						
DC Current Gain	h _{FE}	150	—	—	—	V _{CE} = 2V, I _C = 0.5A
		120	—	360		V _{CE} = 2V, I _C = 1A
		100	—	—		V _{CE} = 2V, I _C = 2A
		50	—	—		V _{CE} = 2V, I _C = 6A
Collector-Emitter Saturation Voltage	V _{CE(sat)}	—	—	40	mV	I _C = 0.1A, I _B = 2.0mA
		—	—	60		I _C = 1A, I _B = 100mA
		—	80	100		I _C = 2A, I _B = 200mA
		—	—	220		I _C = 3A, I _B = 60mA
		—	—	300		I _C = 6A, I _B = 600mA
Equivalent On-Resistance	R _{CE(sat)}	—	40	50	mΩ	I _E = 2A, I _B = 200mA
Base-Emitter Saturation Voltage	V _{BE(sat)}	—	—	0.9	V	I _C = 1A, I _B = 100mA
Base-Emitter Turn-on Voltage	V _{BE(on)}	—	—	0.9	V	V _{CE} = 2V, I _C = 1A
SMALL SIGNAL CHARACTERISTICS						
Transition Frequency	f _T	100	—	—	MHz	V _{CE} = 10V, I _C = 100mA, f = 100MHz
Output Capacitance	C _{obo}	—	26	—	pF	V _{CB} = 10V, f = 1MHz
Input Capacitance	C _{ibo}	—	325	—	pF	V _{EB} = 5V, f = 1MHz
Turn-On Time	t _{on}	—	87	—	ns	V _{CC} = -30V, I _{CC} = 150mA, I _{B1} = -I _{B2} = 15mA
Delay Time	t _d	—	41	—	ns	
Rise Time	t _r	—	46	—	ns	
Turn-Off Time	t _{off}	—	294	—	ns	
Storage Time	t _s	—	250	—	ns	
Fall Time	t _f	—	44	—	ns	

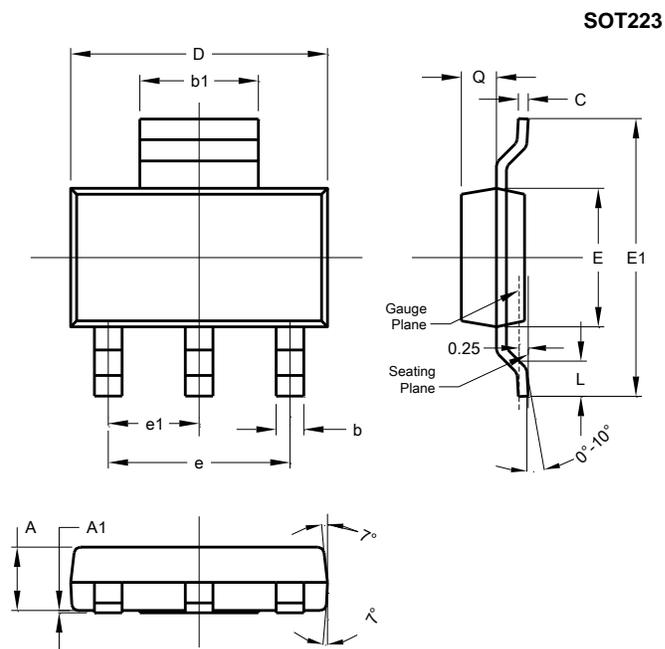
Note: 10. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



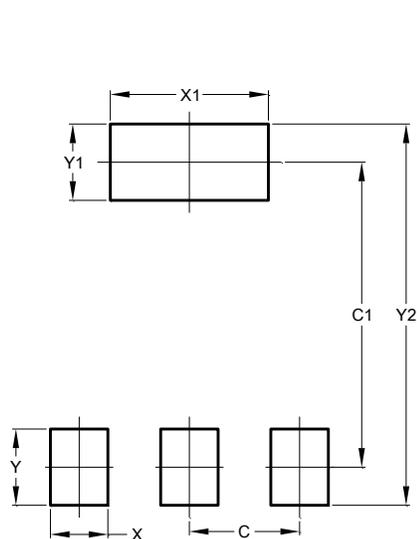


Package Outline Dimensions



SOT223			
Dim	Min	Max	Typ
A	1.55	1.65	1.60
A1	0.010	0.15	0.05
b	0.60	0.80	0.70
b1	2.90	3.10	3.00
C	0.20	0.30	0.25
D	6.45	6.55	6.50
E	3.45	3.55	3.50
E1	6.90	7.10	7.00
e	-	-	4.60
e1	-	-	2.30
L	0.85	1.05	0.95
Q	0.84	0.94	0.89
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00