



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Features

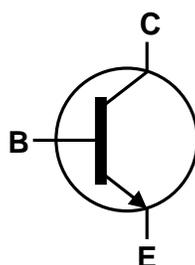
- $BV_{CEO} > 300V$
- $I_C = 200mA$ High Collector Current
- 350mW Power Dissipation
- Excellent h_{FE} Characteristics up to 30mA
- Complementary Part Number NK-FMMTA92

Mechanical Data

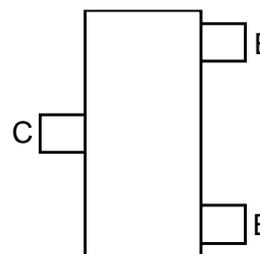
- Package: SOT23
- Package Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 
- Weight: 0.008 grams (Approximate)



Top View



Device Symbol



Top View
Pin-Out

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CB0}	300	V
Collector-Emitter Voltage	V _{CE0}	300	V
Emitter-Base Voltage	V _{EB0}	7	V
Collector Current	I _C	200	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation	P _D	(Note 5) 310	mW
		(Note 6) 350	
Thermal Resistance, Junction to Ambient	R _{θJA}	(Note 5) 403	°C/W
		(Note 6) 357	
Thermal Resistance, Junction to Leads	(Note 7) R _{θJL}	350	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

- Notes:
5. For the device mounted on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady state condition.
 6. Same as Note 5, except the device is mounted on 15mm x 15mm 1oz copper.
 7. Thermal resistance from junction to solder-point (at the end of the leads).
 8. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information

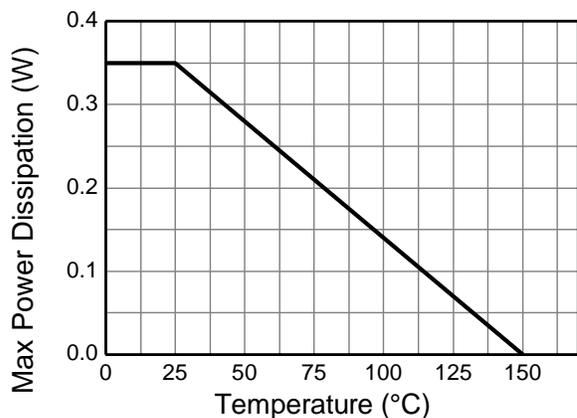


Figure 1. Derating Curve

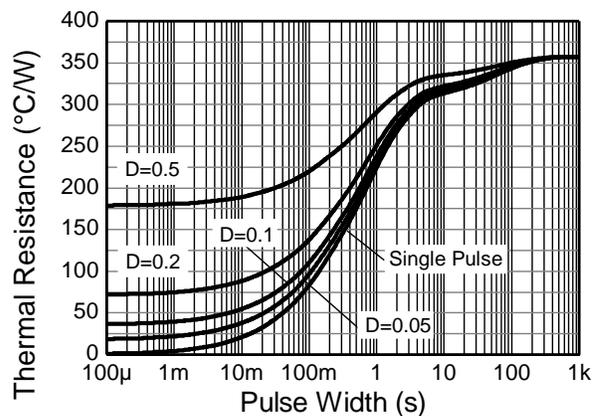


Figure 2. Transient Thermal Impedance

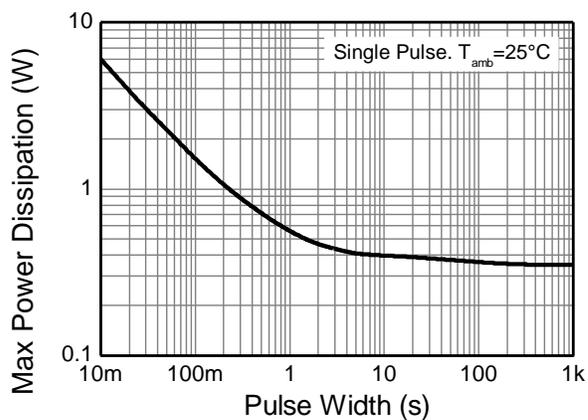


Figure 3. Pulse Power Dissipation

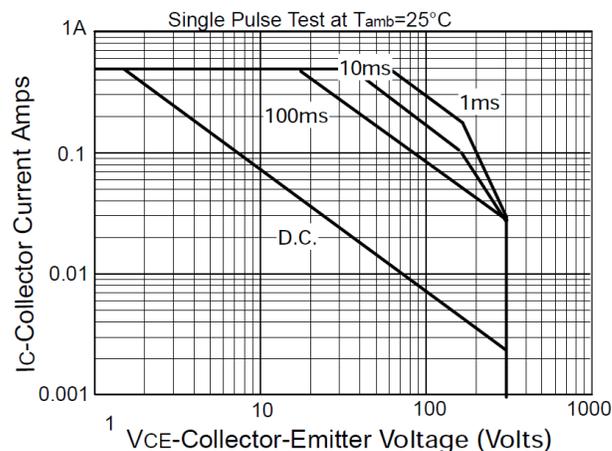
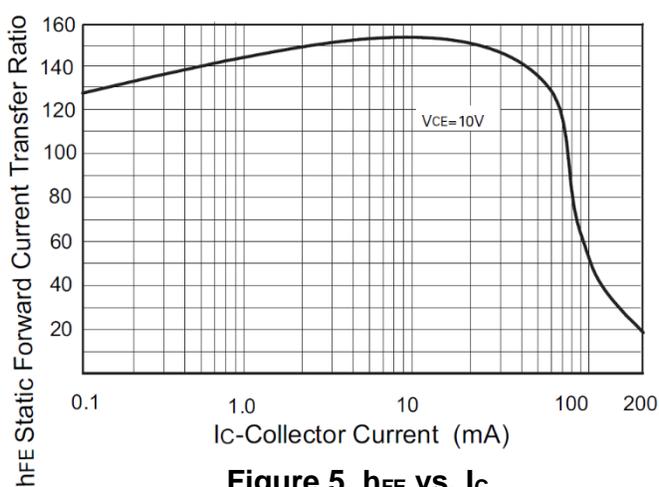
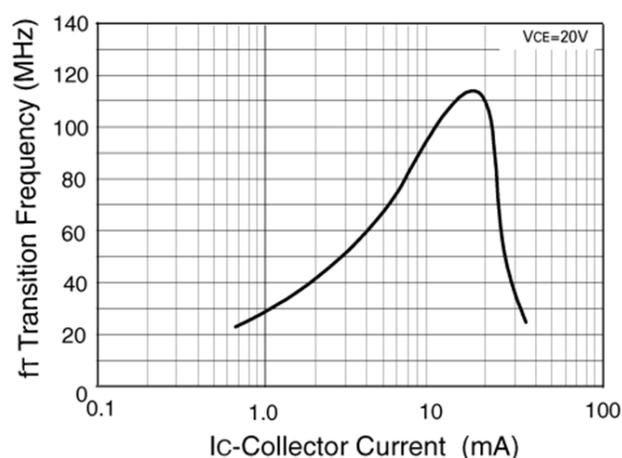
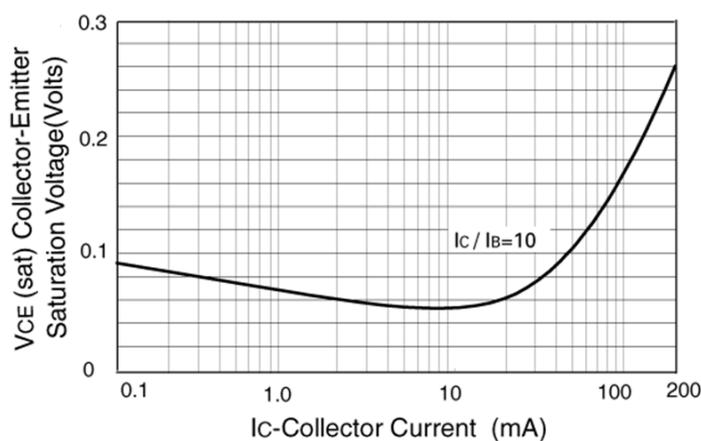


Figure 4. Safe Operating Area

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

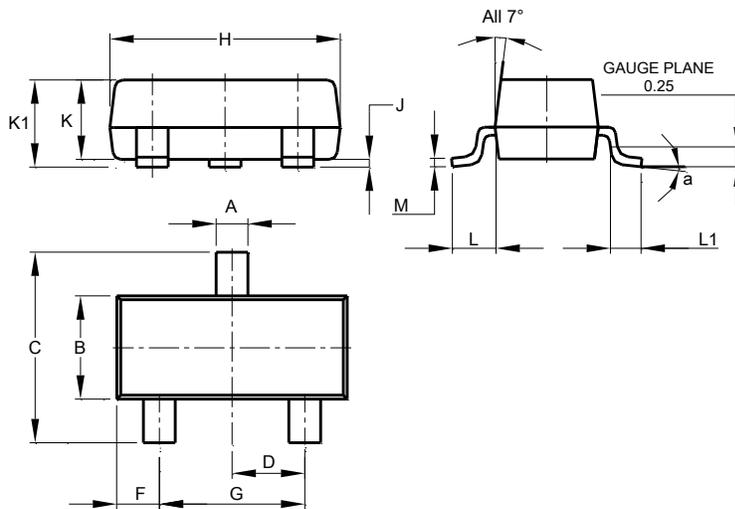
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV_{CBO}	300	—	—	V	$I_C = 100\mu\text{A}$
Collector-Emitter Breakdown Voltage (Note 9)	BV_{CEO}	300	—	—	V	$I_C = 1\text{mA}$
Emitter-Base Breakdown Voltage	BV_{EBO}	7	—	—	V	$I_E = 100\mu\text{A}$
Collector Cutoff Current	I_{CES}	—	—	100	nA	$V_{CE} = 200\text{V}$
Collector Cutoff Current	I_{CBO}	—	—	100	nA	$V_{CB} = 200\text{V}$
Emitter Cutoff Current	I_{EBO}	—	—	100	nA	$V_{EB} = 6\text{V}$
Static Forward Current Transfer Ratio (Note 9)	h_{FE}	25 40 40	—	—	—	$I_C = 1\text{mA}, V_{CE} = 10\text{V}$ $I_C = 10\text{mA}, V_{CE} = 10\text{V}$ $I_C = 30\text{mA}, V_{CE} = 10\text{V}$
Collector-Emitter Saturation Voltage (Note 9)	$V_{CE(sat)}$	—	—	500	mV	$I_C = 20\text{mA}, I_B = 2\text{mA}$
Base-Emitter Saturation Voltage (Note 9)	$V_{BE(sat)}$	—	—	900	mV	$I_C = 20\text{mA}, I_B = 2\text{mA}$
Output Capacitance	C_{obo}	—	—	6	pF	$V_{CB} = 20\text{V}, f = 1\text{MHz}$
Transition Frequency	f_T	50	—	—	MHz	$V_{CE} = 20\text{V}, I_C = 10\text{mA}$ $f = 20\text{MHz}$

 Note: 9. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$.

Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Figure 5. h_{FE} vs. I_C

Figure 6. f_T vs. I_C

Figure 7. $V_{CE(sat)}$ vs. I_C

Package Outline Dimensions

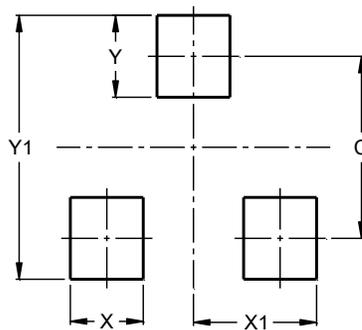
SOT23



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9