优尚

YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Features

- BV_{CEO} > 40V
- I_C = 5A High Continuous Collector Current
- I_{CM} = 20A Peak Pulse Current
- Low Saturation Voltage $V_{CE(sat)}$ < 120mV @ 1A
- R_{SAT} = 50mΩ @ 5A for a Low Equivalent On-Resistance
- h_{FE} Specified up to 10A for a High Gain Hold-Up
- Complementary PNP Type: NK-FZT1151A

Mechanical Data

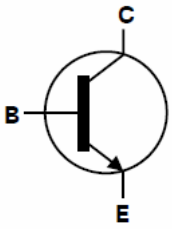
- Package: SOT223 (Type DN)
- Package Material: Molded Plastic. “Green” Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.112 grams (Approximate)

Applications

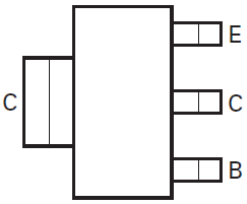
- Solenoid, relay, and actuator drivers
- DC modules
- Motor controls

SOT223 (Type DN)

Top View



Device Symbol



Top View
Pin-Out

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	150	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	7	V
Continuous Collector Current	I_C	5	A
Peak Pulse Current	I_{CM}	20	A
Base Current	I_B	1	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	P_D	3.0	W
	(Note 6)		2.0	
	(Note 7)		1.6	
	(Note 8)		1.2	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	41.7	°C/W
	(Note 6)		62.5	
	(Note 7)		78.1	
	(Note 8)		104	
Thermal Resistance Junction to Lead	(Note 9)	$R_{\theta JL}$	10.9	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

ESD Ratings (Note 10)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

Notes:
5. For a device mounted with the collector lead on 52mm x 52mm 2oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
6. Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
7. Same as Note 5, except the device is mounted on 25mm x 25mm 1oz copper.
8. Same as Note 5, except the device is mounted on minimum recommended pad layout.
9. Thermal resistance from junction to solder-point (at the end of the collector lead).
10. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information

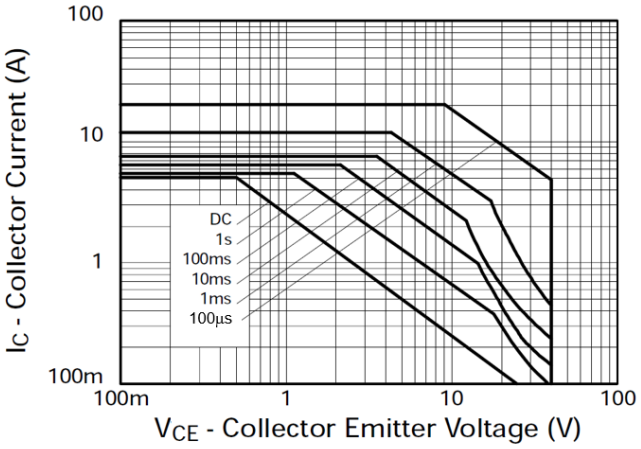


Fig.1 Safe Operating Area

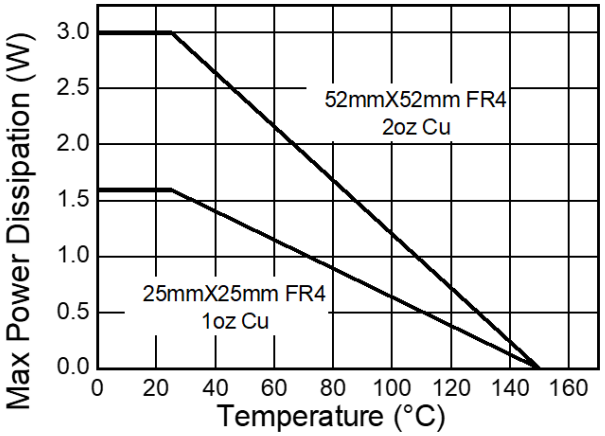


Fig.2 Derating Curve

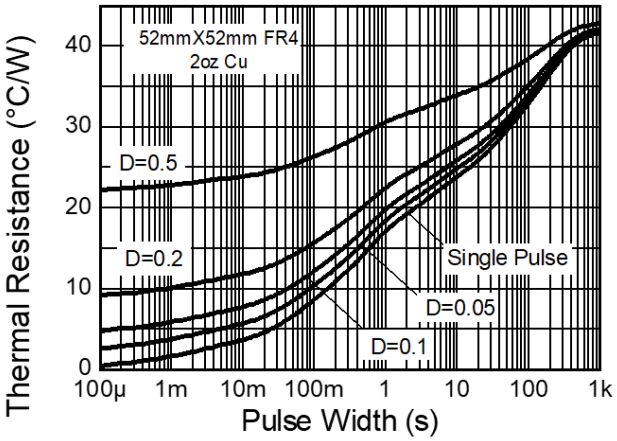


Fig.3 Transient Thermal Impedance

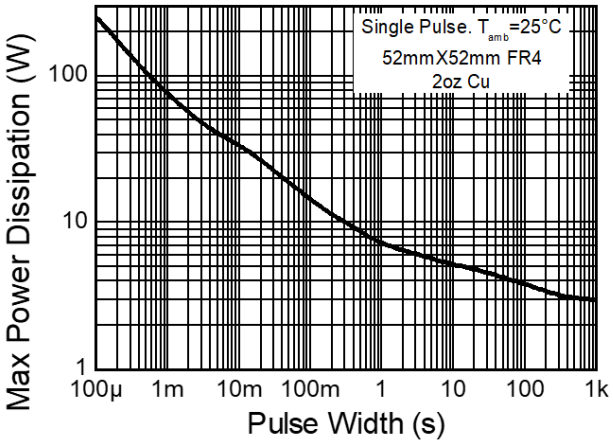


Fig.4 Pulse Power Dissipation

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV_{CBO}	150	190	—	V	I_C = 100μA
Collector-Emitter Breakdown Voltage	BV_{CES}	150	190	—	V	I_C = 100μA
Collector-Emitter Breakdown Voltage	BV_{CEV}	150	190	—	V	I_C = 100μA, V_{EB} = 1V
Collector-Emitter Breakdown Voltage (Note 11)	BV_{CEO}	40	60	—	V	I_C = 10mA
Emitter-Base Breakdown Voltage	BV_{EBO}	7	8.1	—	V	I_E = 100μA
Collector Cut-Off Current	I_{CBO}	— —	<1 —	10 0.5	nA μA	V_{CB} = 120V V_{CB} = 120V, T_A = +100°C
Collector Cut-Off Current	I_{CES}	—	<1	10	nA	V_{CB} = 120V
Emitter Cut-Off Current	I_{EBO}	—	<1	10	nA	V_{EB} = 6V
Collector-Emitter Saturation Voltage (Note 11)	$V_{CE(sat)}$	—	17	25	mV	I_C = 200mA, I_B = 10mA
		—	85	120		I_C = 1A, I_B = 10mA
		—	140	180		I_C = 2A, I_B = 20mA
		—	250	340		I_C = 5A, I_B = 100mA
Base-Emitter Saturation Voltage (Note 11)	$V_{BE(sat)}$	—	980	1100	mV	I_C = 5A, I_B = 100mA
Base-Emitter Turn-On Voltage (Note 11)	$V_{BE(on)}$	—	915	1000	mV	I_C = 5A, V_{CE} = 2V
DC Current Gain (Note 11)	h_{FE}	290	440	—	—	I_C = 10mA, V_{CE} = 2V
		270	450	1200		I_C = 1A, V_{CE} = 2V
		130	220	—		I_C = 5A, V_{CE} = 2V
		40	55	—		I_C = 10A, V_{CE} = 2V
Output Capacitance	C_{obo}	—	27	40	pF	V_{CB} = 10V, f = 1MHz
Current Gain-Bandwidth Product	f_T	—	155	—	MHz	V_{CE} = 10V, I_C = 50mA, f = 100MHz
Switching Times	t_{on}	—	220	—	ns	I_C = 3A, V_{CC} = 10V, I_{B1} = -I_{B2} = 30mA
	t_{off}	—	540	—		

Note: 11. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%

Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

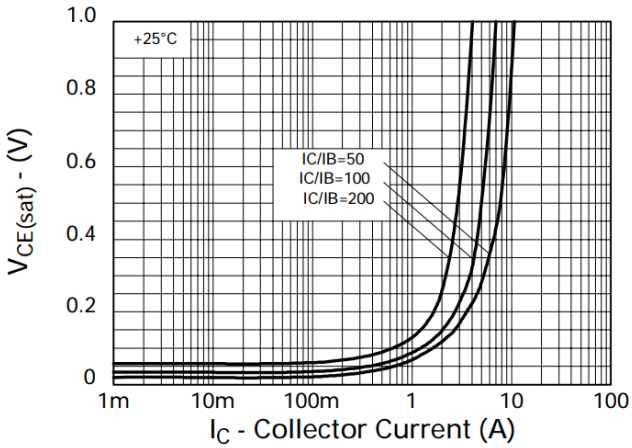


Fig.5 $V_{CE(sat)}$ v I_C

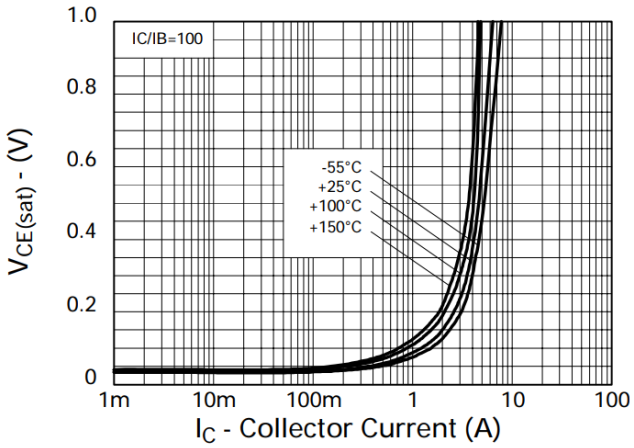


Fig.6 $V_{CE(sat)}$ v I_C

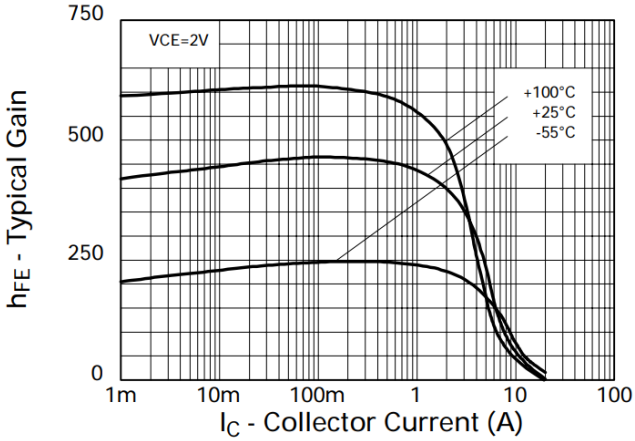


Fig.7 h_{FE} v I_C

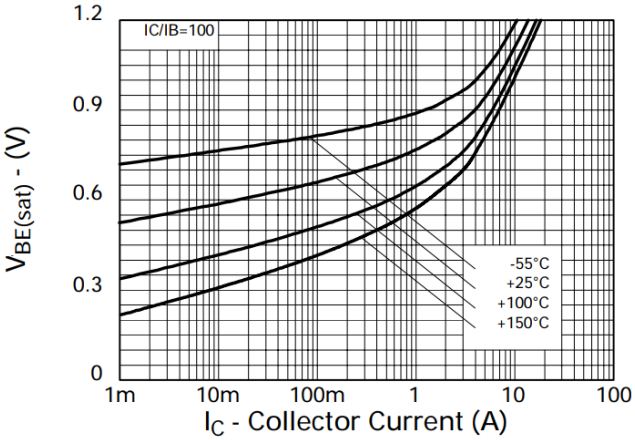


Fig.8 $V_{BE(sat)}$ v I_C

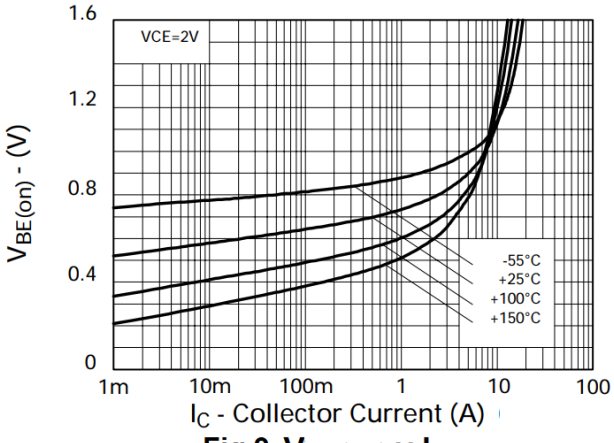
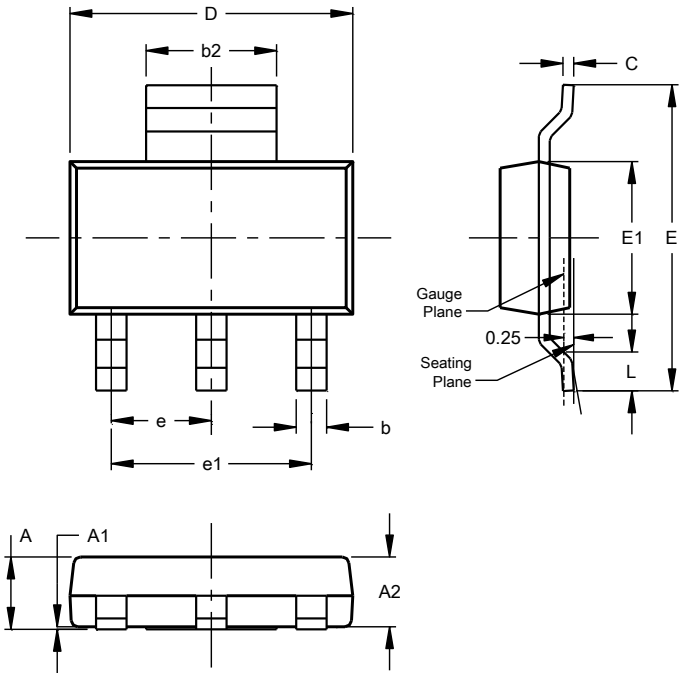


Fig.9 $V_{BE(on)}$ v I_C

Package Outline Dimensions

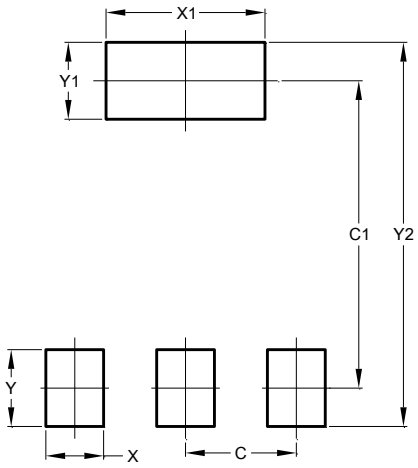
SOT223 (Type DN)



SOT223 (Type DN)			
Dim	**Min**	**Max**	**Typ**
A	--	1.70	--
A1	0.01	0.15	--
A2	1.50	1.68	1.60
b	0.60	0.80	0.70
b2	2.90	3.10	--
c	0.20	0.32	--
D	6.30	6.70	--
E	6.70	7.30	--
E1	3.30	3.70	--
e	--	--	2.30
e1	--	--	4.60
L	0.85	--	--
All Dimensions in mm			

Suggested Pad Layout

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00