



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

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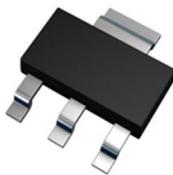
## Features

- $BV_{CEO} > -100V$
- $I_C = -1A$  High Continuous Current
- $I_{CM} = -2A$  Peak Pulse Current
- Low Saturation Voltage
- Complementary NPN Type: NK-FZT493

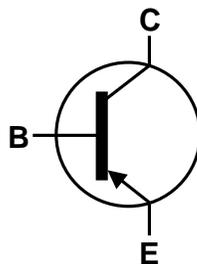
## Mechanical Data

- Package: SOT223 (Type DN)
- Package material: Molded Plastic. "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 
- Weight: 0.112 grams (Approximate)

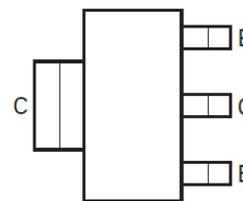
SOT223 (Type DN)



Top View



Device Symbol



Top View  
Pin-Out

### Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-120	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-100	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	I <sub>C</sub>	-1	A
Base Current	I <sub>B</sub>	-200	mA
Peak Pulse Current	I <sub>CM</sub>	-2	A

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

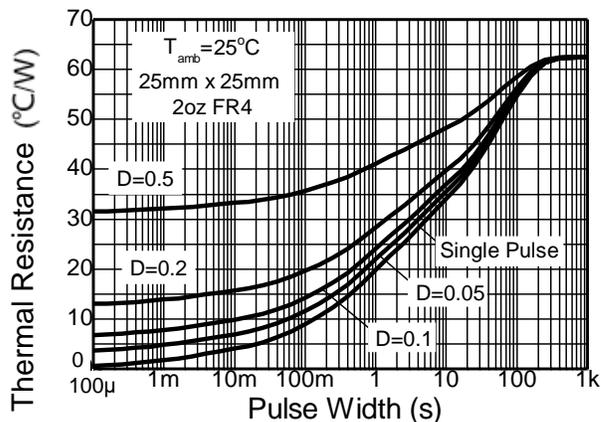
Characteristic	Symbol	Value	Unit
Power Dissipation	P <sub>D</sub>	3.0	W
		2.0	
		1.6	
		1.2	
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	41.7	°C/W
		62.5	
		78.1	
		104	
Thermal Resistance Junction to Lead	R <sub>θJL</sub>	19.4	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

### ESD Ratings (Note 10)

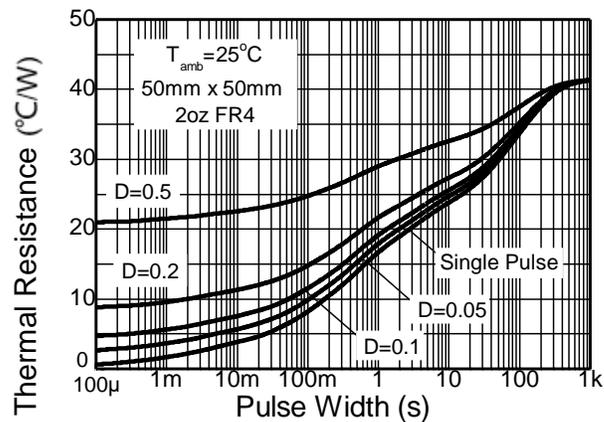
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

- Notes:
- For a device mounted with the collector lead on 50mm x 50mm 2oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
  - Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
  - Same as Note 5, except the device is mounted on 25mm x 25mm 1oz copper.
  - Same as Note 5, except the device is mounted on minimum recommended pad layout.
  - Thermal resistance from junction to solder-point (at the end of the collector lead).
  - Refer to JEDEC specification JESD22-A114 and JESD22-A115.

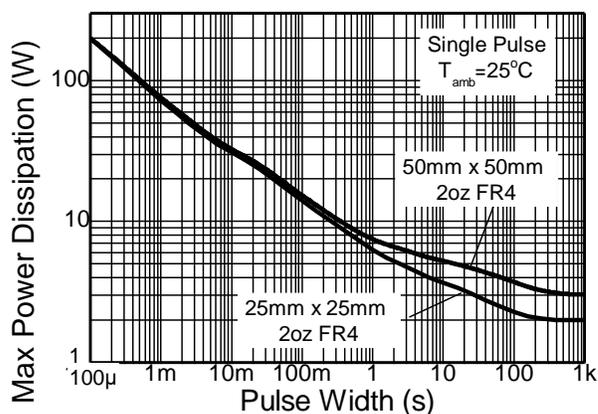
## Thermal Characteristics and Derating Characteristics



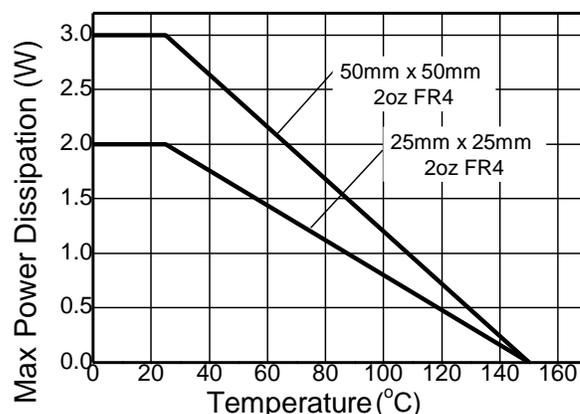
**Transient Thermal Impedance**



**Transient Thermal Impedance**



**Pulse Power Dissipation**



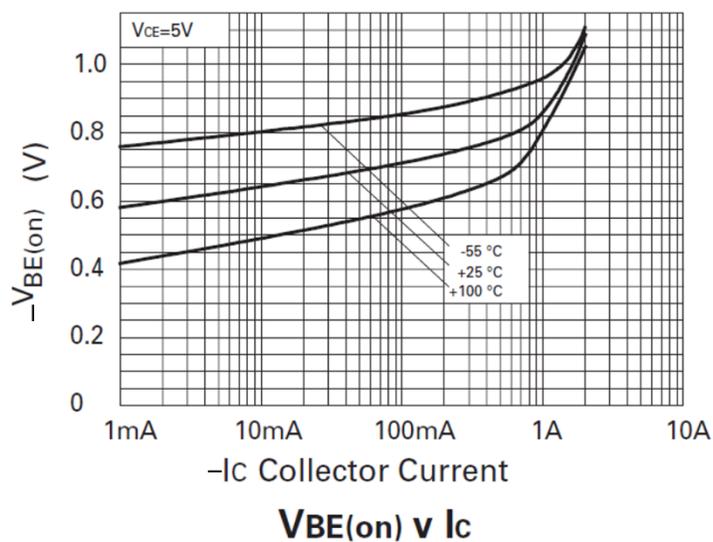
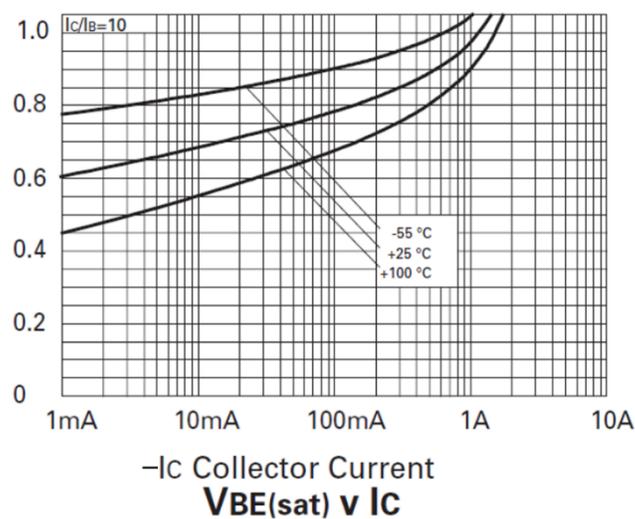
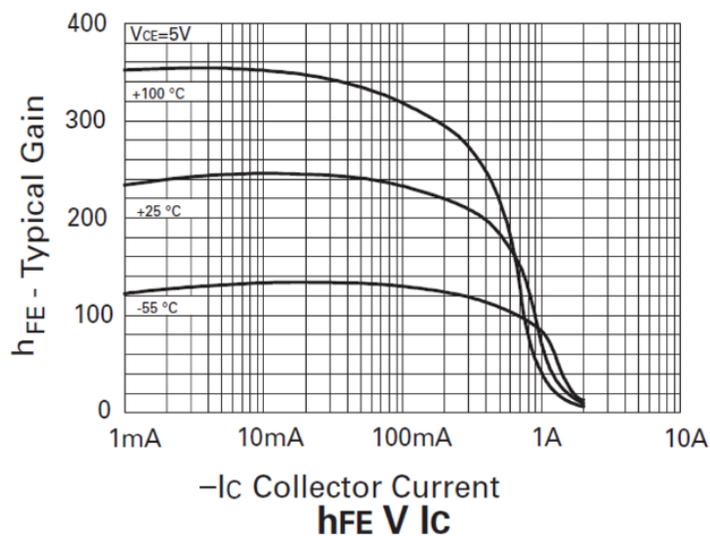
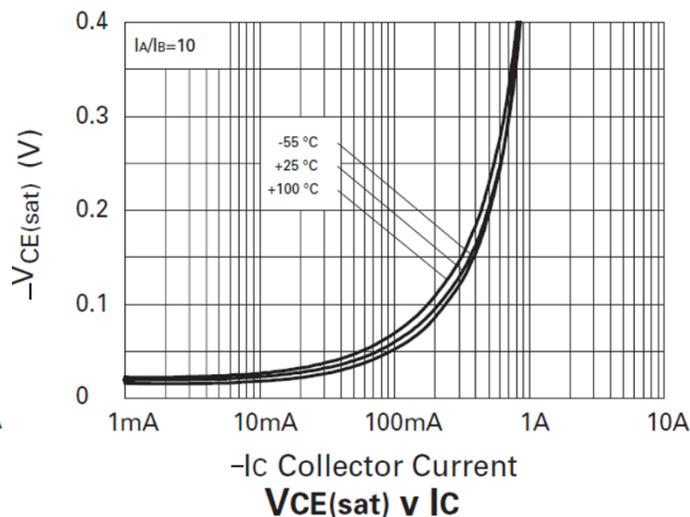
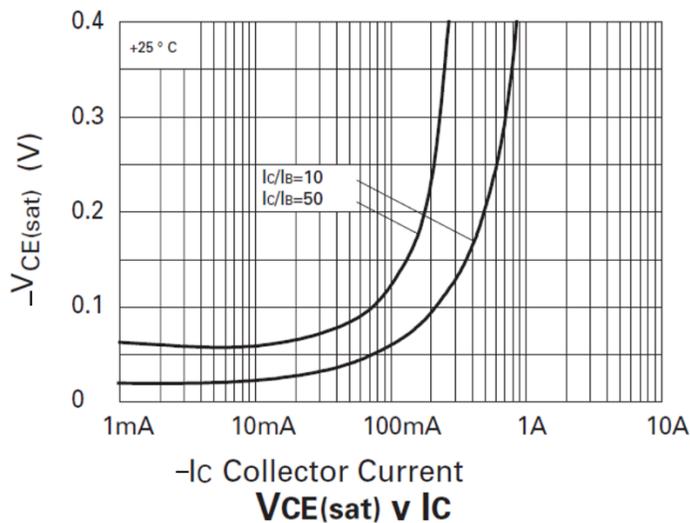
**Derating Curve**

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-120	—	—	V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage (Note 11)	BV <sub>CEO</sub>	-100	—	—	V	I <sub>C</sub> = -10mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5	—	—	V	I <sub>E</sub> = -100μA
Collector Cut-Off Current	I <sub>CBO</sub>	—	—	-100	nA	V <sub>CB</sub> = -100V
Collector Cut-Off Current	I <sub>CES</sub>	—	—	-100	nA	V <sub>CE</sub> = -100V
Emitter Cut-Off Current	I <sub>EBO</sub>	—	—	-100	nA	V <sub>EB</sub> = -4V
Collector-Emitter Saturation Voltage (Note 11)	V <sub>CE(sat)</sub>	—	—	-0.2 -0.3	v	I <sub>C</sub> = -250mA, I <sub>B</sub> = -25mA I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA
Base-Emitter Saturation Voltage (Note 11)	V <sub>BE(sat)</sub>	—	—	-1.1	V	I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA
Base-Emitter Turn-On Voltage (Note 11)	V <sub>BE(on)</sub>	—	—	-1.0	V	I <sub>C</sub> = -500mA, V <sub>CE</sub> = -5V
DC Current Gain (Note 11)	h <sub>FE</sub>	100 100 100 50	— — — —	— — 300 —	—	I <sub>C</sub> = -1mA, V <sub>CE</sub> = -5V I <sub>C</sub> = -250mA, V <sub>CE</sub> = -5V I <sub>C</sub> = -500mA, V <sub>CE</sub> = -5V I <sub>C</sub> = -1A, V <sub>CE</sub> = -5V
Current Gain-Bandwidth Product	f <sub>T</sub>	50	—	—	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -50mA f = 100MHz
Output Capacitance	C <sub>obo</sub>	—	—	5	pF	V <sub>CB</sub> = -10V, f = 1MHz

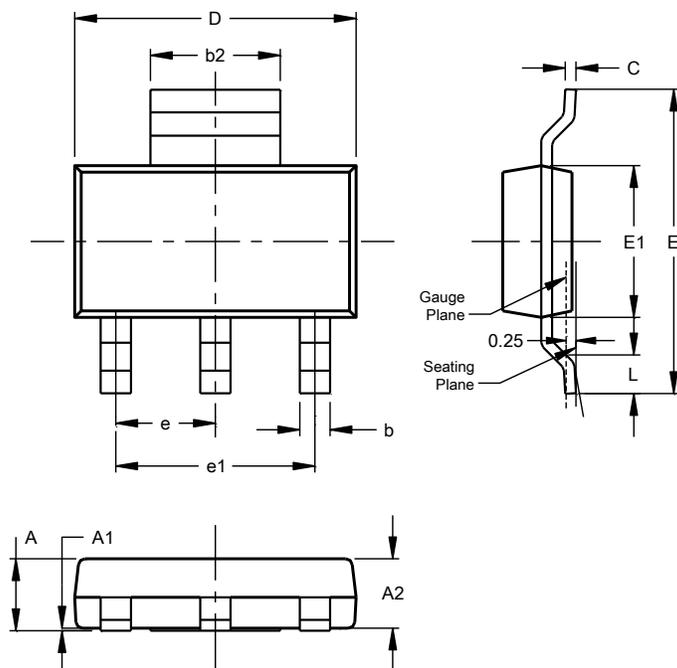
Note: 11. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)



## Package Outline Dimensions

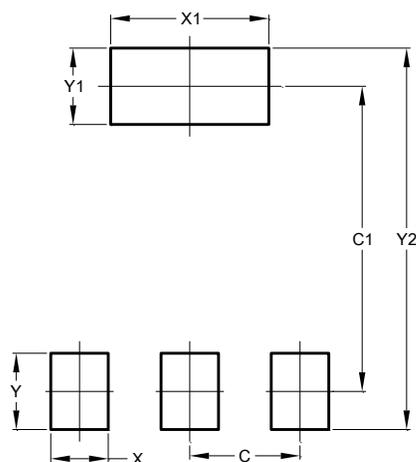
SOT223 (Type DN)



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Dim	Min	Max	Typ
A	--	1.70	--
A1	0.01	0.15	--
A2	1.50	1.68	1.60
b	0.60	0.80	0.70
b2	2.90	3.10	--
c	0.20	0.32	--
D	6.30	6.70	--
E	6.70	7.30	--
E1	3.30	3.70	--
e	--	--	2.30
e1	--	--	4.60
L	0.85	--	--
All Dimensions in mm			

## Suggested Pad Layout

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00