优尚

YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

0755-83047638
ysbdt@szyoushang.cn
www.szyoushang.cn

企业微信二维码

企业QQ二维码

Features

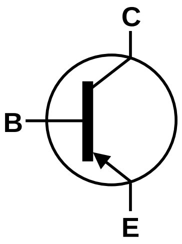
- Epitaxial Planar Die Construction
- Ideal for Medium Power Amplification and Switching
- Complementary NPN Type: NK-MMBT3904

Mechanical Data

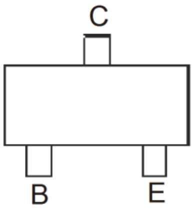
- Case: SOT23
- Case Material: Molded Plastic, “Green” Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 ⓔ3
- Weight: 0.008 grams (Approximate)

SOT23

Top View



Device Symbol



Top View
Pin-Out

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-40	V
Collector-Emitter Voltage	V_{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-6.0	V
Collector Current	I_C	-200	mA
Peak Collector Current	I_{CM}	-200	mA
Peak Base Current	I_{BM}	-100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

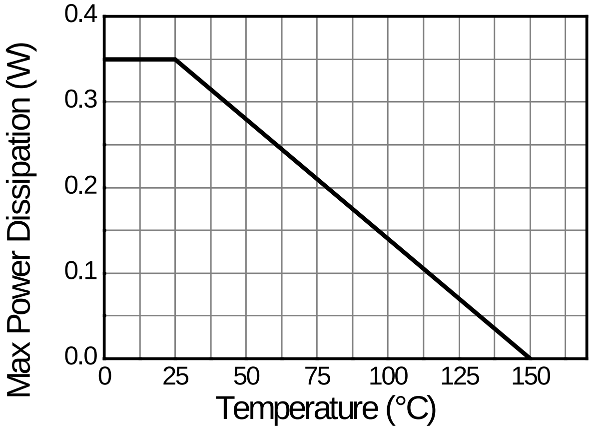
Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 6)	P_D	310	mW
	(Note 7)		350	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\Theta JA}$	403	°C/W
	(Note 7)		357	
Thermal Resistance, Junction to Leads	(Note 8)	$R_{\Theta JL}$	350	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

ESD Ratings (Note 9)

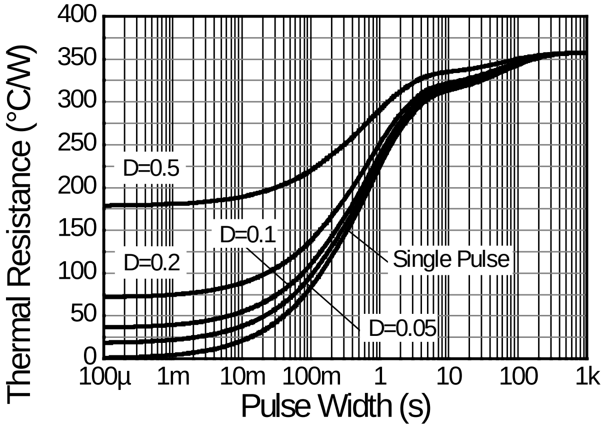
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge—Human Body Model	ESD HBM	4000	V	3A
Electrostatic Discharge—Machine Model	ESD MM	400	V	C

Notes:
6. For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR4 PCB; the device is measured under still air conditions while operating in a steady-state.
7. Same as Note 6 except the device is mounted on 15 mm × 15mm 1oz copper.
8. Thermal resistance from junction to solder-point (at the end of the leads).
9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

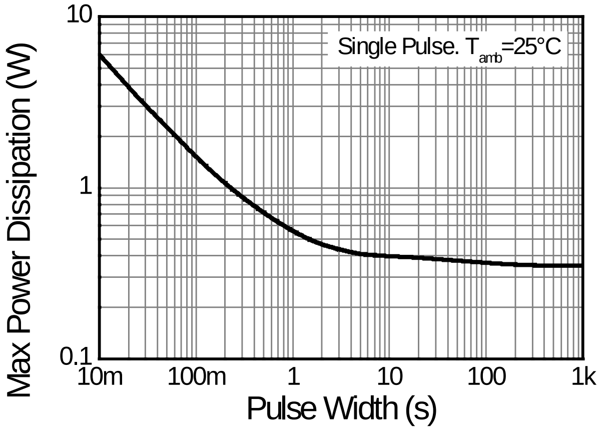
Thermal Characteristics and Derating Information



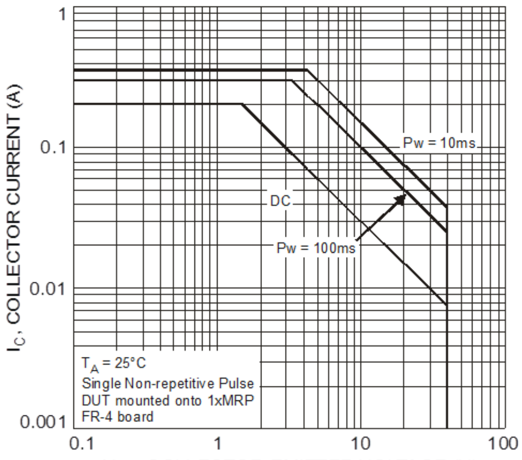
Derating Curve



Transient Thermal Impedance



Pulse Power Dissipation



Typical Collector Current
vs. Collector-Emitter Voltage

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS					
Collector-Base Breakdown Voltage	BV_{CBO}	-40	—	V	I_C = -100µA, I_E = 0
Collector-Emitter Breakdown Voltage (Note 10)	BV_{CEO}	-40	—	V	I_C = -10mA, I_B = 0
Emitter-Base Breakdown Voltage	BV_{EBO}	-6.0	—	V	I_E = -100µA, I_C = 0
Collector Cutoff Current	I_{CEV}	—	-50	nA	V_{CE} = -30V, V_{BE} = 3.0V
		—	-50	nA	V_{CE} = -30V, V_{BE} = -0.25V
Emitter-Base Cutoff Current	I_{EBO}	—	-50	nA	V_{EB} = -5V
ON CHARACTERISTICS (Note 10)					
DC Current Gain	h_{FE}	60	—	—	I_C = -100µA, V_{CE} = -1.0V
		80	—		I_C = -1.0mA, V_{CE} = -1.0V
		100	300		I_C = -10mA, V_{CE} = -1.0V
		60	—		I_C = -50mA, V_{CE} = -1.0V
		30	—		I_C = -100mA, V_{CE} = -1.0V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	-0.25	V	I_C = -10mA, I_B = -1.0mA
			-0.40		I_C = -50mA, I_B = -5.0mA
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	-0.65	-0.85	V	I_C = -10mA, I_B = -1.0mA
		—	-0.95		I_C = -50mA, I_B = -5.0mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C_{obo}	—	4.5	pF	V_{CB} = -5.0V, f = 1.0MHz, I_E = 0
Input Capacitance	C_{ibo}	—	10	pF	V_{EB} = -0.5V, f = 1.0MHz, I_C = 0
Input Impedance	h_{ie}	2.0	12	kΩ	V_{CE} = 10V, I_C = 1.0mA, f = 1.0kHz
Voltage Feedback Ratio	h_{re}	0.1	10	$\times 10^{-4}$	
Small Signal Current Gain	h_{fe}	100	400	—	
Output Admittance	h_{oe}	3.0	60	µS	
Current Gain-Bandwidth Product	f_T	250	—	MHz	V_{CE} = -20V, I_C = -10mA, f = 100MHz
Noise Figure	NF	—	4.0	dB	V_{CE} = -5.0V, I_C = -100µA, R_S = 1.0kΩ, f = 1.0kHz
SWITCHING CHARACTERISTICS					
Delay Time	t_d	—	35	ns	V_{CC} = -3.0V, I_C = -10mA, $V_{BE(off)}$ = 0.5V, I_{B1} = -1.0mA
Rise Time	t_r	—	35	ns	
Storage Time	t_s	—	225	ns	V_{CC} = -3.0V, I_C = -10mA, I_{B1} = I_{B2} = -1.0mA
Fall Time	t_f	—	75	ns	

Note: 10. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤ 2%.

Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

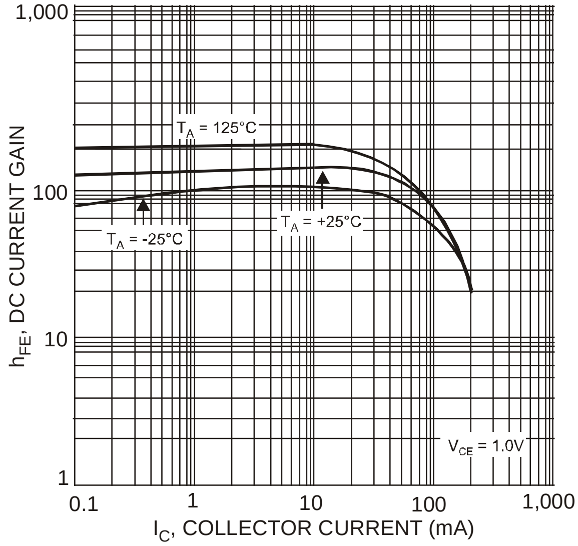


Figure 1 Typical DC Current Gain
vs. Collector Current

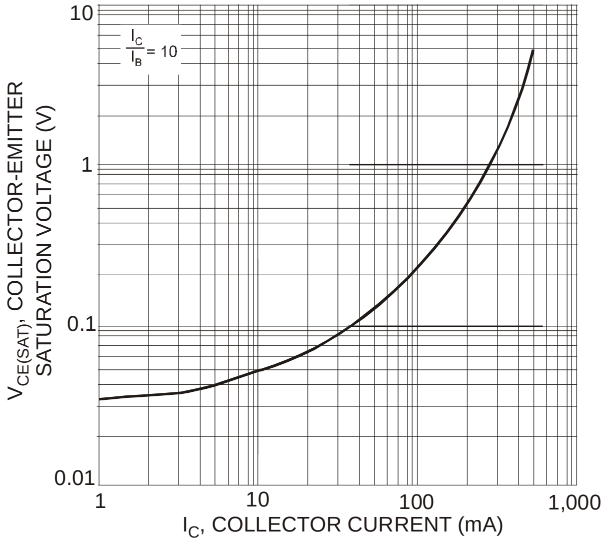


Figure 2 Typical Collector-Emitter Saturation Voltage
vs. Collector Current

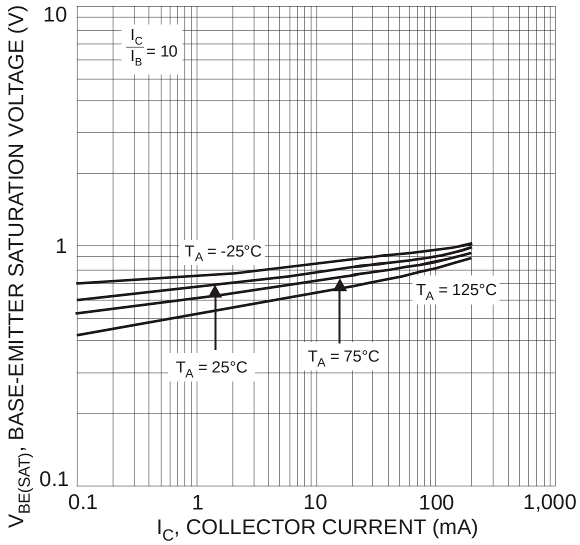


Figure 3 Typical Base-Emitter Saturation Voltage
vs. Collector Current

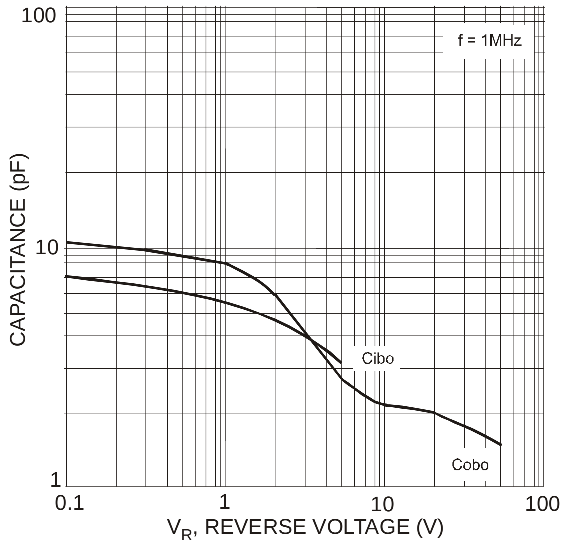
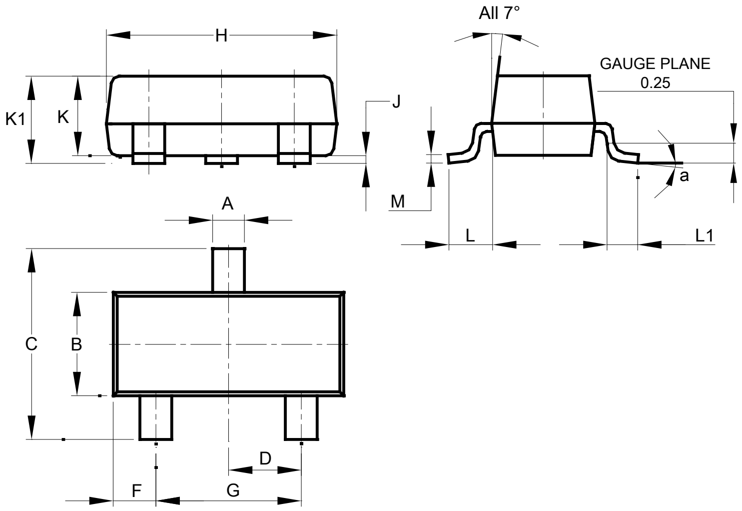


Figure 4 Typical Capacitance Characteristics

Package Outline Dimensions

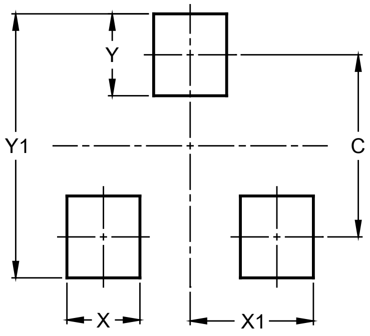
SOT23



SOT23			
Dim	**Min**	**Max**	**Typ**
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9