



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

0755-83047638

ysbdt@szyoushang.cn

www.szyoushang.cn



企业微信二维码



企业QQ二维码

## Description

This Bipolar Junction Transistor (BJT) is designed to meet the stringent requirements of Automotive Applications.

## Features

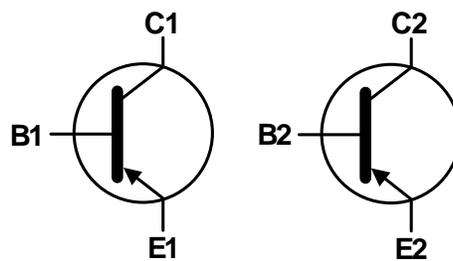
- Epitaxial Planar Die Construction
- Complementary NPN Type Available (NK-MMDT5551Q)
- Ideal for Medium Power Amplification and Switching
- Ultra-Small Surface Mount Package

## Mechanical Data

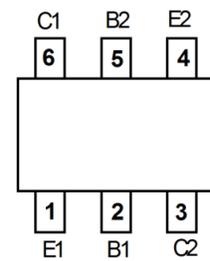
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Finish. Solderable per MIL-STD-202, Method 208 
- Weight: 0.006 grams (Approximate)



Top View



Device Symbol



Top View  
Pin-Out

**Absolute Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	-160	V
Collector-Emitter Voltage	$V_{CEO}$	-150	V
Emitter-Base Voltage	$V_{EBO}$	-6	V
Continuous Collector Current	$I_C$	-200	mA

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

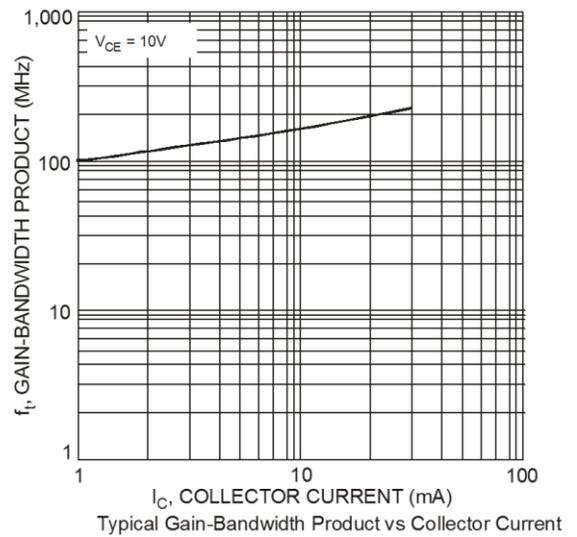
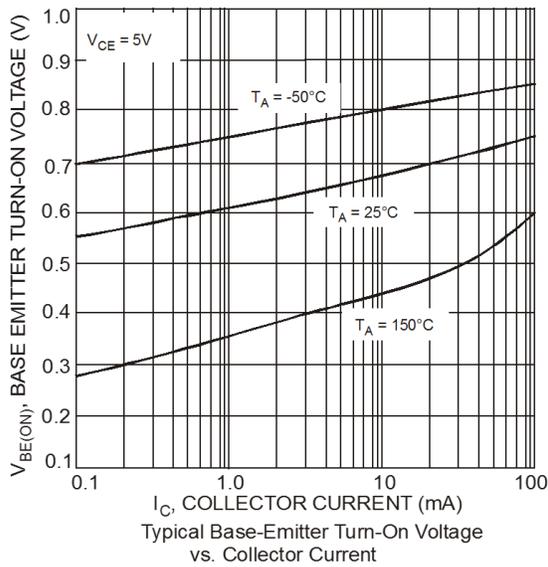
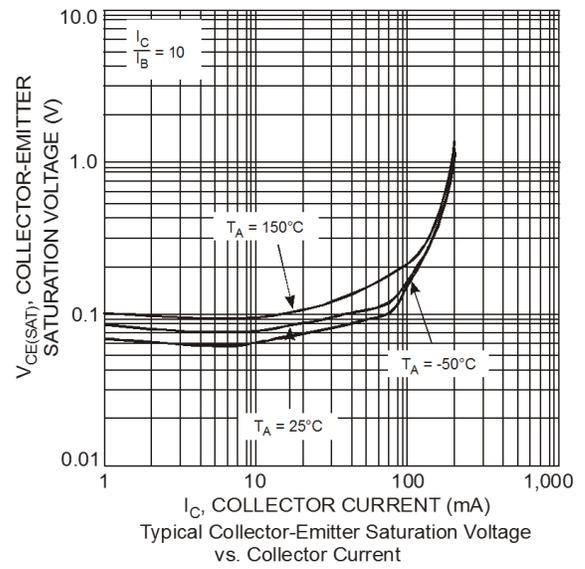
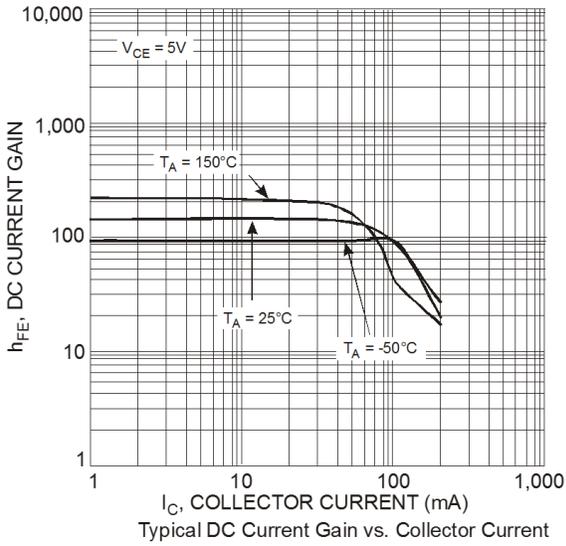
Characteristic	Symbol	Value	Unit
Power Dissipation	$P_D$	200	mW
		320	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	625	$^\circ\text{C/W}$
		390	
Thermal Resistance, Junction to Case	$R_{\theta JC}$	140	$^\circ\text{C}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b>						
Collector-Base Breakdown Voltage	$BV_{CBO}$	-160	—	—	V	$I_C = -100\mu\text{A}, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 10)	$BV_{CEO}$	-150	—	—	V	$I_C = -1\text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$BV_{EBO}$	-6	—	—	V	$I_E = -100\mu\text{A}, I_C = 0$
Collector-Base Cutoff Current	$I_{CBO}$	—	—	-50	nA	$V_{CB} = -120\text{V}, I_E = 0$
		—	—	-50	$\mu\text{A}$	$V_{CB} = -120\text{V}, I_E = 0, T_A = +100^\circ\text{C}$
Base-Emitter Cutoff Current	$I_{EBO}$	—	—	-50	nA	$V_{EB} = -5\text{V}, I_C = 0$
<b>ON CHARACTERISTICS (Note 10)</b>						
DC Current Gain	$h_{FE}$	50	—	—	—	$I_C = -1.0\text{mA}, V_{CE} = -5.0\text{V}$
		60	—	240	—	$I_C = -10\text{mA}, V_{CE} = -5.0\text{V}$
		50	—	—	—	$I_C = -50\text{mA}, V_{CE} = -5.0\text{V}$
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	—	—	-0.2	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$
		—	—	-0.5	V	$I_C = -50\text{mA}, I_B = -5.0\text{mA}$
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	—	—	-1.0	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$
		—	—	-1.0	V	$I_C = -50\text{mA}, I_B = -5.0\text{mA}$
<b>SMALL SIGNAL CHARACTERISTICS</b>						
Output Capacitance	$C_{obo}$	—	—	6.0	pF	$V_{CB} = -10\text{V}, f = 1.0\text{MHz}, I_E = 0$
Small Signal Current Gain	$h_{fe}$	40	—	260	—	$I_C = -1\text{mA}, V_{CE} = -10\text{V}, f = 1.0\text{MHz}$
Current Gain-Bandwidth Product	$f_T$	100	—	300	MHz	$I_C = -10\text{mA}, V_{CE} = -10\text{V}, f = 100\text{MHz}$
Noise Figure	NF	—	—	8.0	dB	$V_{CE} = -5.0\text{V}, I_C = -200\mu\text{A}, R_S = 10\Omega, f = 1.0\text{kHz}$

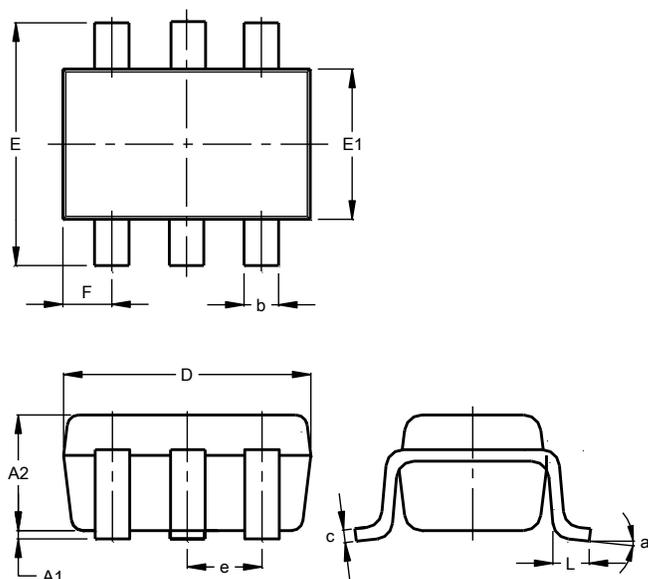
- Notes:
6. For a device mounted on minimum recommended pad layout 1oz weight copper that is on a single-sided FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.
  7. Same as Note 6, except the device is mounted on 25mm x 25mm 2oz copper.
  8. Maximum combined dissipation.
  9. Thermal resistance from junction to the top of package.
  10. Measured under pulsed conditions. Pulse width  $\leq 300\mu\text{s}$ . Duty cycle  $\leq 2\%$ .

**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)



### Package Outline Dimensions

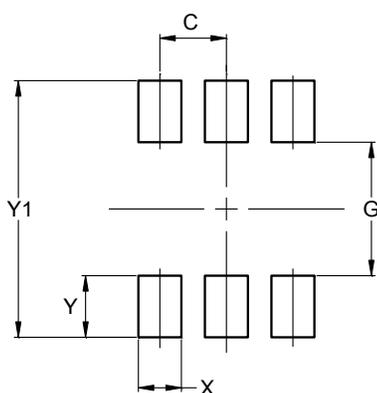
SOT363



SOT363			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.10	0.30	0.25
c	0.10	0.22	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
F	0.40	0.45	0.425
L	0.25	0.40	0.30
a	0°	8°	--
<b>All Dimensions in mm</b>			

### Suggested Pad Layout

SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.