



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

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## Features

- $BV_{CEO} > -40V$
- $I_C = -200mA$  Collector Current
- Epitaxial Planar Die Construction
- Ultra-Small Surface Mount Package
- Complementary NPN Type: NK-MMST3904

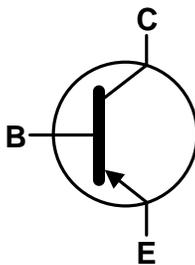
## Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208  $\text{E3}$
- Weight: 0.006 grams (Approximate)

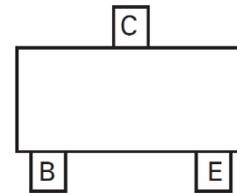
SOT323



Top View



Device Symbol



Pin-out Top View

**Absolute Maximum Ratings** (@ $T_A = +25^{\circ}\text{C}$  unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CB0}$	-40	V
Collector-Emitter Voltage	$V_{CEO}$	-40	V
Emitter-Base Voltage	$V_{EBO}$	-5	V
Collector Current	$I_C$	-200	mA

**Thermal Characteristics** (@ $T_A = +25^{\circ}\text{C}$  unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	$P_d$	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	625	$^{\circ}\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_j, T_{STG}$	-55 to +150	$^{\circ}\text{C}$

**ESD Ratings** (Note 6)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

- Notes: 5. For a device mounted with the collector lead on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.  
 6. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

**Thermal Characteristics and Derating Information**

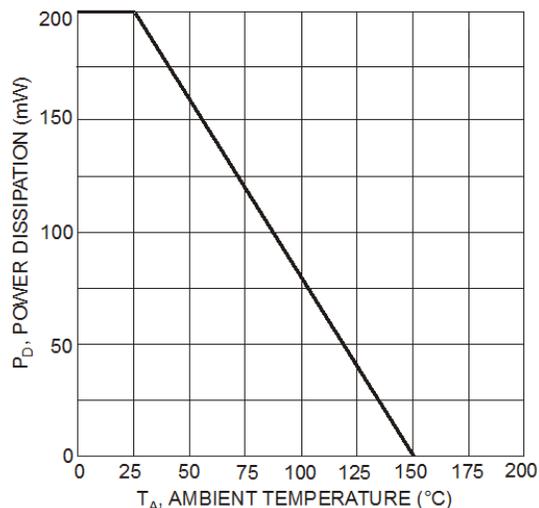


Fig. 1 Max Power Dissipation vs. Ambient Temperature

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$  unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>					
Collector-Base Breakdown Voltage	$BV_{CBO}$	-40	—	V	$I_C = -10\mu\text{A}, I_E = 0$
Collector-Emitter Breakdown Voltage	$BV_{CEO}$	-40	—	V	$I_C = -1\text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$BV_{EBO}$	-5	—	V	$I_E = -10\mu\text{A}, I_C = 0$
Collector Cutoff Current	$I_{CEX}$	—	-50	nA	$V_{CE} = -30\text{V}, V_{EB(OFF)} = -3\text{V}$
Base Cutoff Current	$I_{BL}$	—	-50	nA	$V_{CE} = -30\text{V}, V_{EB(OFF)} = -3\text{V}$
<b>ON CHARACTERISTICS (Note 7)</b>					
DC Current Gain	$h_{FE}$	60	—	—	$I_C = -100\mu\text{A}, V_{CE} = -1\text{V}$
		80	—		
		100	300		
		60	—		
		30	—		
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	-0.20 -0.30	V	$I_C = -10\text{mA}, I_B = -1\text{mA}$ $I_C = -50\text{mA}, I_B = -5\text{mA}$
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	-0.65 —	-0.85 -0.95	V	$I_C = -10\text{mA}, I_B = -1\text{mA}$ $I_C = -50\text{mA}, I_B = -5\text{mA}$
<b>SMALL SIGNAL CHARACTERISTICS</b>					
Output Capacitance	$C_{OBO}$	—	4.5	pF	$V_{CB} = -5\text{V}, f = 1.0\text{MHz}, I_E = 0$
Input Capacitance	$C_{IBO}$	—	10	pF	$V_{EB} = -0.5\text{V}, f = 1.0\text{MHz}, I_C = 0$
Input Impedance	$h_{iE}$	2	12	k $\Omega$	$V_{CE} = 1\text{V}, I_C = 10\text{mA},$ $f = 1\text{kHz}$
Voltage Feedback Ratio	$h_{rE}$	0.1	10	$\times 10^{-4}$	
Small Signal Current Gain	$H_{FE}$	100	400	—	
Output Admittance	$h_{oE}$	3	60	$\mu\text{S}$	
Current Gain-Bandwidth Product	$f_T$	300	—	MHz	
Noise Figure	NF	—	4	dB	$V_{CE} = -5\text{V}, I_C = -100\mu\text{A},$ $R_S = 1\text{k}\Omega, f = 1\text{kHz}$
<b>SWITCHING CHARACTERISTICS</b>					
Delay Time	$t_d$	—	35	ns	$V_{CC} = -3\text{V}, I_C = -10\text{mA},$
Rise Time	$t_r$	—	35	ns	$I_{B1} = -1\text{mA}, V_{BE(off)} = 0.5\text{V}$
Storage Time	$t_s$	—	225	ns	$V_{CC} = -3\text{V}, I_C = -10\text{mA},$
Fall Time	$t_f$	—	75	ns	$I_{B1} = I_{B2} = -1\text{mA}$

 Note: 7. Measured under pulsed conditions. Pulse width  $\leq 300\mu\text{s}$ . Duty cycle  $\leq 2\%$ .

**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

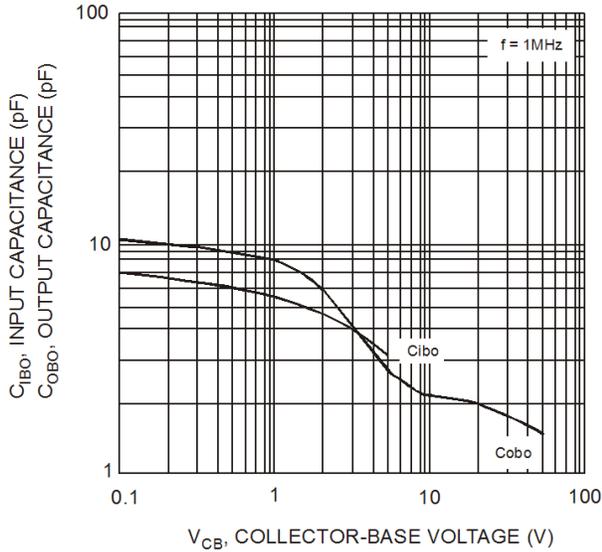


Fig. 2, Input and Output Capacitance vs. Collector-Base Voltage

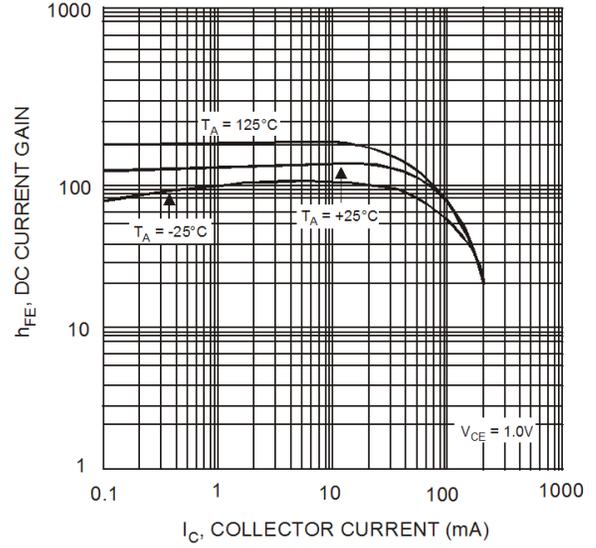


Fig. 3, Typical DC Current Gain vs. Collector Current

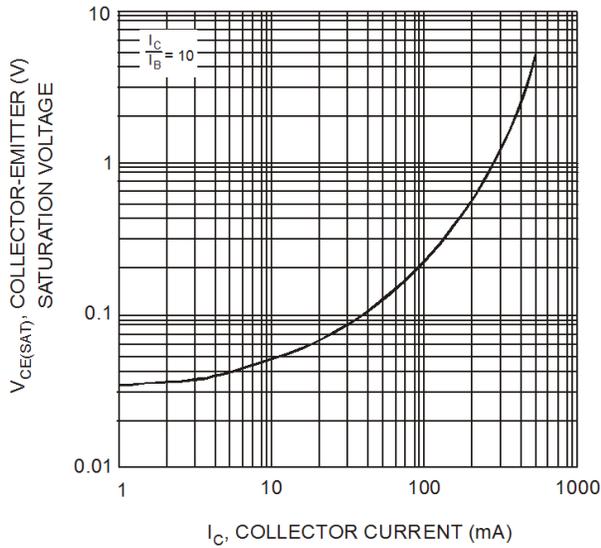


Fig. 4, Typical Collector-Emitter Saturation Voltage vs. Collector Current

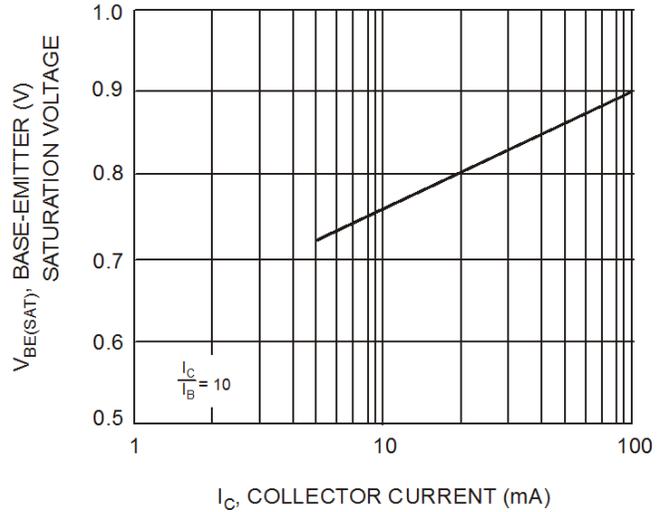
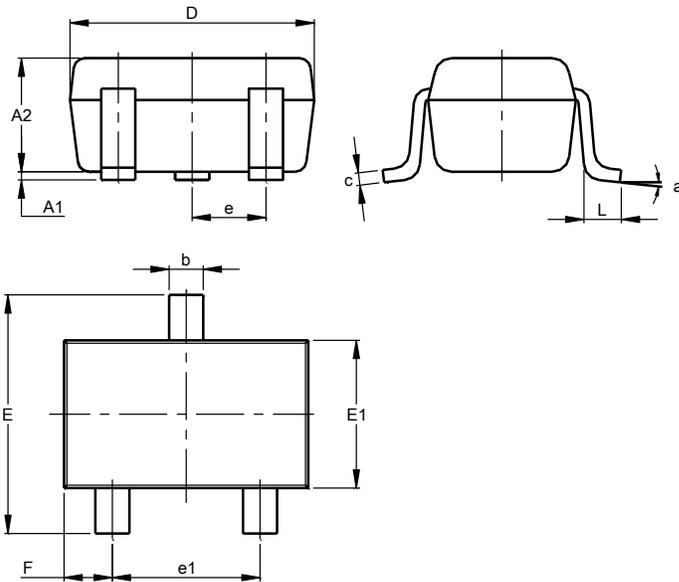


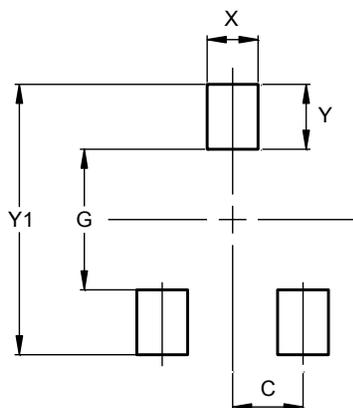
Fig. 5, Typical Base-Emitter Saturation Voltage vs. Collector Current

## Package Outline Dimensions



SOT323			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.25	0.40	0.30
c	0.10	0.18	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
e1	1.20	1.40	1.30
F	0.375	0.475	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

## Suggested Pad Layout



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500