优尚

YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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企业微信二维码

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Features

- NPN + PNP Combination
- BV_{CEO} > 40 (-40)V
- BV_{ECO} > 6 (-3)V
- I_{CM} = 9 (-9)A Peak Pulse Current
- $V_{CE(sat)}$ < 60 (-90)mV @ 1A
- $R_{CE(sat)}$ = 38 (58)mΩ

Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic, “Green” Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.015 grams (Approximate)

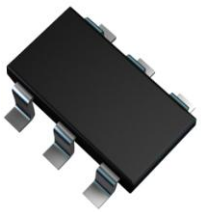
Description

Advanced process capability is used to achieve this high performance device. Combining NPN and PNP transistors in the SOT26 package provides a compact solution for the intended applications.

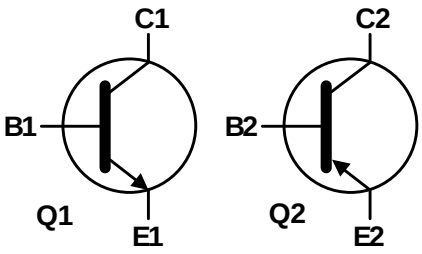
Applications

- MOSFET and IGBT Gate Driving
- Motor Drive

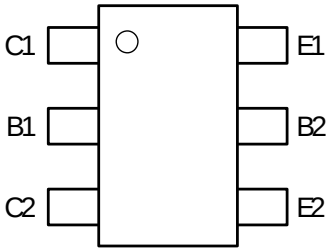
SOT26



Top View



Device Symbol



Top View
Pin-Out

Absolute Maximum Ratings – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	130	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Collector Voltage (Reverse blocking)	V_{ECO}	6	V
Emitter-Base Voltage	V_{EBO}	7	V
Continuous Collector Current	I_C	3.5	A
Peak Pulsed Collector Current	I_{CM}	9	A
Base Current	I_B	1	A

Absolute Maximum Ratings – Q2 (PNP Transistor) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-45	V
Collector-Emitter Voltage	V_{CEO}	-40	V
Emitter-Collector Voltage (Reverse blocking)	V_{ECO}	-3	V
Emitter-Base Voltage	V_{EBO}	-7	V
Continuous Collector Current	I_C	-3	A
Peak Pulsed Collector Current	I_{CM}	-9	A
Base Current	I_B	-1	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

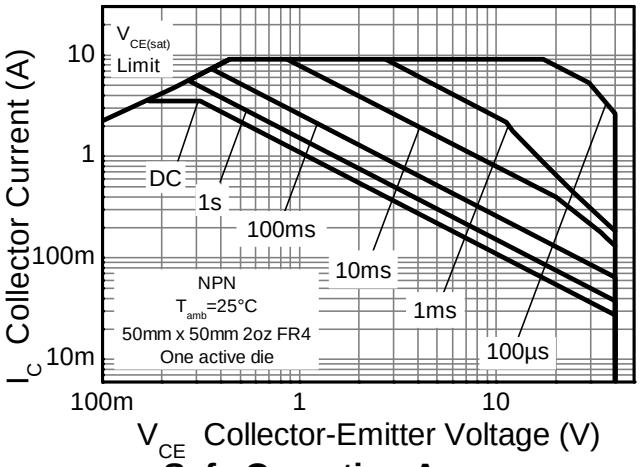
Characteristic		Symbol	Value	Unit
Power Dissipation Linear Derating Factor	(Notes 5 & 9)	P_D	0.7 5.6	W mW/°C
	(Notes 6 & 9)		0.9 7.2	
	(Notes 6 & 10)		1.1 8.8	
	(Notes 7 & 9)		1.1 8.8	
	(Notes 8 & 9)		1.7 13.6	
Thermal Resistance, Junction to Ambient	(Notes 5 & 9)	$R_{\theta JA}$	179	°C/W
	(Notes 6 & 9)		139	
	(Notes 6 & 10)		113	
	(Notes 7 & 9)		113	
	(Notes 8 & 9)		73	
Thermal Resistance, Junction to Lead	(Note 11)	$R_{\theta JL}$	87.58	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

ESD Ratings (Note 12)

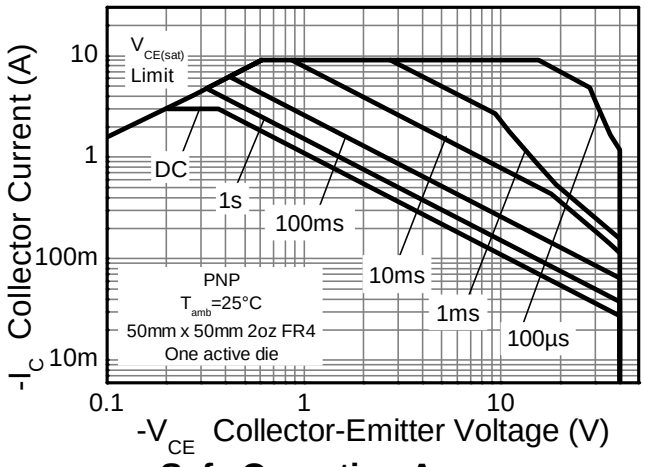
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

Notes:
5. For a device surface mounted on 15mm x 15mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
6. Same as Note (5), except the device is surface mounted on 25mm x 25mm 1oz copper.
7. Same as Note (5), except the device is surface mounted on 50mm x 50mm 2oz copper.
8. Same as Note (7), except the device is measured at t < 5 seconds.
9. For device with one active die, both collectors attached to a common heatsink.
10. For device with two active dice running at equal power, split heatsink 50% to each collector.
11. Thermal resistance from junction to solder-point (at the end of the collector lead).
12. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

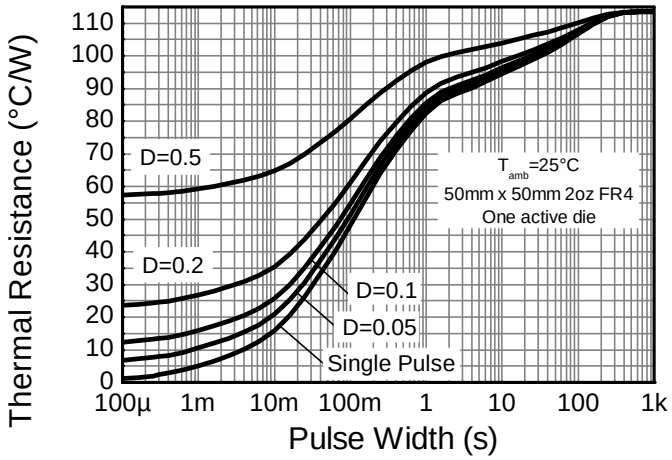
Thermal Characteristics and Derating Information



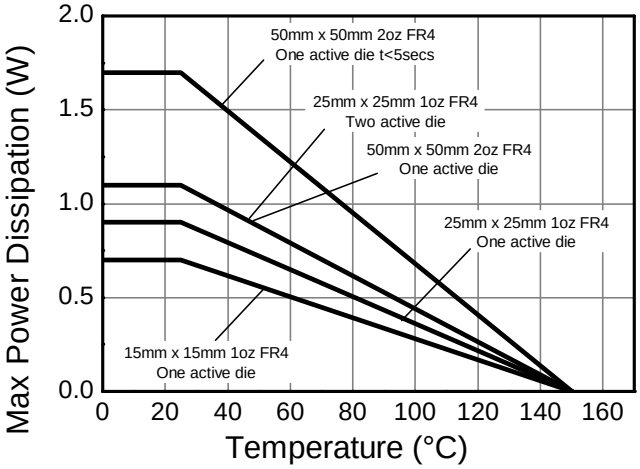
Safe Operating Area



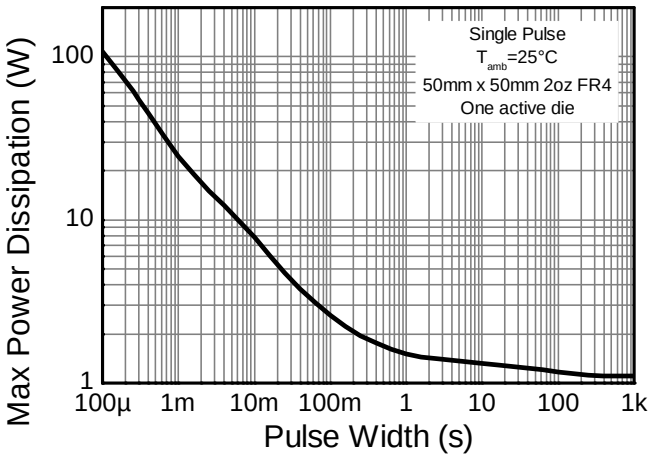
Safe Operating Area



Transient Thermal Impedance



Derating Curve



Pulse Power Dissipation

Electrical Characteristics – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV_{CBO}	130	170	—	V	I_C = 100μA, I_E = 0
Collector-Emitter Breakdown Voltage (Note 13)	BV_{CEO}	40	63	—	V	I_C = 10mA, I_B = 0
Emitter-Base Breakdown Voltage	BV_{EBO}	7	8.3	—	V	I_E = 100μA, I_C = 0
Emitter-Collector Breakdown Voltage (reverse blocking)	BV_{ECX}	6	7.4	—	V	I_E =100μA, R_{BC} < 1kΩ or 0.25V > V_{BC} > -0.25V
Emitter-Collector Breakdown Voltage (base open)	BV_{ECO}	6	7.4	—	V	I_E = 100μA
Collector Cutoff Current	I_{CBO}	—	<1 —	50 20	nA μA	V_{CB} = 100V V_{CB} = 100V, T_A = +100°C
Collector Cutoff Current	I_{EBO}	—	<1	50	nA	V_{EB} = 5.6V
ON CHARACTERISTICS (Note 13)						
DC Current Gain	h_{FE}	300 280 40	450 400 60	900	—	I_C = 10mA, V_{CE} = 2V I_C = 1.0A, V_{CE} = 2V I_C = 3.5A, V_{CE} = 2V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	50 85 150 135	60 110 220 195	mV	I_C = 1.0A, I_B = 100mA I_C = 1.0A, I_B = 20mA I_C = 2.0A, I_B = 40mA I_C = 3.5A, I_B = 350mA
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	—	960	1,050	mV	I_C = 3.5A, I_B = 350mA
Base-Emitter Turn-On Voltage	$V_{BE(on)}$	—	860	950	mV	I_C = 3.5A, V_{CE} = 2V
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C_{obo}	—	12	20	pF	V_{CB} = 10V, f = 1.0MHz
Current Gain-Bandwidth Product	f_T	—	190	—	MHz	V_{CE} = 10V, I_C = 50mA, f = 100MHz
Delay Time	t_d	—	64	—	ns	V_{CC} = 10V, I_C = 1A, I_{B1} = I_{B2} = 10mA
Rise Time	t_r	—	108	—	ns	
Storage Time	t_s	—	428	—	ns	
Fall Time	t_f	—	130	—	ns	

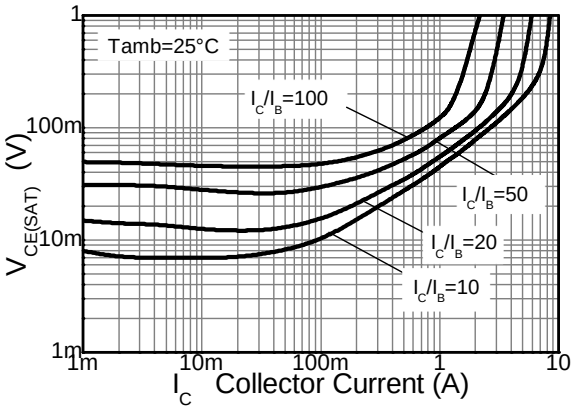
Note: 13. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

Electrical Characteristics – Q2 (PNP Transistor) (@T_A = +25°C, unless otherwise specified.)

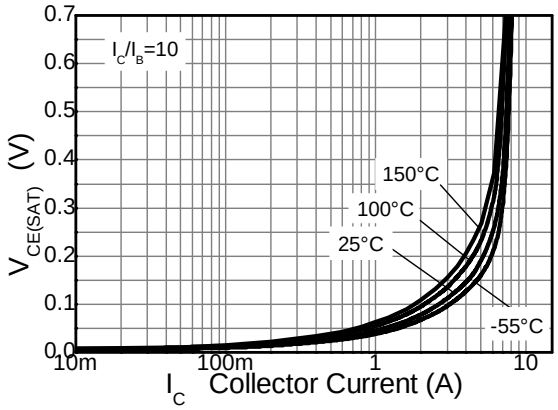
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV_{CBO}	-45	-80	—	V	I_C = -100μA, I_E = 0
Collector-Emitter Breakdown Voltage (Note 14)	BV_{CEO}	-40	-65	—	V	I_C = -10mA, I_B = 0
Emitter-Base Breakdown Voltage	BV_{EBO}	-7	-8.3	—	V	I_E = -100μA, I_C = 0
Emitter-Collector Breakdown Voltage (reverse blocking)	BV_{ECX}	-6	-7.4	—	V	$-I_E$ = 100μA, R_{BC} < 1kΩ or 0.25V < V_{BC} < -0.25V
Emitter-Collector Breakdown Voltage (base open)	BV_{ECO}	-3	-8.7	—	V	I_E = -100μA
Collector Cutoff Current	I_{CBO}	— —	<1	-50 -20	nA μA	V_{CB} = -36V V_{CB} = -36V, T_A = +100°C
Collector Cutoff Current	I_{EBO}	—	<1	-50	nA	V_{EB} = -5.6V
ON CHARACTERISTICS (Note 14)						
DC Current Gain	h_{FE}	300 200 20	450 280 50	900 — —	—	I_C = -10mA, V_{CE} = -2V I_C = -1.0A, V_{CE} = -2V I_C = -3.0A, V_{CE} = -2V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	— — —	-70 -195 -175	-90 -290 -260	mV	I_C = -1.0A, I_B = -100mA I_C = -1.0A, I_B = -20mA I_C = -3.0A, I_B = -300mA
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	—	-935	-1,000	mV	I_C = -3.0A, I_B = -300mA
Base-Emitter Turn-On Voltage	$V_{BE(on)}$	—	-855	-950	mV	I_C = -3.0A, V_{CE} = -2V
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C_{obo}	—	17	25	pF	V_{CB} = -10V, f = 1.0MHz
Current Gain-Bandwidth Product	f_T	—	270	—	MHz	V_{CE} = -10V, I_C = -50mA, f = 100MHz
Delay Time	t_d	—	57	—	ns	V_{CC} = -10V, I_C = -1A, I_{B1} = I_{B2} = -10mA
Rise Time	t_r	—	69	—	ns	
Storage Time	t_s	—	154	—	ns	
Fall Time	t_f	—	60	—	ns	

Note: 14. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

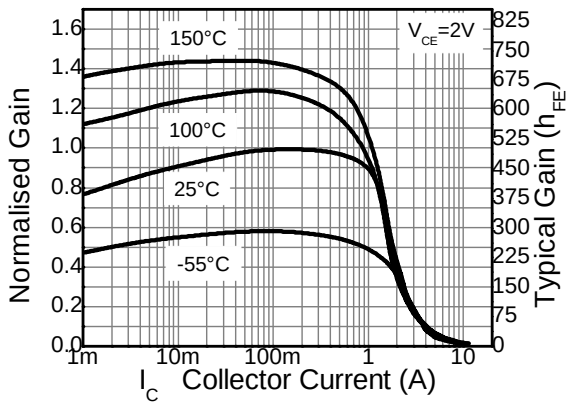
Typical Electrical Characteristics – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)



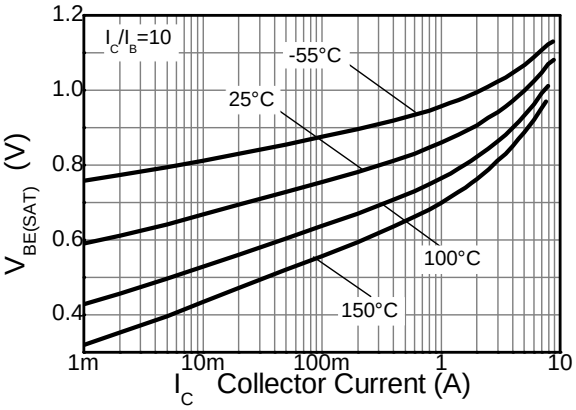
$V_{CE(SAT)}$ v I_C



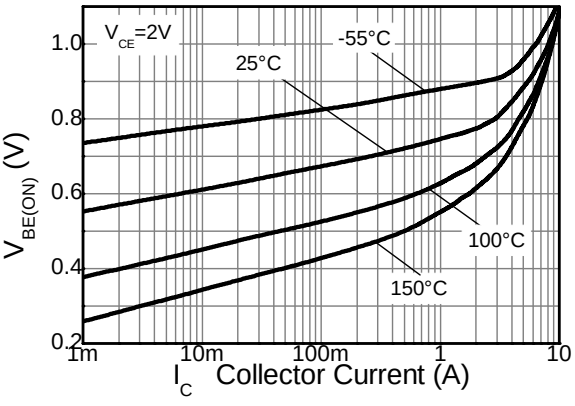
$V_{CE(SAT)}$ v I_C



h_{FE} v I_C

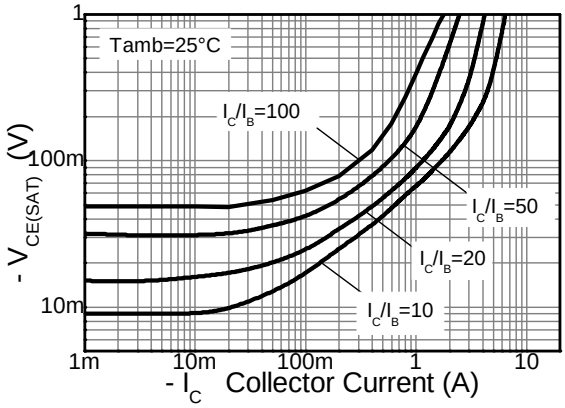


$V_{BE(SAT)}$ v I_C

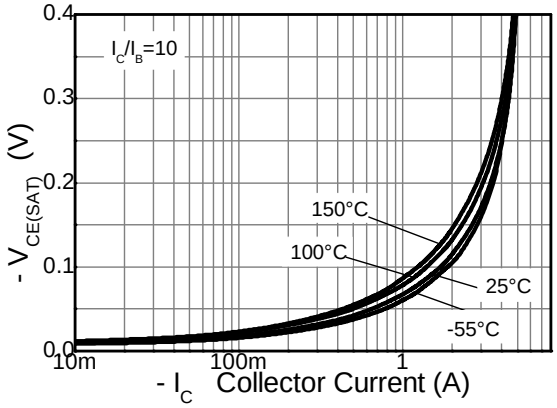


$V_{BE(ON)}$ v I_C

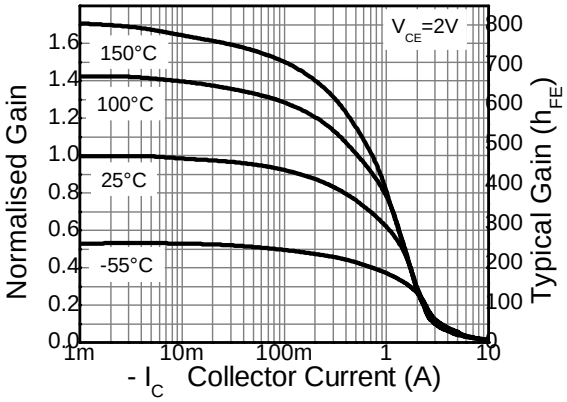
Typical Electrical Characteristics – Q2 (PNP Transistor) (@T_A = +25°C, unless otherwise specified.)



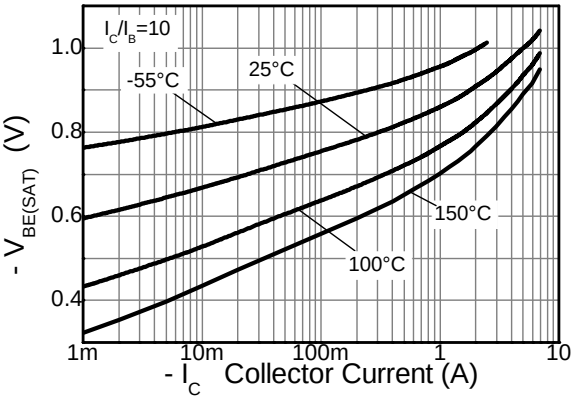
$V_{CE(SAT)}$ v I_C



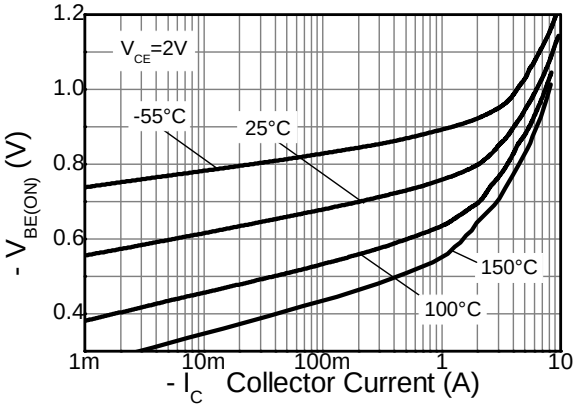
$V_{CE(SAT)}$ v I_C



h_{FE} v I_C



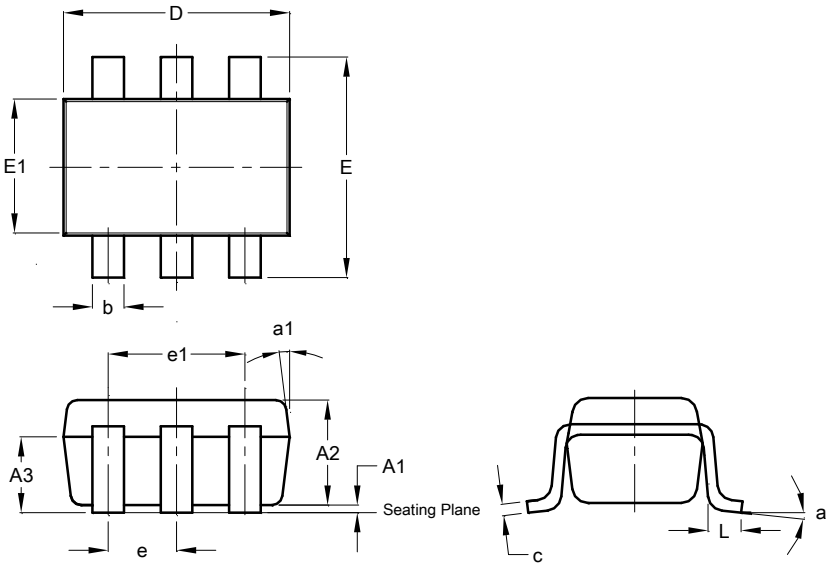
$V_{BE(SAT)}$ v I_C



$V_{BE(ON)}$ v I_C

Package Outline Dimensions

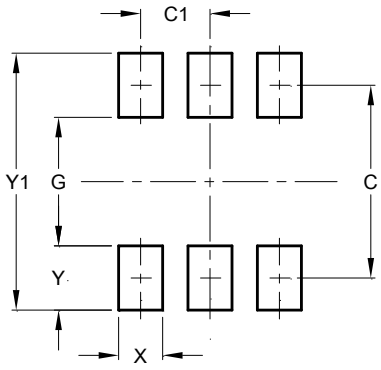
SOT26



SOT26			
Dim	**Min**	**Max**	**Typ**
A1	0.013	0.10	0.05
A2	1.00	1.30	1.10
A3	0.70	0.80	0.75
b	0.35	0.50	0.38
c	0.10	0.20	0.15
D	2.90	3.10	3.00
e	-	-	0.95
e1	-	-	1.90
E	2.70	3.00	2.80
E1	1.50	1.70	1.60
L	0.35	0.55	0.40
a	-	-	8°
a1	-	-	7°
All Dimensions in mm			

Suggested Pad Layout

SOT26



Dimensions	Value (in mm)
C	2.40
C1	0.95
G	1.60
X	0.55
Y	0.80
Y1	3.20