



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

0755-83047638
ysbdt@szyoushang.cn
www.szyoushang.cn



企业微信二维码



企业QQ二维码

Features

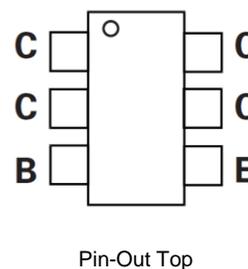
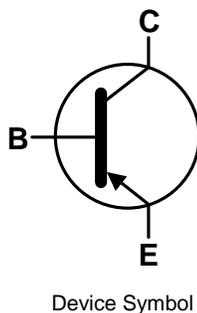
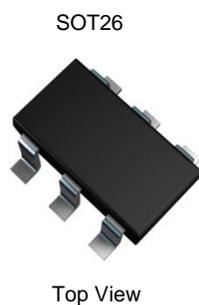
- $BV_{CEO} > -20V$
- $I_C = -3.5A$ Max Continuous Collector Current
- $I_{CM} = -10A$ Peak Pulse Current
- $R_{CE(SAT)} = 31m\Omega$ for a low equivalent On-Resistance
- Low Saturation Voltage (-70mV max @ 1A/100mA)
- h_{FE} characterized up to -10A for high current gain hold up

Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads,
Solderable per MIL-STD-202, Method 208 
- Weight: 0.015 grams (Approximate)

Applications

- DC - DC Converters
- Power Management Functions
- Power Switches
- Motor Control



Absolute Maximum Ratings (@ $T_A = +25^{\circ}\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-25	V
Collector-Emitter Voltage	V_{CEO}	-20	V
Emitter-Base Voltage	V_{EBO}	-7.5	V
Continuous Collector Current	I_C	-3.5	A
Peak Pulse Collector Current	I_{CM}	-10	A

Thermal Characteristics (@ $T_A = +25^{\circ}\text{C}$, unless otherwise specified.)

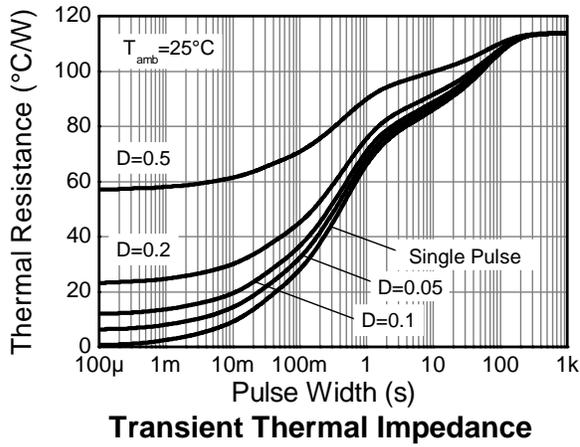
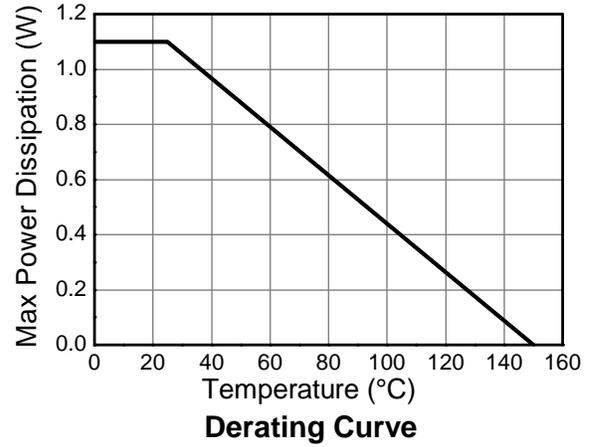
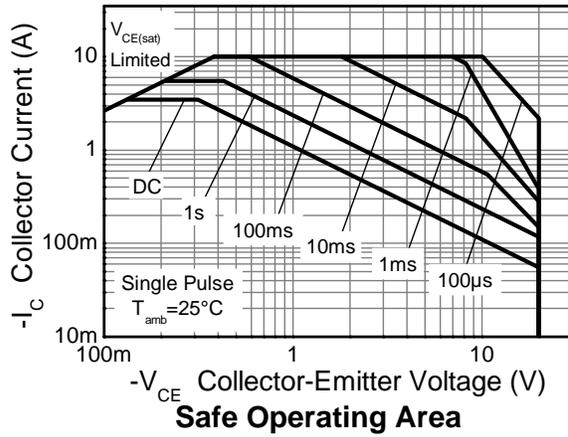
Characteristic	Symbol	Value	Unit
Power Dissipation Linear Derating Factor	P_D	1.1	W
		8.8	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	1.7	$\text{mW}/^{\circ}\text{C}$
		13.6	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	113	$^{\circ}\text{C}/\text{W}$
		73	
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	18.61	
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^{\circ}\text{C}$

ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

- Notes:
5. For a device mounted with collector leads on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
 6. Same as note (5), except the device is measured at $t \leq 5\text{secs}$.
 7. Thermal resistance from junction to solder-point (at the end of the collector leads).
 8. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information

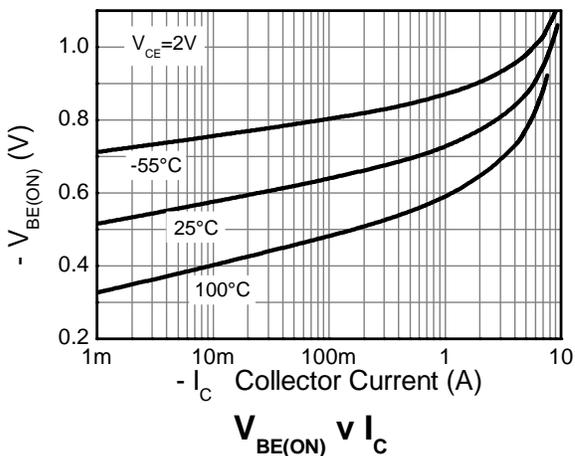
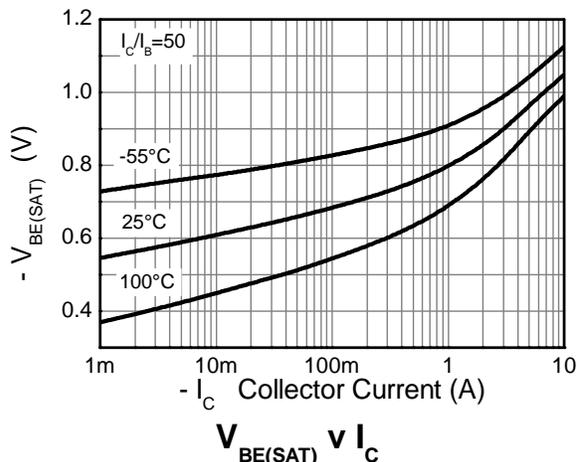
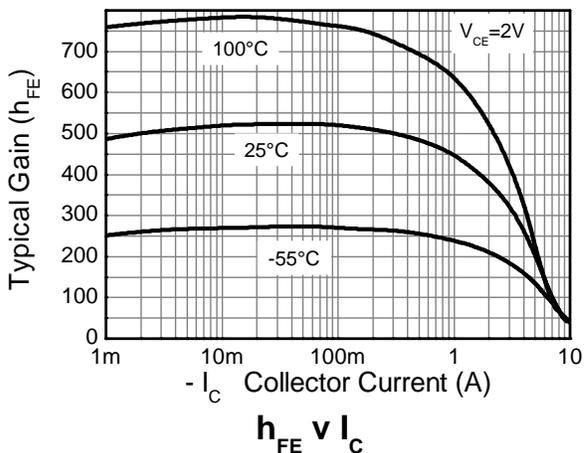
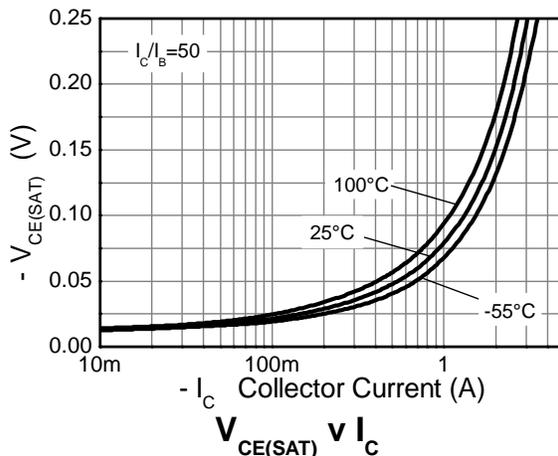
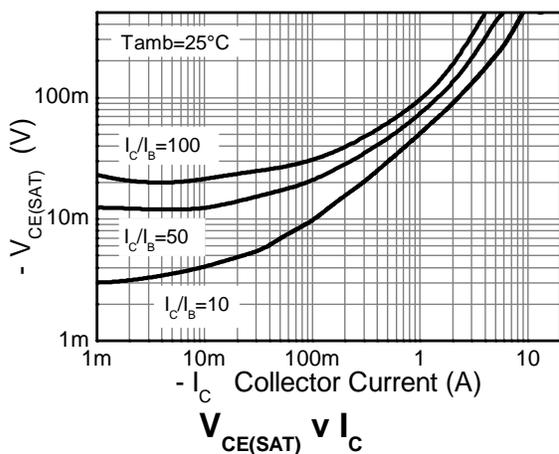


Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

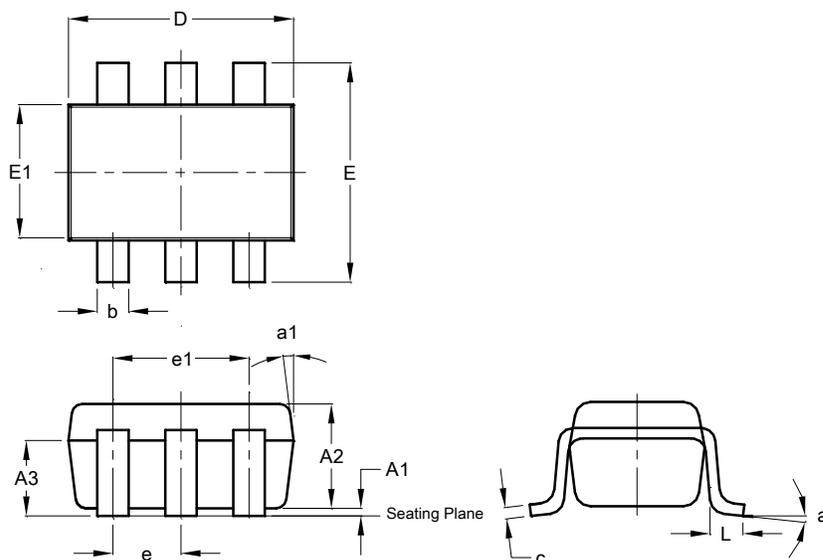
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV_{CBO}	-25	-49	—	V	$I_C = -100\mu\text{A}$
Collector-Emitter Breakdown Voltage (Note 9)	BV_{CEO}	-20	-43	—	V	$I_C = -10\text{mA}$
Emitter-Base Breakdown Voltage	BV_{EBO}	-7.5	-8.4	—	V	$I_E = -100\mu\text{A}$
Collector-Base Cutoff Current	I_{CBO}	—	—	-100	nA	$V_{CB} = -20\text{V}$
Emitter Cutoff Current	I_{EBO}	—	—	-100	nA	$V_{EB} = -6\text{V}$
Collector-Emitter Cutoff Current	I_{CES}	—	—	-100	nA	$V_{CES} = -20\text{V}$
ON CHARACTERISTICS (Note 9)						
DC Current Gain	h_{FE}	300	575	—	—	$I_C = -10\text{mA}, V_{CE} = -2\text{V}$
		300	450	900	—	$I_C = -1\text{A}, V_{CE} = -2\text{V}$
		150	285	—	—	$I_C = -3.5\text{A}, V_{CE} = -2\text{V}$
		10	40	—	—	$I_C = -10\text{A}, V_{CE} = -2\text{V}$
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	-10	-15	mV	$I_C = -100\text{mA}, I_B = -10\text{mA}$
		—	-100	-140		$I_C = -1\text{A}, I_B = -10\text{mA}$
		—	-110	-130		$I_C = -3.5\text{A}, I_B = -350\text{mA}$
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	—	-0.96	-1.1	V	$I_C = -3.5\text{A}, I_B = -350\text{mA}$
Base-Emitter Turn-On Voltage	$V_{BE(on)}$	—	-0.8	-0.9	V	$I_C = -3.5\text{A}, V_{CE} = -2\text{V}$
SMALL SIGNAL CHARACTERISTICS						
Current Gain-Bandwidth Product	f_T	—	110	—	MHz	$V_{CE} = -10\text{V}, I_C = -50\text{mA}, f = 50\text{MHz}$
Output Capacitance	C_{obo}	—	45	—	pF	$V_{CB} = -10\text{V}, f = 1\text{MHz}$

 Note: 9. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$.

Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

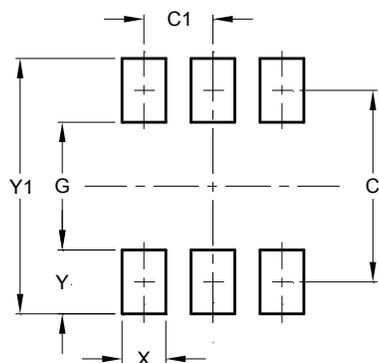


Package Outline Dimensions



SOT26			
Dim	Min	Max	Typ
A1	0.013	0.10	0.05
A2	1.00	1.30	1.10
A3	0.70	0.80	0.75
b	0.35	0.50	0.38
c	0.10	0.20	0.15
D	2.90	3.10	3.00
e	-	-	0.95
e1	-	-	1.90
E	2.70	3.00	2.80
E1	1.50	1.70	1.60
L	0.35	0.55	0.40
a	-	-	8°
a1	-	-	7°
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
C	2.40
C1	0.95
G	1.60
X	0.55
Y	0.80
Y1	3.20