



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Description

This regulator transistor is designed to meet the stringent requirements of automotive applications.

The NK-ZXTR2105FFQ monolithically integrates a transistor, zener diode and resistor to function as a linear regulator. The device regulates with a 5V nominal output at 15mA. It is designed for use in high-voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT23F package, minimizing PCB area and reducing the number of components when compared with a multi-chip discrete solution.

Applications

Supply voltage regulation for:

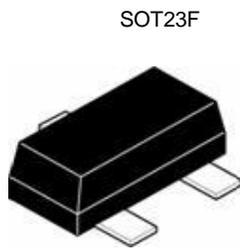
- 12V to 5V Rails
- 24V to 5V Rails
- Other Customized Input Rails

Features

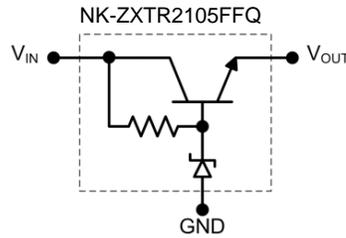
- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 7V to 60V (For Regulated Output Voltage)
- Output Voltage = 5V \pm 5%
- Fully Integrated into a SOT23F Package

Mechanical Data

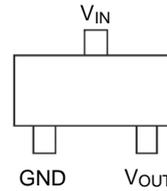
- Case: SOT23F
- Case Material: Molded Plastic "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (Approximate)



Top View



Internal Device Schematic



Top View Pin-Out

Pin Name	Pin Function
V_{IN}	Input Supply
GND	Power Ground
V_{OUT}	Voltage Output

Absolute Maximum Ratings (Voltage relative to GND, @ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	V_{IN}	-0.3 to 60	V
Continuous Input and Output Current	I_{IN}, I_{OUT}	320	mA
Peak Pulsed Input and Output Current	I_{IM}, I_{OM}	2	A
Maximum Voltage Applied to V_{OUT}	$V_{OUT(MAX)}$	Smaller of $V_{IN}+5V$ or 10V	V

Maximum Current at $V_{IN} = 12V$ (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Continuous Output Current	I_{OUT}	89	mA
Pulsed Output Current	I_{OM}	2,000	mA
		890	

Thermal Characteristics

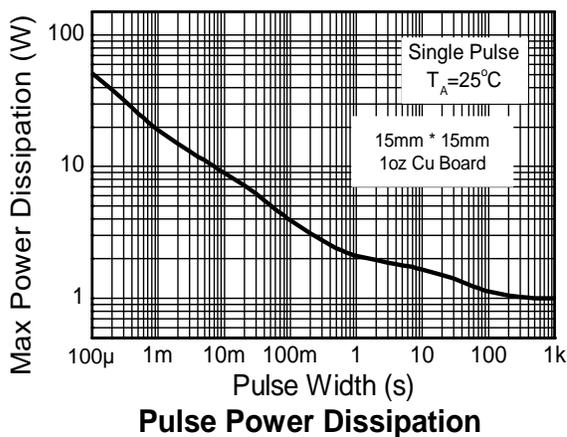
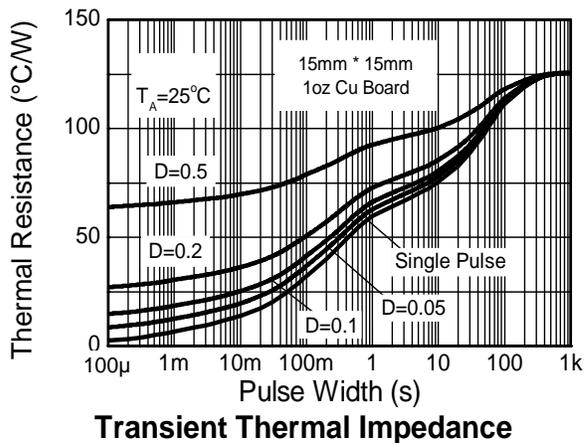
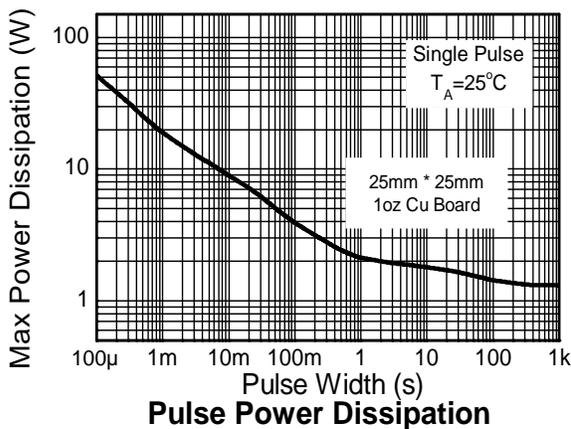
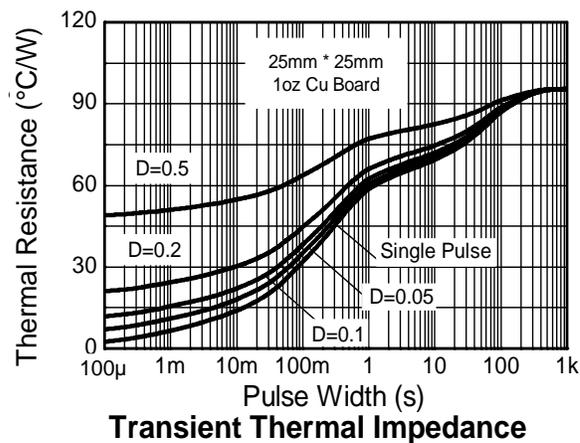
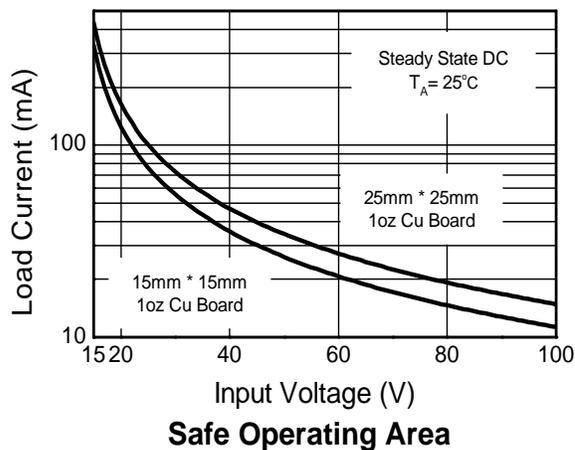
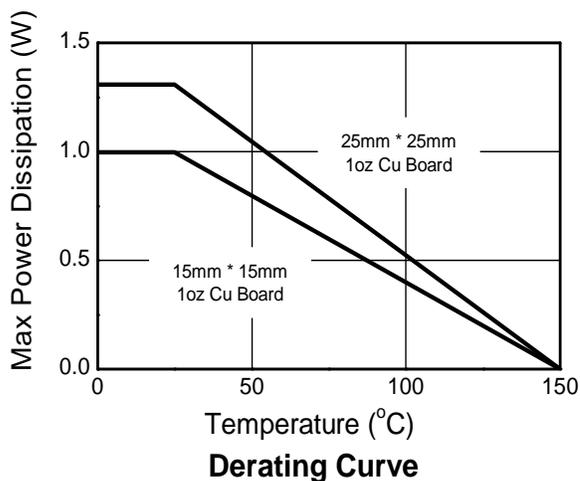
Characteristic	Symbol	Value	Unit	
Power Dissipation	P_D	1.3	W	
		1		
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	95	$^\circ\text{C/W}$	
		126		
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	59		
Thermal Resistance, Junction to Case	$R_{\theta JC}$	38		
Maximum Operating Junction and Storage Temperature Range	T_J, T_{STG}	-65 to +150		$^\circ\text{C}$

ESD Ratings (Note 12)

Characteristics	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Charge Device Model	ESD CDM	1,000	V	IV

- Notes:
6. For a device mounted with the V_{IN} lead on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in steady-state.
 7. Same as Note 6, except mounted on 15mm x 15mm 1oz copper.
 8. Same as Note 6, whilst operating at $V_{IN}=12V$. Refer to Safe Operating Area for other Input Voltages.
 9. Same as Note 6, except measured with a single pulse width = 100 μs and $V_{IN}=12V$.
 10. Same as Note 6, except measured with a single pulse width = 10ms and $V_{IN}=12V$.
 11. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (at the end of the V_{IN} lead). $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
 12. Refer to JEDEC specification JESD22-A114 and JESD22-C101.

Thermal Characteristics and Derating Information



Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Output Voltage (Note 13)	V_{OUT}	4.75	5.0	5.25	V	$V_{IN} = 12\text{V}$, $I_{OUT} = 15\text{mA}$
Line Regulation (Notes 13 & 14)	ΔV_{OUT}	—	33	220	mV	$V_{IN} = 10\text{V to } 15\text{V}$, $I_{OUT} = 15\text{mA}$
		—	400	700		$V_{IN} = 7\text{V to } 60\text{V}$, $I_{OUT} = 15\text{mA}$
		—	145	400		$V_{IN} = 10\text{V to } 60\text{V}$, $I_{OUT} = 15\text{mA}$
Temperature Coefficient	$\Delta V_{OUT}/\Delta T$	—	3.52	—	mV/ $^\circ\text{C}$	$T_J = -40^\circ\text{C to } +150^\circ\text{C}$ $V_{IN} = 12\text{V}$, $I_{OUT} = 15\text{mA}$
Load Regulation (Notes 13 & 15)	ΔV_{OUT}	—	-20 -166	-130 -300	mV	$I_{OUT} = 10\text{mA to } 20\text{mA}$, $V_{IN} = 12\text{V}$ $I_{OUT} = 0.1\text{mA to } 50\text{mA}$, $V_{IN} = 12\text{V}$
Minimum Value of Input Voltage Required to Maintain Line Regulation	$V_{IN(MIN)}$	7	—	—	V	—
Quiescent Current	I_Q	—	450 4,000	800 6,700	μA	$V_{IN} = 12\text{V}$, $I_{OUT} = 10\mu\text{A}$ $V_{IN} = 60\text{V}$, $I_{OUT} = 10\mu\text{A}$
Power Supply Rejection Ratio	$\Delta V_{IN}/\Delta V_{OUT}$	—	46	—	dB	$C_{OUT} = 100\text{nF}$, $I_{OUT} = 15\text{mA}$, $V_{OUT} = 5\text{V}$, $V_{IN} = 7\text{V to } 60\text{V}$, $f = 100\text{Hz}$

Notes: 13. Measured Under Pulsed Conditions; Pulse Width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$.

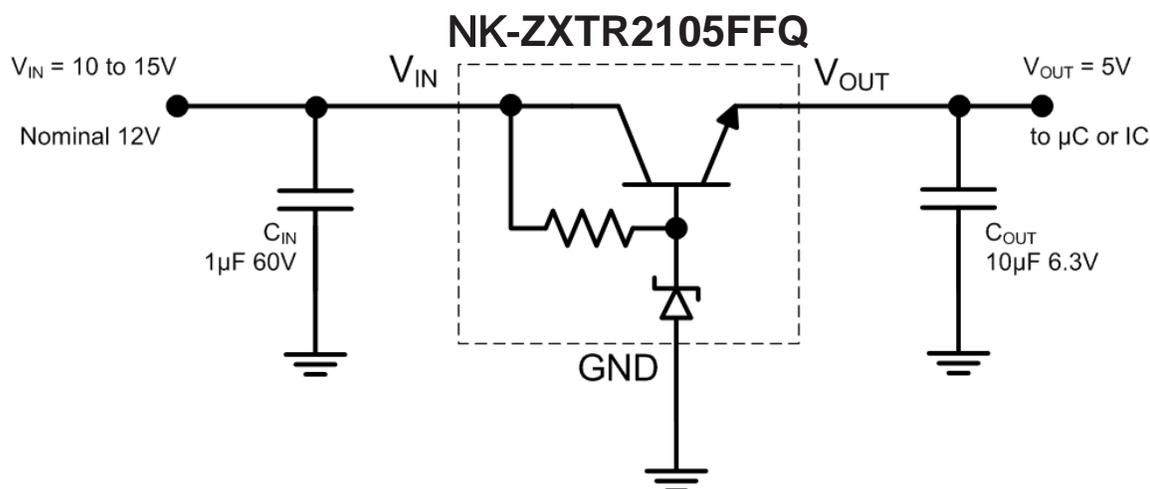
14. Line Regulation $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 15\text{V}) - V_{OUT}(@V_{IN} = 10\text{V})$

$\Delta V_{OUT} = V_{OUT}(@V_{IN} = 60\text{V}) - V_{OUT}(@V_{IN} = 7\text{V})$

$\Delta V_{OUT} = V_{OUT}(@V_{IN} = 60\text{V}) - V_{OUT}(@V_{IN} = 10\text{V})$

15. Load Regulation $\Delta V_{OUT} = V_{OUT}(@I_{OUT} = 20\text{mA}) - V_{OUT}(@I_{OUT} = 10\text{mA})$

$\Delta V_{OUT} = V_{OUT}(@I_{OUT} = 50\text{mA}) - V_{OUT}(@I_{OUT} = 0.1\text{mA})$

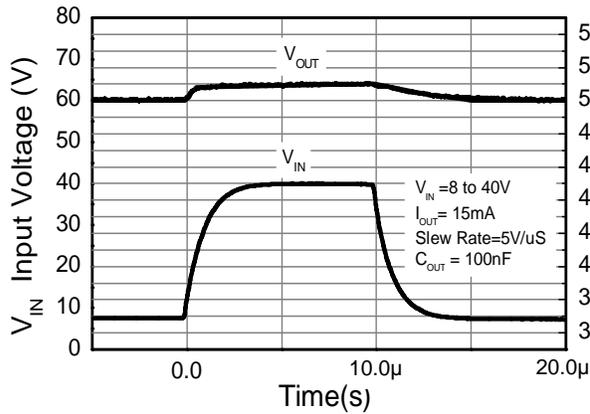
Typical Application Circuit


Example of a 5V regulated supply from a nominal 12V for powering a Controller IC.

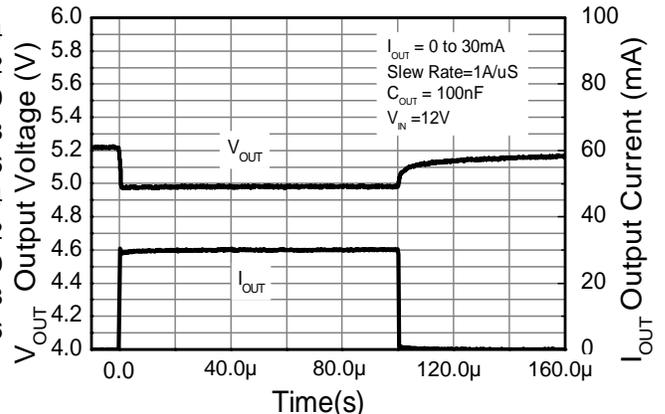
Pin Functions

Pin Name	Pin Function	Notes
V_{IN}	Input Supply	Input voltage can vary from -0.3V to 60V with respect to GND; for V_{OUT} regulated then $7\text{V} \leq V_{IN} \leq 60\text{V}$. It is recommended to connect a $1\mu\text{F}$ capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
V_{OUT}	Voltage Output	Outputs a regulated 5V when $7\text{V} \leq V_{IN} \leq 60\text{V}$. When $V_{IN} < 7\text{V}$, then V_{OUT} maximum = $V_{IN} - 1\text{V}$. The pin can be pulled high to a maximum of $+10\text{V}$ with respect to GND, or $+5\text{V}$ with respect to V_{IN} , whichever is lower. It is recommended to connect a $10\mu\text{F}$ capacitor to GND and a minimum of $10\mu\text{A}$ to be drawn from V_{OUT} to maintain regulation.

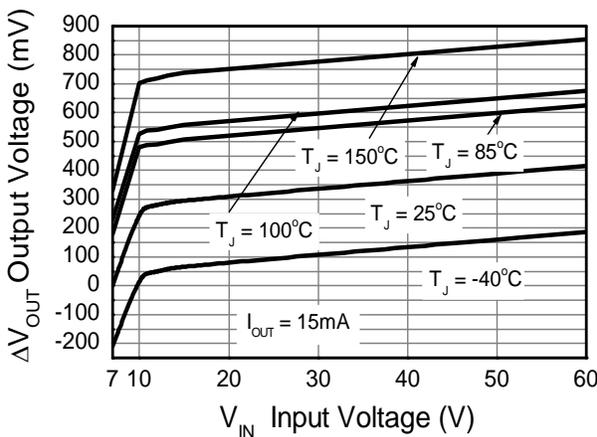
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



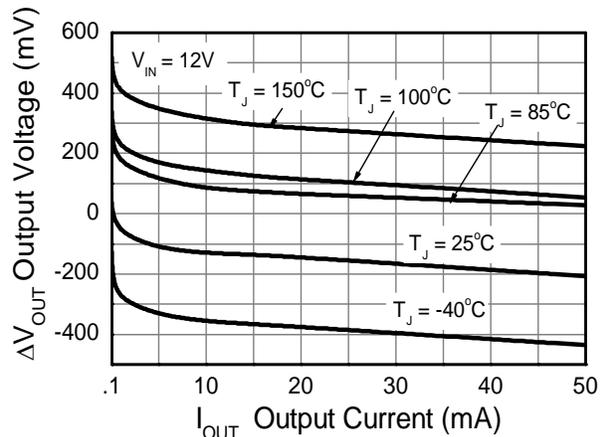
Line Transient Response



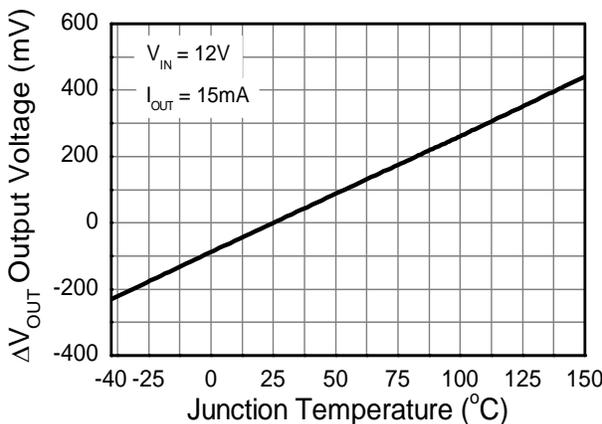
Load Transient Response



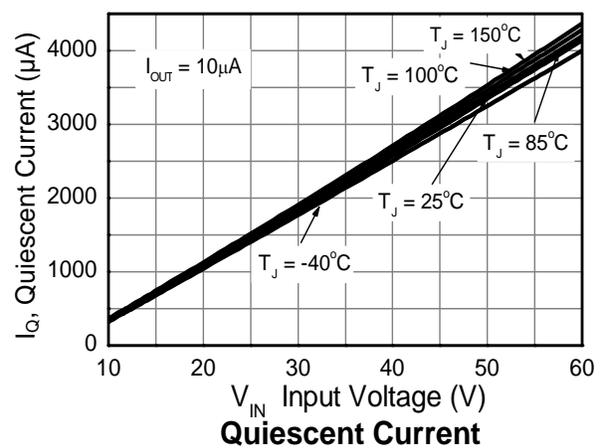
Line Regulation (Note 16)



Load Regulation (Note 17)



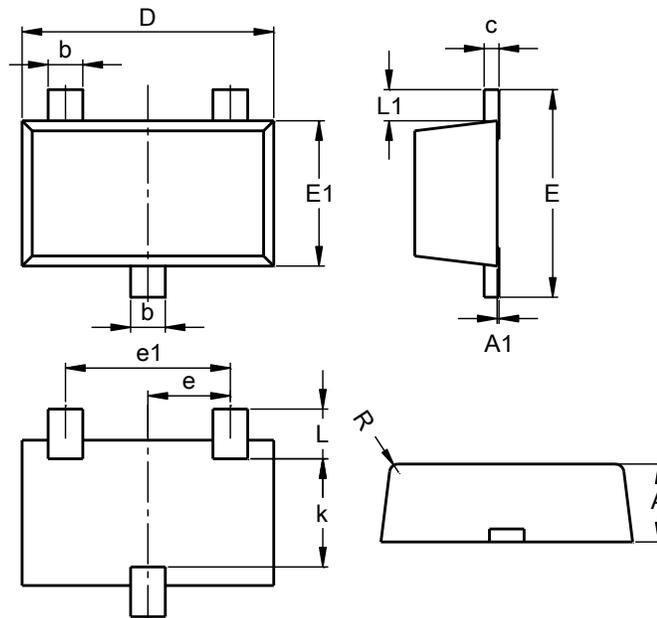
Temperature Coefficient (Note 18)



Quiescent Current

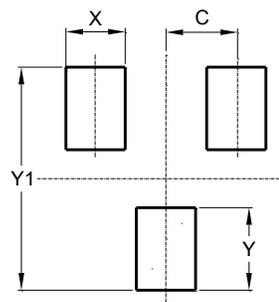
- Notes:
- 16. Line Regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 7\text{V}, I_{OUT} = 15\text{mA}, T_J = +25^\circ\text{C})$.
 - 17. Load Regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 12\text{V}, I_{OUT} = 0.1\text{mA}, T_J = +25^\circ\text{C})$.
 - 18. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 12\text{V}, I_{OUT} = 15\text{mA}, T_J = +25^\circ\text{C})$.

Package Outline Dimensions

SOT23F


SOT23F			
Dim	Min	Max	Typ
A	0.80	1.00	0.90
A1	0.00	0.10	0.01
b	0.35	0.50	0.44
c	0.10	0.20	0.16
D	2.80	3.00	2.90
e	0.95 REF		
e1	1.90 REF		
E	2.30	2.50	2.40
E1	1.50	1.70	1.65
k	1.20	-	-
L	0.30	0.65	0.50
L1	0.30	0.50	0.40
R	0.05	0.15	-
All Dimensions in mm			

Suggested Pad Layout

SOT23F


Dimensions	Value (in mm)
C	0.95
X	0.80
Y	1.110
Y1	3.000