



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

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## Description

The NK-ZXTR2105FQ monolithically integrates a transistor, zener diode and resistor to function as a linear regulator. The device regulates with a 5V nominal output at 15mA. It is designed for use in high-voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT23 package, minimizing PCB area and reducing the number of components when compared with a multi-chip discrete solution.

This linear regulator is designed to meet the stringent requirement of automotive applications.

## Applications

Supply voltage regulation for:

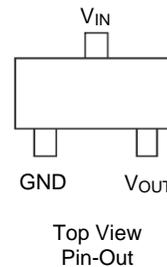
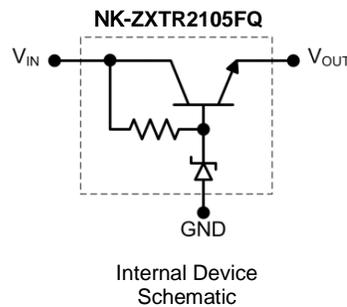
- 12V to 5V Rails
- 24V to 5V Rails
- Other Customized Input Rails

## Features

- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 7V to 60V (For Regulated Output Voltage)
- Output Voltage = 5V  $\pm$  5%
- Fully Integrated into a SOT23 Package

## Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (Approximate)



Pin Name	Pin Function
V <sub>IN</sub>	Input Supply
GND	Power Ground
V <sub>OUT</sub>	Voltage Output

**Absolute Maximum Ratings** (Voltage relative to GND, @ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	$V_{IN}$	-0.3 to 60	V
Continuous Input and Output Current	$I_{IN}, I_{OUT}$	320	mA
Peak Pulsed Input and Output Current	$I_{IM}, I_{OM}$	2	A
Maximum Voltage Applied to $V_{OUT}$	$V_{OUT(MAX)}$	Smaller of $V_{IN}+5V$ or 10V	V

**Maximum Current at  $V_{IN} = 12V$**  (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Continuous Output Current	$I_{OUT}$	89	mA
Pulsed Output Current	$I_{OM}$	2,000	mA
		890	

**Thermal Characteristics**

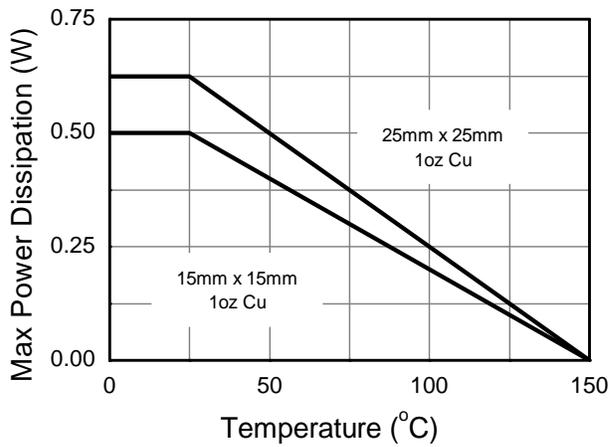
Characteristic	Symbol	Value	Unit
Power Dissipation	$P_D$	625	mW
		500	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	$^\circ\text{C/W}$
		250	
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	197	$^\circ\text{C}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	17	
Maximum Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-65 to +150	$^\circ\text{C}$

**ESD Ratings** (Note 12)

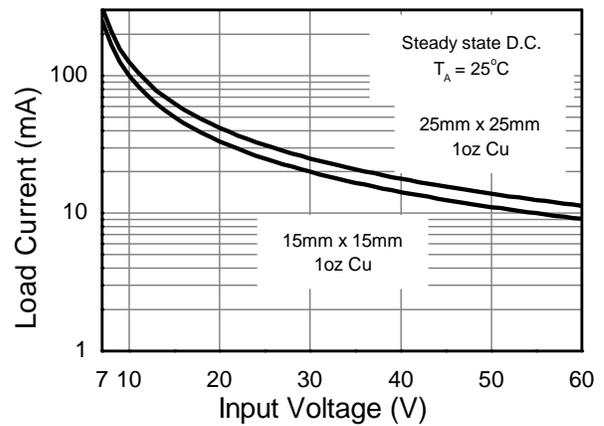
Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	C

- Notes:
6. For a device mounted with the  $V_{IN}$  lead on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in steady-state.
  7. Same as Note 6, except mounted on 15mm x 15mm 1oz copper.
  8. Same as Note 6, whilst operating at  $V_{IN}=12V$ . Refer to Safe Operating Area for other Input Voltages.
  9. Same as Note 6, except measured with a single pulse width = 100 $\mu\text{s}$  and  $V_{IN}=12V$ .
  10. Same as Note 6, except measured with a single pulse width = 10ms and  $V_{IN}=12V$ .
  11.  $R_{\theta JL}$  = Thermal resistance from junction to solder-point (at the end of the  $V_{IN}$  lead).  $R_{\theta JC}$  = Thermal resistance from junction to the top of case.
  12. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

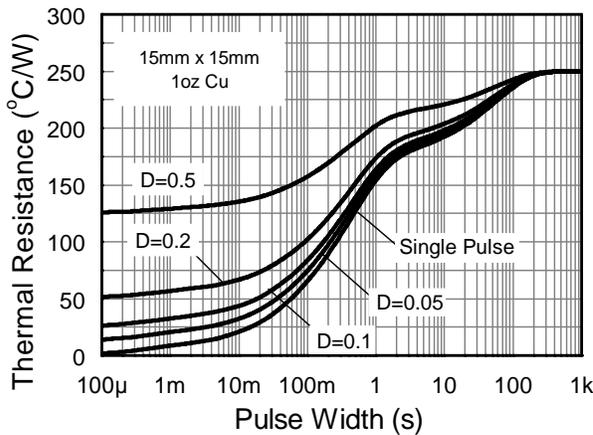
**Thermal Characteristics and Derating Information**



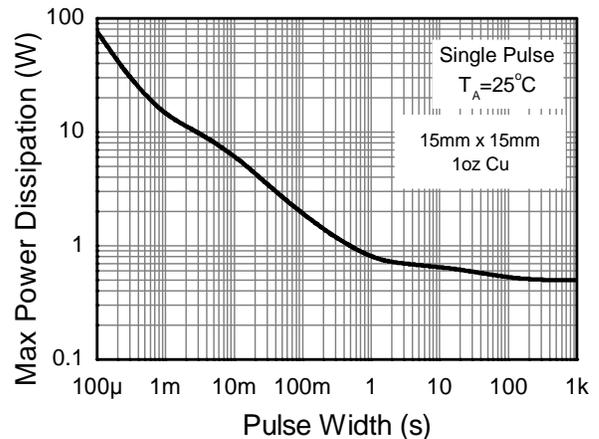
**Derating Curve**



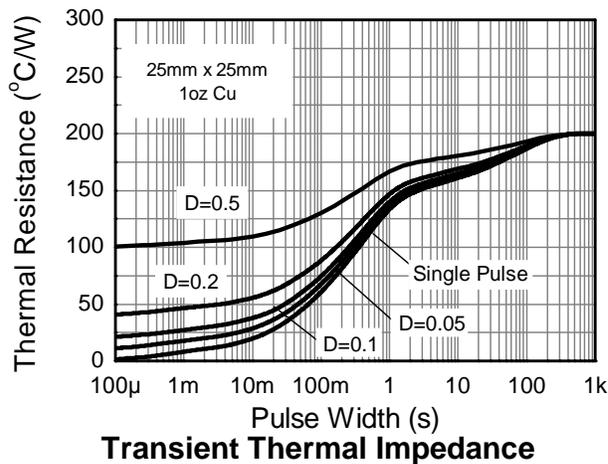
**Safe Operating Area**



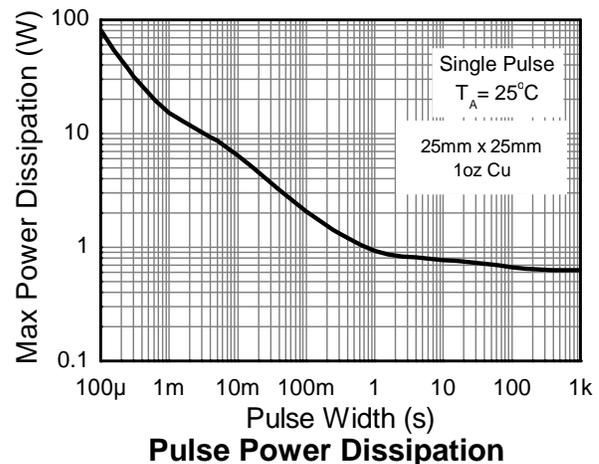
**Transient Thermal Impedance**



**Pulse Power Dissipation**



**Transient Thermal Impedance**



**Pulse Power Dissipation**

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Output Voltage (Note 13)	$V_{OUT}$	4.75	5.0	5.25	V	$V_{IN} = 12\text{V}$ , $I_{OUT} = 15\text{mA}$
Line Regulation (Notes 13 & 14)	$\Delta V_{OUT}$	—	33	220	mV	$V_{IN} = 10\text{V}$ to $15\text{V}$ , $I_{OUT} = 15\text{mA}$
		—	400	700		$V_{IN} = 7\text{V}$ to $60\text{V}$ , $I_{OUT} = 15\text{mA}$
		—	145	400		$V_{IN} = 10\text{V}$ to $60\text{V}$ , $I_{OUT} = 15\text{mA}$
Temperature Coefficient	$\Delta V_{OUT}/\Delta T$	—	3.52	—	mV/ $^\circ\text{C}$	$T_J = -40^\circ\text{C}$ to $+150^\circ\text{C}$ $V_{IN} = 12\text{V}$ , $I_{OUT} = 15\text{mA}$
Load Regulation (Notes 13 & 15)	$\Delta V_{OUT}$	—	-20 -166	-130 -300	mV	$I_{OUT} = 10\text{mA}$ to $20\text{mA}$ , $V_{IN} = 12\text{V}$ $I_{OUT} = 0.1\text{mA}$ to $50\text{mA}$ , $V_{IN} = 12\text{V}$
Minimum Value of Input Voltage Required to Maintain Line Regulation	$V_{IN(MIN)}$	7	—	—	V	—
Quiescent Current	$I_Q$	—	450 4,000	800 6,700	$\mu\text{A}$	$V_{IN} = 12\text{V}$ , $I_{OUT} = 10\mu\text{A}$ $V_{IN} = 60\text{V}$ , $I_{OUT} = 10\mu\text{A}$
Power Supply Rejection Ratio	$\Delta V_{IN}/\Delta V_{OUT}$	—	46	—	dB	$C_{OUT} = 100\text{nF}$ , $I_{OUT} = 15\text{mA}$ , $V_{OUT} = 5\text{V}$ , $V_{IN} = 7\text{V}$ to $60\text{V}$ , $f = 100\text{Hz}$

Notes: 13. Measured Under Pulsed Conditions; Pulse Width  $\leq 300\mu\text{s}$ . Duty cycle  $\leq 2\%$ .

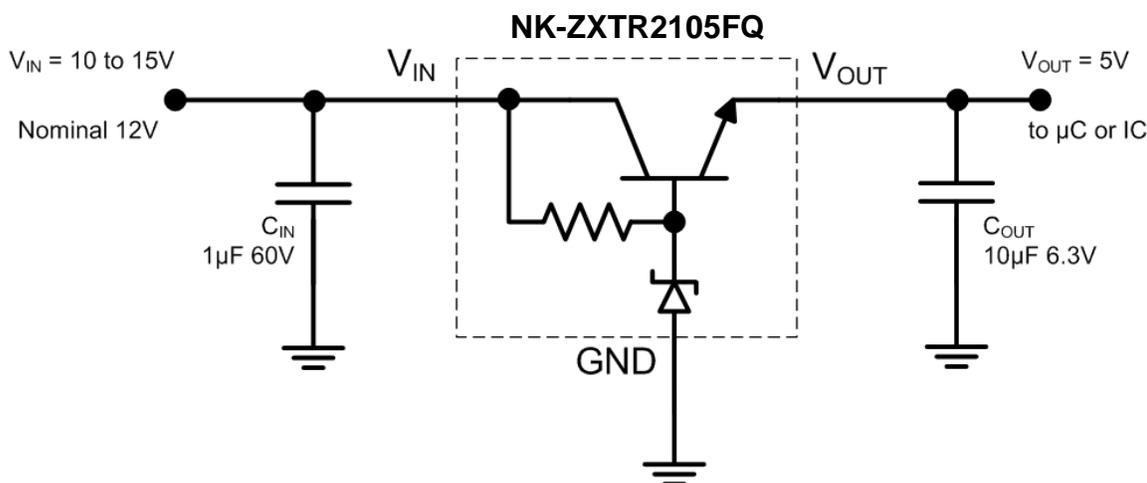
14. Line Regulation  $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 15\text{V}) - V_{OUT}(@V_{IN} = 10\text{V})$

$\Delta V_{OUT} = V_{OUT}(@V_{IN} = 60\text{V}) - V_{OUT}(@V_{IN} = 7\text{V})$

$\Delta V_{OUT} = V_{OUT}(@V_{IN} = 60\text{V}) - V_{OUT}(@V_{IN} = 10\text{V})$

15. Load Regulation  $\Delta V_{OUT} = V_{OUT}(@I_{OUT} = 20\text{mA}) - V_{OUT}(@I_{OUT} = 10\text{mA})$

$\Delta V_{OUT} = V_{OUT}(@I_{OUT} = 50\text{mA}) - V_{OUT}(@I_{OUT} = 0.1\text{mA})$

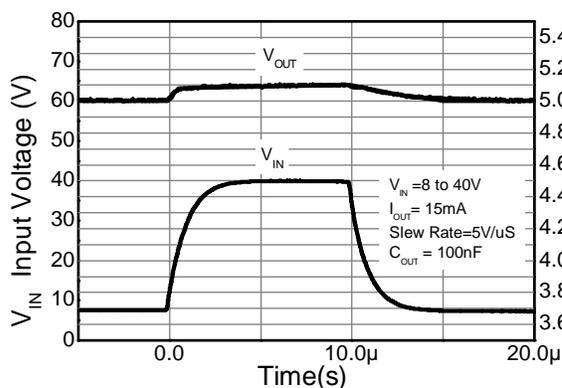
**Typical Application Circuit**


Example of a 5V regulated supply from a nominal 12V for powering a Controller IC.

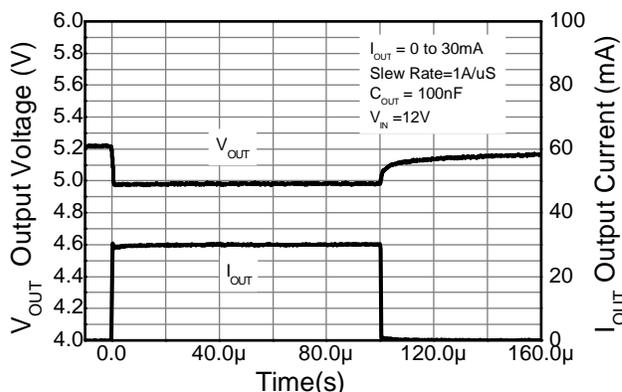
**Pin Functions**

Pin Name	Pin Function	Notes
$V_{IN}$	Input Supply	Input voltage can vary from $-0.3\text{V}$ to $60\text{V}$ with respect to GND; for $V_{OUT}$ regulated then $7\text{V} \leq V_{IN} \leq 60\text{V}$ . It is recommended to connect a $1\mu\text{F}$ capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
$V_{OUT}$	Voltage Output	Outputs a regulated $5\text{V}$ when $7\text{V} \leq V_{IN} \leq 60\text{V}$ . When $V_{IN} < 7\text{V}$ , then $V_{OUT}$ maximum = $V_{IN} - 1\text{V}$ . The pin can be pulled high to a maximum of $+10\text{V}$ with respect to GND, or $+5\text{V}$ with respect to $V_{IN}$ , whichever is lower. It is recommended to connect a $10\mu\text{F}$ capacitor to GND and a minimum of $10\mu\text{A}$ to be drawn from $V_{OUT}$ to maintain regulation.

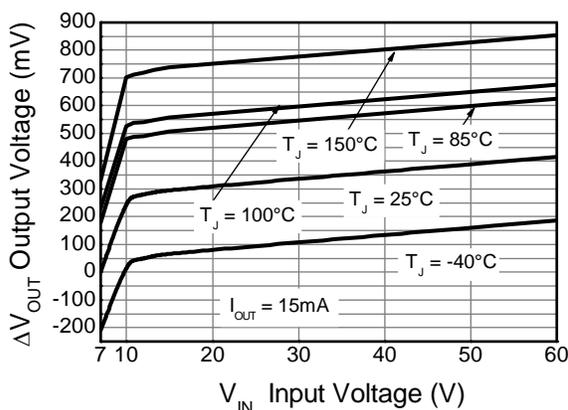
Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)



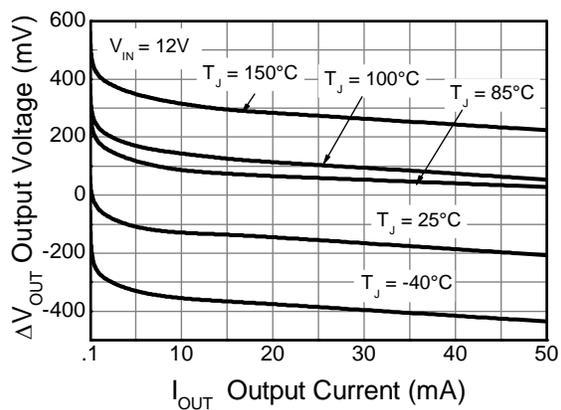
Line transient response



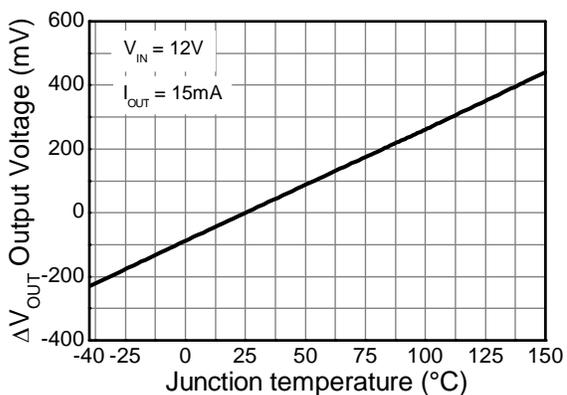
Load transient response



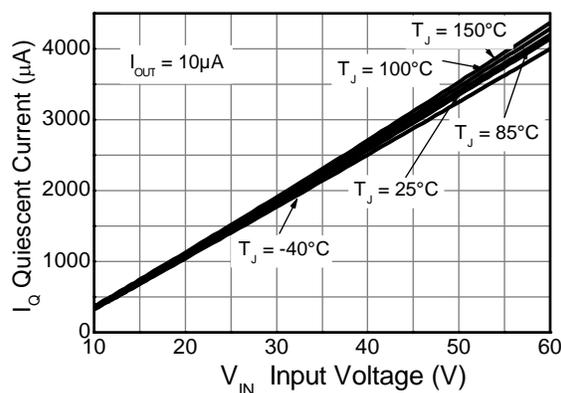
Line Regulation (Note 16)



Load Regulation (Note 17)



Temperature Coefficient (Note 18)

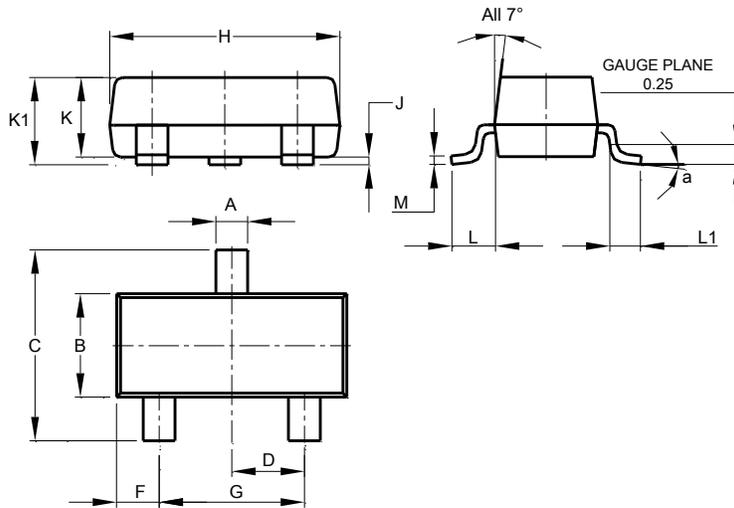


Quiescent Current

- Notes:
- 16. Line Regulation  $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 7V, I_{OUT} = 15mA, T_J = +25^\circ C)$ .
  - 17. Load Regulation  $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 12V, I_{OUT} = 0.1mA, T_J = +25^\circ C)$ .
  - 18. Temperature Coefficient  $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 12V, I_{OUT} = 15mA, T_J = +25^\circ C)$ .

## Package Outline Dimensions

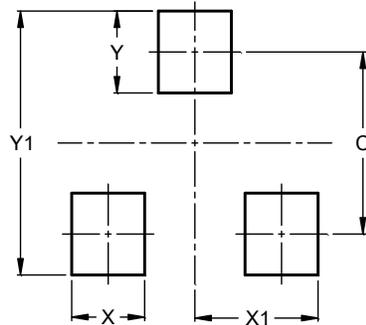
SOT23



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

## Suggested Pad Layout

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9