



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

0755-83047638  
ysbdt@szyoushang.cn  
www.szyoushang.cn



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## Description

The NK-ZXTR2108FQ monolithically integrates a transistor, zener diode and resistor to function as a linear regulator. The device regulates with an 8V nominal output at 15mA. It is designed for use in high voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT23 package, minimizing PCB area and reducing the number of components when compared with a multi-chip discrete solution.

This linear regulator is designed to meet the stringent requirements of automotive applications.

## Applications

Supply Voltage Regulation for:

- 24V to 8V Rails
- Other Customized Input Rails

## Features

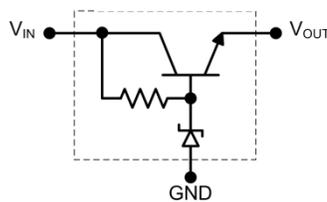
- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage – 10 to 60V (For Regulated Output Voltage)
- Output Voltage –  $8V \pm 10\%$
- Fully Integrated into a SOT23 package

## Mechanical Data

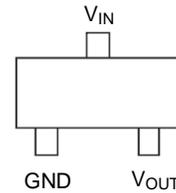
- Case: SOT23
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 <sup>Ⓔ</sup>
- Weight: 0.008 grams (Approximate)



Top View



Internal Device Schematic



Top View Pin-Out

Pin Name	Pin Function
V <sub>IN</sub>	Input Supply
GND	Power Ground
V <sub>OUT</sub>	Voltage Output

### Absolute Maximum Ratings

 (Voltage relative to GND, @ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	$V_{IN}$	-0.3 to 60	V
Continuous Input & Output Current	$I_{IN}, I_{OUT}$	320	mA
Peak Pulsed Input & Output Current	$I_{IM}, I_{OM}$	2	A
Maximum Voltage Applied to $V_{OUT}$	$V_{OUT(max)}$	Smaller of $V_{IN}+5V$ or 13V	V

### Maximum Current

 (@ $V_{IN} = 24V$ ) (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Continuous Output Current	$I_{OUT}$	40	mA
Pulsed Output Current	$I_{OM}$	2,000	mA
		375	

### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation	$P_D$	625	mW
		500	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	$^\circ\text{C/W}$
		250	
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	197	
Thermal Resistance, Junction to Case	$R_{\theta JC}$	17	
Maximum Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-65 to +150	$^\circ\text{C}$

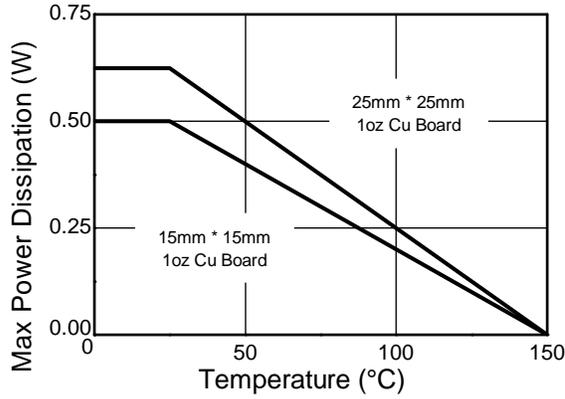
### ESD Ratings

 (Note 12)

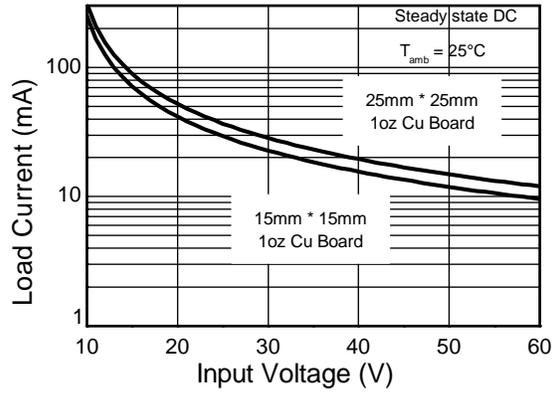
Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	C

- Notes:
- For a device mounted with the  $V_{IN}$  lead on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.
  - Same as Note 6, except mounted on 15mm x 15mm 1oz copper.
  - Same as Note 6, whilst operating at  $V_{IN}=24V$ . Refer to Safe Operating Area for other Input Voltages.
  - Same as Note 6, except measured with a single pulse width = 100 $\mu\text{s}$  and  $V_{IN}=24V$ .
  - Same as Note 6, except measured with a single pulse width = 10ms and  $V_{IN}=24V$ .
  - $R_{\theta JL}$  = Thermal resistance from junction to solder-point (at the end of the  $V_{IN}$  lead).  
 $R_{\theta JC}$  = Thermal resistance from junction to the top of case.
  - Refer to JEDEC specification JESD22-A114 and JESD22-A115.

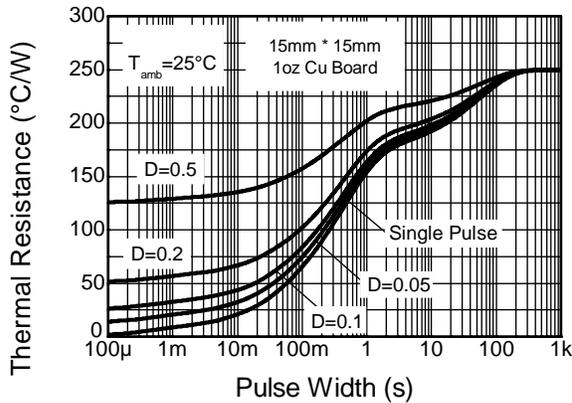
**Thermal Characteristics and Derating Information**



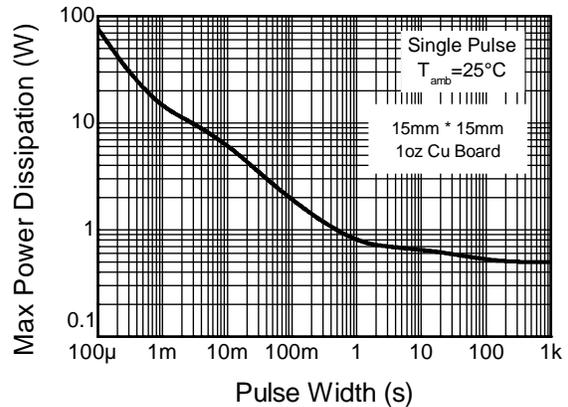
**Derating Curve**



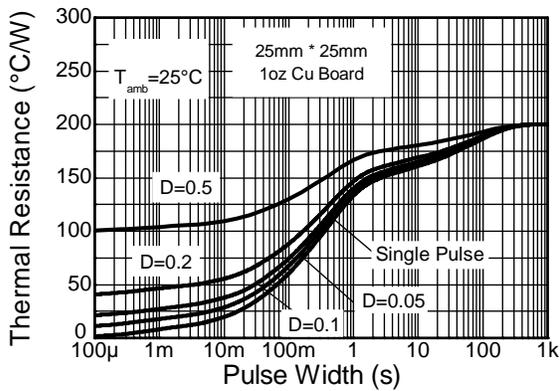
**Safe Operating Area**



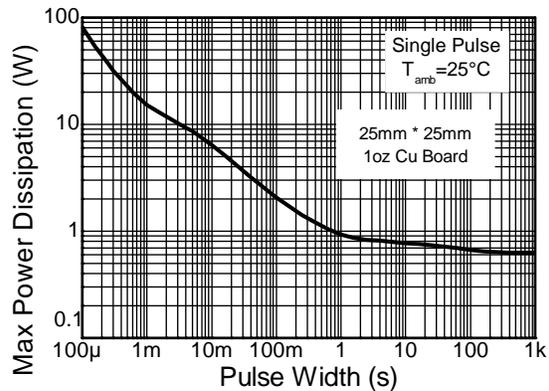
**Transient Thermal Impedance**



**Pulse Power Dissipation**



**Transient Thermal Impedance**

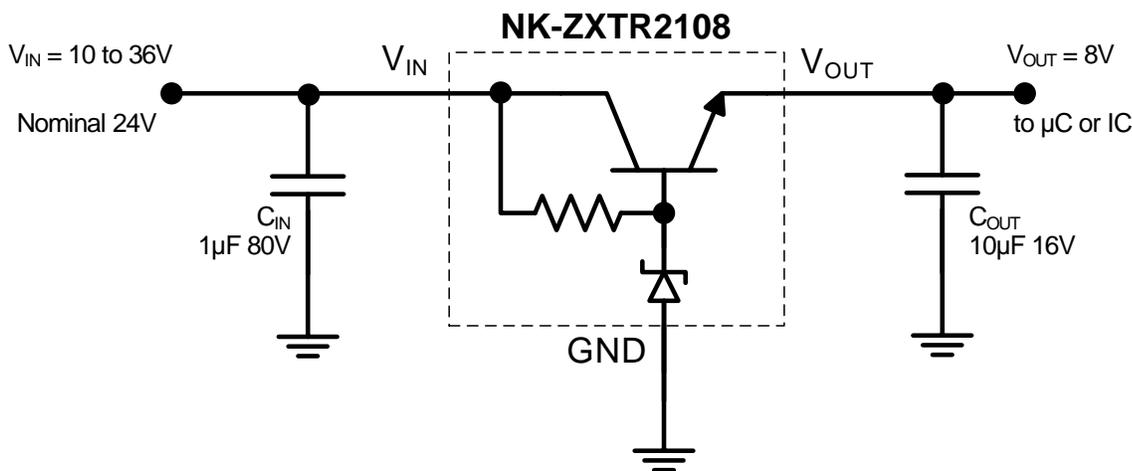


**Pulse Power Dissipation**

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Output Voltage (Note 13)	$V_{OUT}$	7.2	8	8.8	V	$V_{IN} = 24\text{V}$ , $I_{OUT} = 15\text{mA}$
Line Regulation (Notes 13 & 14)	$\Delta V_{OUT}$	—	15	50	mV	$V_{IN} = 18$ to $24\text{V}$ , $I_{OUT} = 15\text{mA}$
		—	110	—		$V_{IN} = 12$ to $60\text{V}$ , $I_{OUT} = 15\text{mA}$
		—	120	—		$V_{IN} = 10$ to $60\text{V}$ , $I_{OUT} = 15\text{mA}$
Temperature Coefficient	$\Delta V_{OUT}/\Delta T$	—	7.2	—	mV/ $^\circ\text{C}$	$T_J = -40^\circ\text{C}$ to $+125^\circ\text{C}$ $V_{IN} = 24\text{V}$ , $I_{OUT} = 15\text{mA}$
Load Regulation (Notes 13 & 15)	$\Delta V_{OUT}$	—	-16 -150	-50 -300	mV	$I_{OUT} = 10$ to $20\text{mA}$ , $V_{IN} = 24\text{V}$ $I_{OUT} = 0.1$ to $50\text{mA}$ , $V_{IN} = 24\text{V}$
Minimum Value of Input Voltage Required to Maintain Line Regulation	$V_{IN(MIN)}$	10	—	—	V	—
Quiescent Current	$I_Q$	—	260	500	$\mu\text{A}$	$V_{IN} = 12\text{V}$ , $I_{OUT} = 10\mu\text{A}$
		—	3,700	6,000		$V_{IN} = 60\text{V}$ , $I_{OUT} = 10\mu\text{A}$
Power Supply Rejection Ratio	$\Delta V_{IN}/\Delta V_{out}$	—	45	—	dB	$C_{OUT} = 100\text{nF}$ , $I_{OUT} = 15\text{mA}$ , $V_{OUT} = 8\text{V}$ , $V_{IN} = 10$ to $60\text{V}$ , $f = 100\text{Hz}$

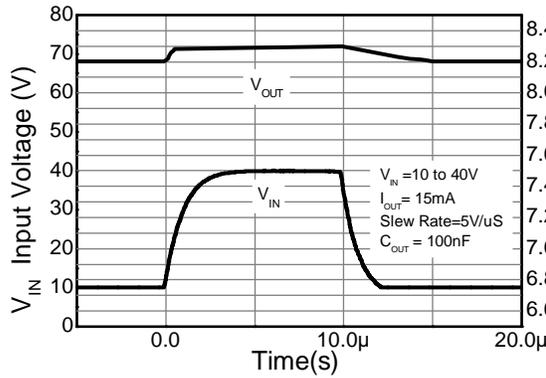
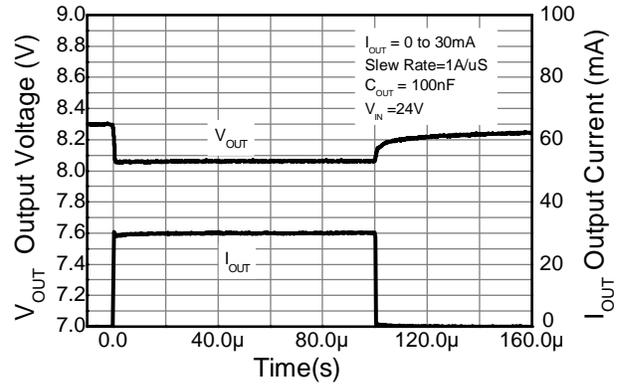
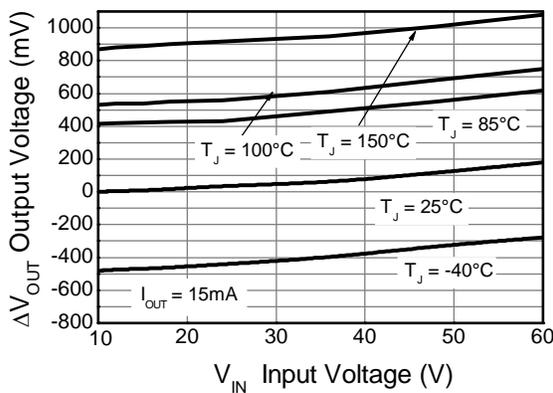
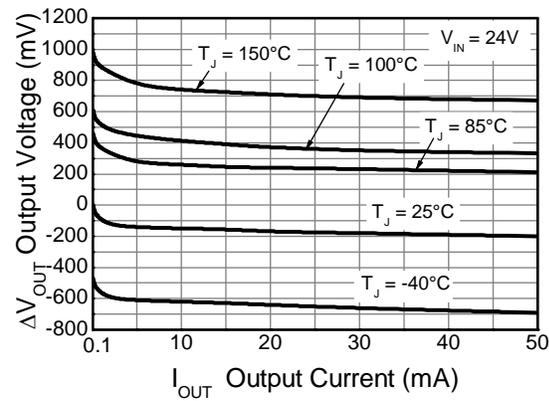
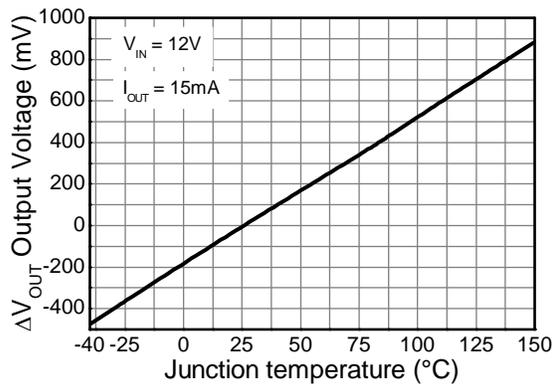
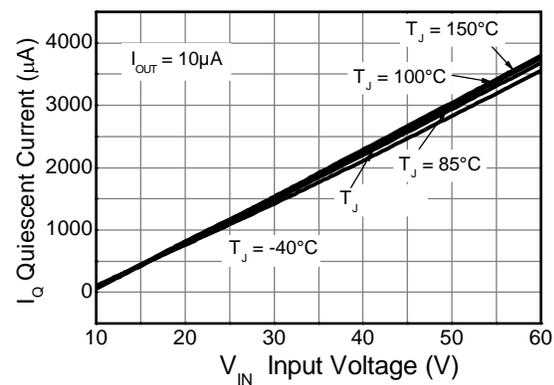
- Notes:
- 13. Measured under pulsed conditions. Pulse width  $\leq 300\mu\text{s}$ . Duty cycle  $\leq 2\%$ .
  - 14. Line regulation:  $\Delta V_{OUT} = V_{OUT}(@V_{IN}=24\text{V}) - V_{OUT}(@V_{IN} = 18\text{V})$   
 $\Delta V_{OUT} = V_{OUT}(@V_{IN}=60\text{V}) - V_{OUT}(@V_{IN} = 10\text{V})$   
 $\Delta V_{OUT} = V_{OUT}(@V_{IN}=60\text{V}) - V_{OUT}(@V_{IN} = 12\text{V})$
  - 15. Load regulation:  $\Delta V_{OUT} = V_{OUT}(@I_{OUT} = 20\text{mA}) - V_{OUT}(@I_{OUT} = 10\text{mA})$   
 $\Delta V_{OUT} = V_{OUT}(@I_{OUT} = 50\text{mA}) - V_{OUT}(@I_{OUT} = 0.1\text{mA})$

**Typical Application Circuit**


Example of a 8V regulated supply from a nominal 24V for powering a Controller IC.

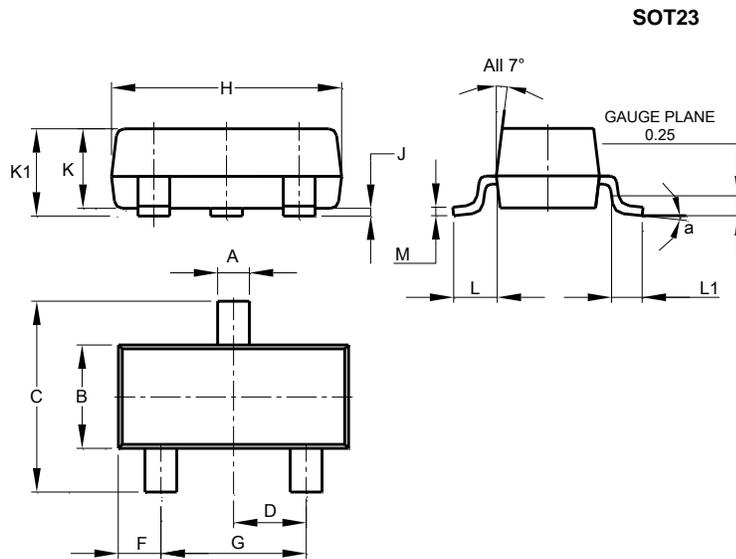
**Pin Function**

Pin Name	Pin Function	Notes
$V_{IN}$	Input Supply	Input voltage can vary from $-0.3\text{V}$ to $60\text{V}$ with respect to GND; for $V_{OUT}$ regulated then $10\text{V} \leq V_{IN} \leq 60\text{V}$ . It is recommended to connect a $1\mu\text{F}$ capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
$V_{OUT}$	Voltage Output	Outputs a regulated 8V when $10\text{V} \leq V_{IN} \leq 60\text{V}$ . When $V_{IN} < 10\text{V}$ , then $V_{OUT}$ maximum = $V_{IN} - 1\text{V}$ . The pin can be pulled high to a maximum of $+13\text{V}$ with respect to GND, or $+5\text{V}$ with respect to $V_{IN}$ , whichever is lower. It is recommended to connect a $10\mu\text{F}$ capacitor to GND and a minimum of $10\mu\text{A}$ to be drawn from $V_{OUT}$ to maintain regulation.

**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

**Line transient response**

**Load transient response**

**Line Regulation (Note 16)**

**Load Regulation (Note 17)**

**Temperature Coefficient (Note 18)**

**Quiescent Current**

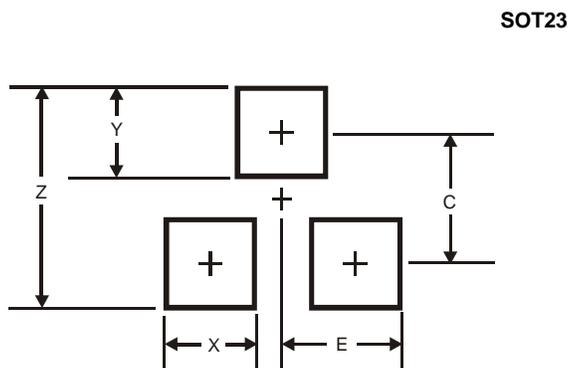
- Notes:
16. Line Regulation  $\Delta V_{OUT} = V_{OUT} - V_{OUT}$  (@  $V_{IN} = 10\text{V}$ ,  $I_{OUT} = 15\text{mA}$ ,  $T_J = +25^\circ\text{C}$ ).
  17. Load Regulation  $\Delta V_{OUT} = V_{OUT} - V_{OUT}$  (@  $V_{IN} = 24\text{V}$ ,  $I_{OUT} = 0.1\text{mA}$ ,  $T_J = +25^\circ\text{C}$ ).
  18. Temperature Coefficient  $\Delta V_{OUT} = V_{OUT} - V_{OUT}$  (@  $V_{IN} = 24\text{V}$ ,  $I_{OUT} = 15\text{mA}$ ,  $T_J = +25^\circ\text{C}$ ).

## Package Outline Dimensions



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	8°		
All Dimensions in mm			

## Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35