



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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企业微信二维码



企业QQ二维码

Product Summary

Device	BV _{DSS}	R _{DS(ON)} MAX	I _D MAX T _A = +25°C
Q1 N-Channel	25V	0.45Ω @ V _{GS} = 4.5V	0.68A
		0.60Ω @ V _{GS} = 2.7V	
		0.73Ω @ V _{GS} = 1.8V	
Q2 P-Channel	-25V	1.1Ω @ V _{GS} = -4.5V	-0.46A
		1.5Ω @ V _{GS} = -2.7V	
		2.2Ω @ V _{GS} = -1.8V	

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- DC-DC Converters
- Power Management Functions

Features and Benefits

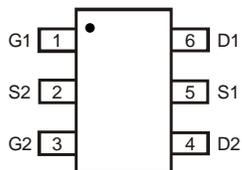
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage

Mechanical Data

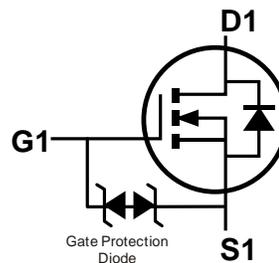
- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 
- Weight: 0.013 grams (Approximate)



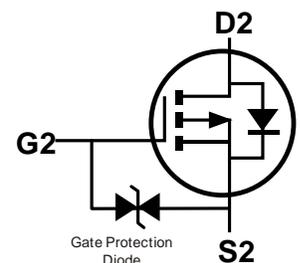
Top View



Top View



Q1 N-Channel



Q2 P-Channel

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Q1 N-CHANNEL	Q2 P-CHANNEL	Unit
Drain-Source Voltage			V_{DSS}	25	-25	V
Gate-Source Voltage			V_{GSS}	± 8	± 8	V
Continuous Drain Current (Note 6)	Steady State	$T_A = +25^\circ\text{C}$	I_D	0.68	-0.46	A
N-Channel: $V_{GS} = 4.5\text{V}$ P-Channel: $V_{GS} = -4.5\text{V}$						
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	0.3	-0.3	A
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)			I_{DM}	3	-2.5	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	Steady State	P_D	0.7	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	180	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	Steady State	P_D	0.9	W
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	140	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	60	$^\circ\text{C/W}$
Operating and Storage Temperature Range		T_{L}, T_{STG}	-55 to +175	$^\circ\text{C}$

Electrical Characteristics N-CHANNEL – Q1 (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

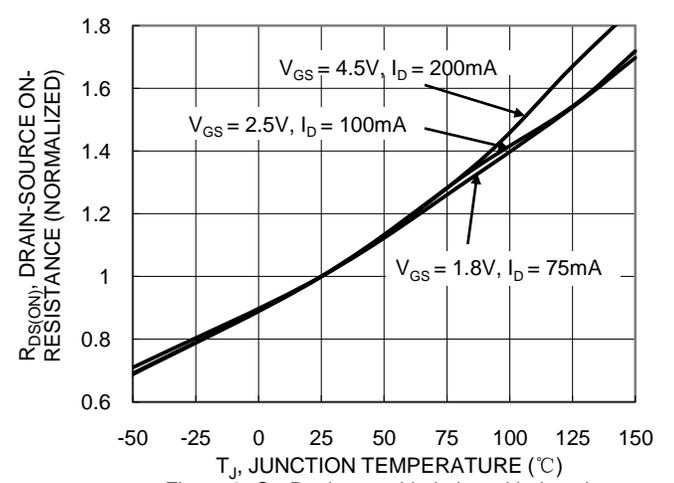
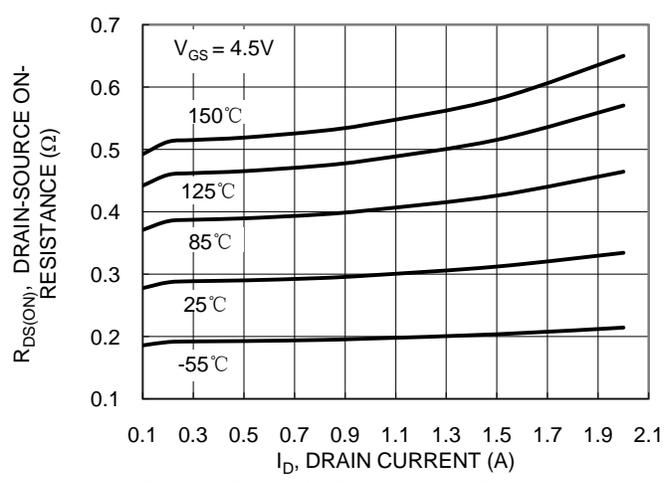
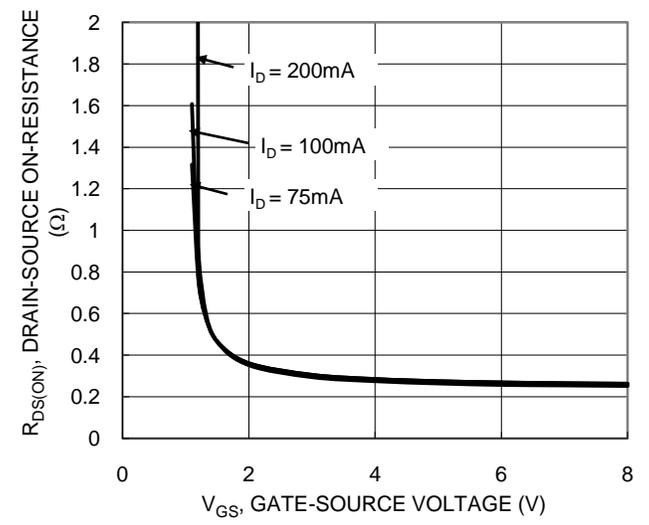
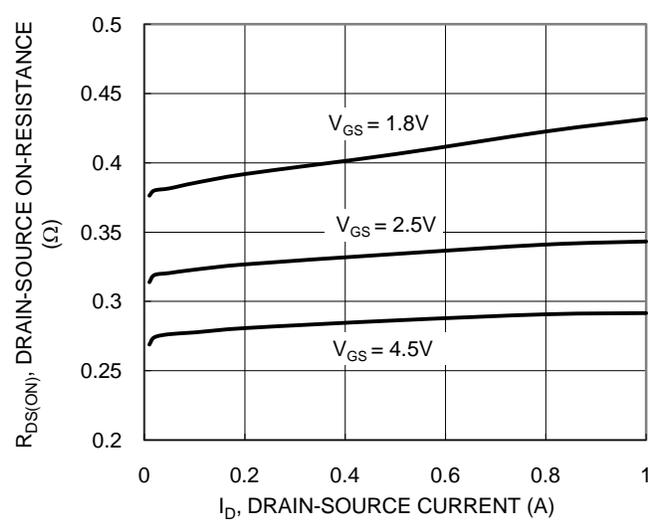
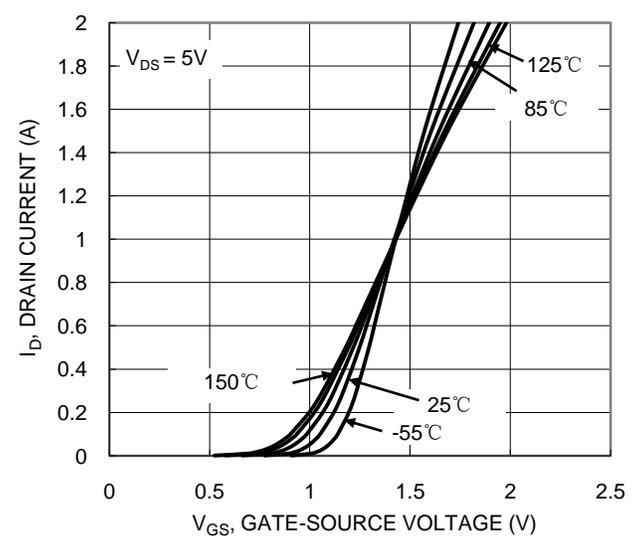
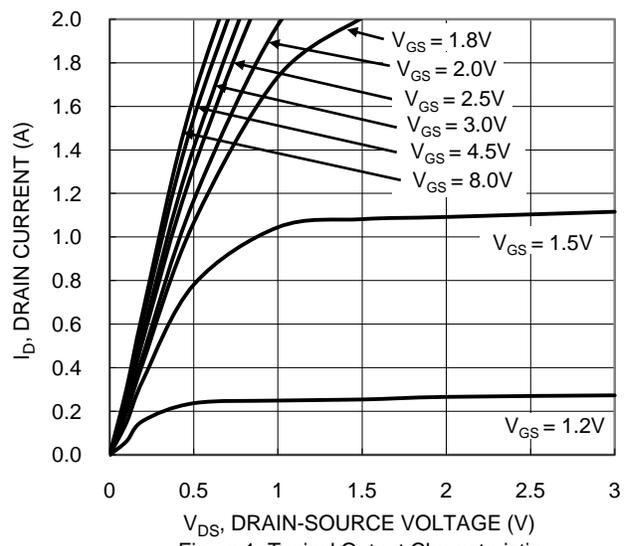
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	25	-	-	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current $T_J = +25^\circ\text{C}$	I_{DSS}	-	-	1.0	μA	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	-	-	± 10	μA	$V_{GS} = \pm 8\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.45	0.7	1.1	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	-	0.28	0.45	Ω	$V_{GS} = 4.5\text{V}, I_D = 0.5\text{A}$
		-	0.33	0.60		$V_{GS} = 2.7\text{V}, I_D = 0.25\text{A}$
		-	0.39	0.73		$V_{GS} = 1.8\text{V}, I_D = 0.1\text{A}$
		-	0.75	1.2		$V_{GS} = 0\text{V}, I_S = 0.5\text{A}$
Diode Forward Voltage	V_{SD}	-	0.75	1.2	V	$V_{GS} = 0\text{V}, I_S = 0.5\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{ISS}	-	50	-	pF	$V_{DS} = 10\text{V}, V_{GS} = 0\text{V},$ $f = 1.0\text{MHz}$
Output Capacitance	C_{OSS}	-	28	-	pF	
Reverse Transfer Capacitance	C_{RSS}	-	9	-	pF	
Gate Resistance	R_g	-	64	-	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge	Q_g	-	1.64	-	nC	$V_{DS} = 5\text{V}, I_D = 0.5\text{A},$ $V_{GS} = 4.5\text{V}$
Gate-Source Charge	Q_{gs}	-	0.38	-	nC	
Gate-Drain Charge	Q_{gd}	-	0.45	-	nC	
Turn-On Delay Time	$t_{D(ON)}$	-	3	-	ns	$V_{GS} = 4.5\text{V}, V_{DS} = 6\text{V},$ $R_g = 50\Omega, I_D = 0.5\text{A}$
Turn-On Rise Time	t_R	-	8	-	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	-	17	-	ns	
Turn-Off Fall Time	t_F	-	13	-	ns	

Electrical Characteristics P-CHANNEL – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-25	-	-	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	-1.0	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	μA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	-0.8	-1.1	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	0.65	1.1	Ω	V _{GS} = -4.5V, I _D = -0.5A
		-	0.80	1.5		V _{GS} = -2.7V, I _D = -0.25A
		-	1.0	2.2		V _{GS} = -1.8V, I _D = -0.1A
Diode Forward Voltage	V _{SD}	-	-0.7	-1.2	V	V _{GS} = 0V, I _S = -0.5A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	63	-	pF	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	34	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	10	-	pF	
Gate Resistance	R _g	-	178	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	-	1.1	-	nC	V _{DS} = -5V, I _D = -0.25A , V _{GS} = -4.5V
Gate-Source Charge	Q _{gs}	-	0.32	-	nC	
Gate-Drain Charge	Q _{gd}	-	0.25	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	7	-	ns	V _{GS} = -4.5V, V _{DS} = -6V, R _g = 50Ω, I _D = -0.5A
Turn-On Rise Time	t _R	-	9	-	ns	
Turn-Off Delay Time	t _{D(OFF)}	-	55	-	ns	
Turn-Off Fall Time	t _F	-	35	-	ns	

- Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.

Typical Characteristics - N-CHANNEL



Typical Characteristics - N-CHANNEL (Cont.)

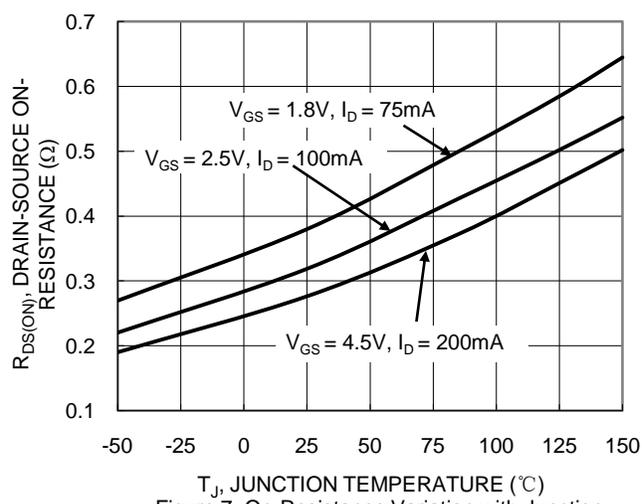


Figure 7. On-Resistance Variation with Junction Temperature

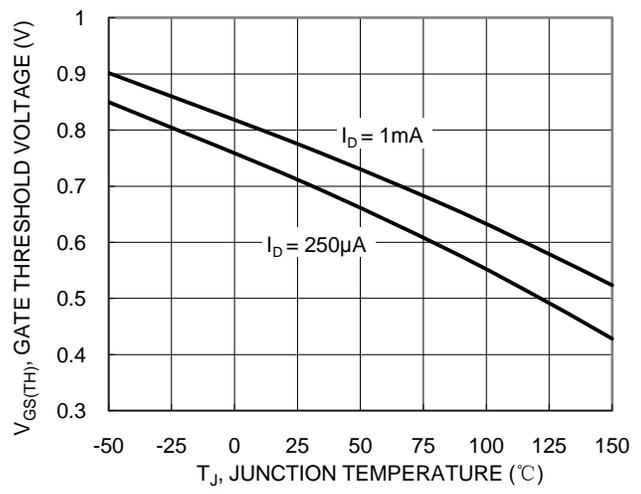


Figure 8. Gate Threshold Variation vs. Junction Temperature

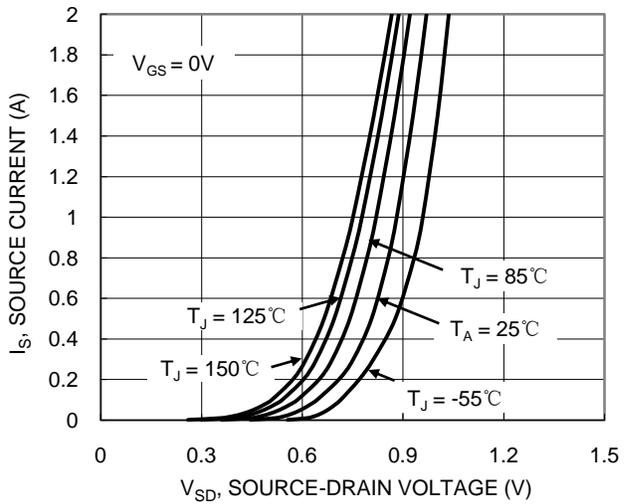


Figure 9. Diode Forward Voltage vs. Current

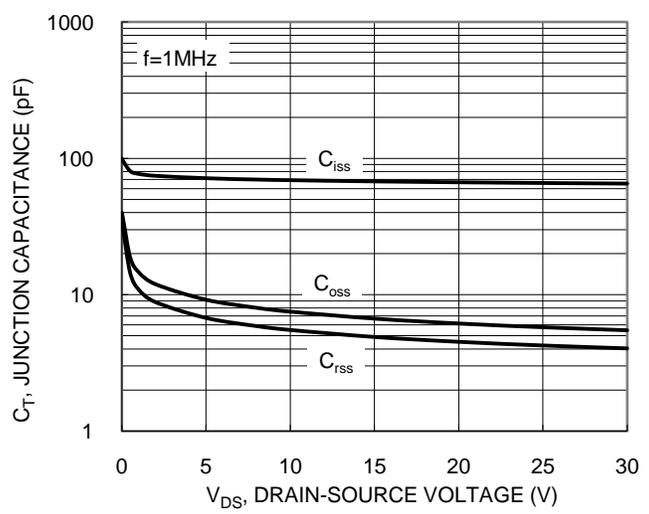


Figure 10. Typical Junction Capacitance

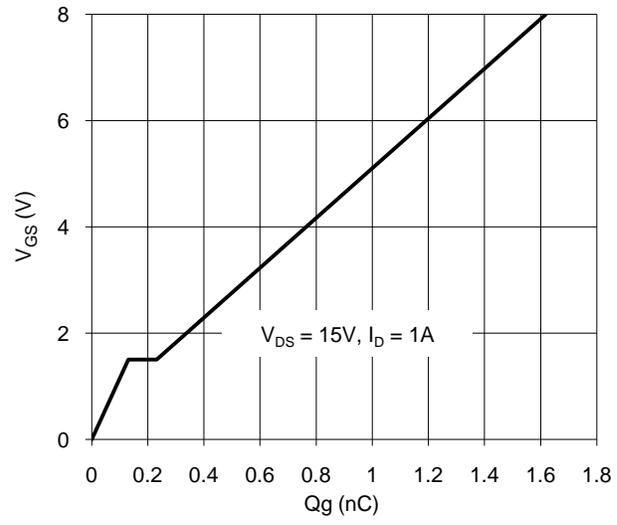


Figure 11. Gate Charge

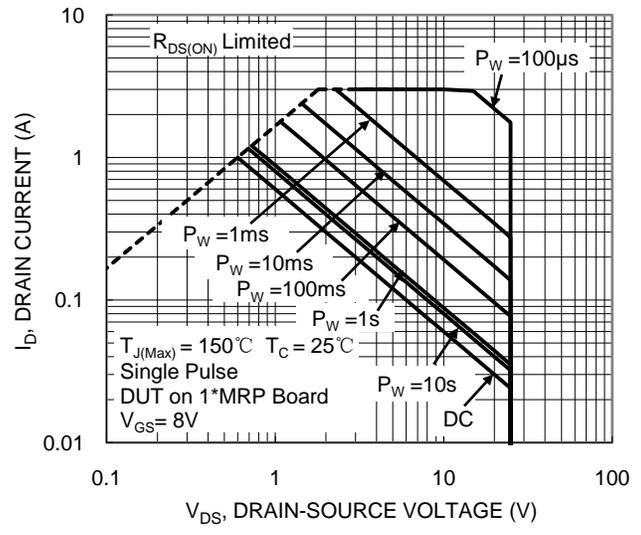


Figure 12. SOA, Safe Operation Area

Typical Characteristics - P-CHANNEL

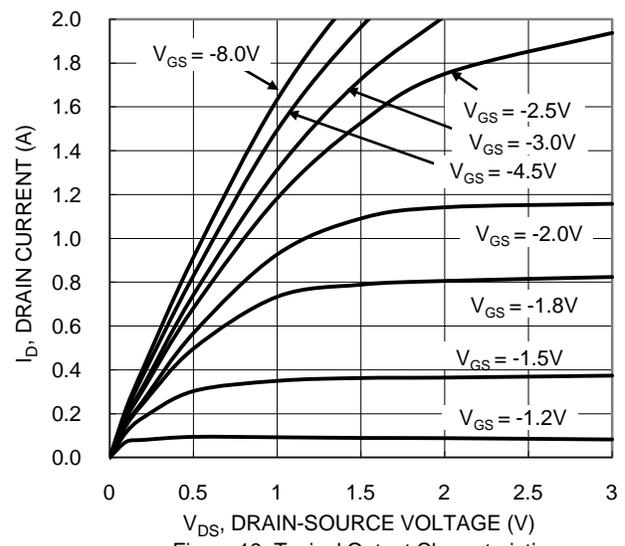


Figure 13. Typical Output Characteristic

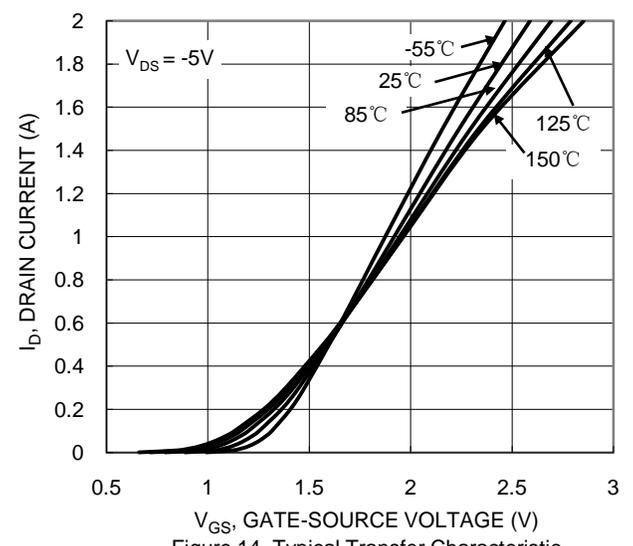


Figure 14. Typical Transfer Characteristic

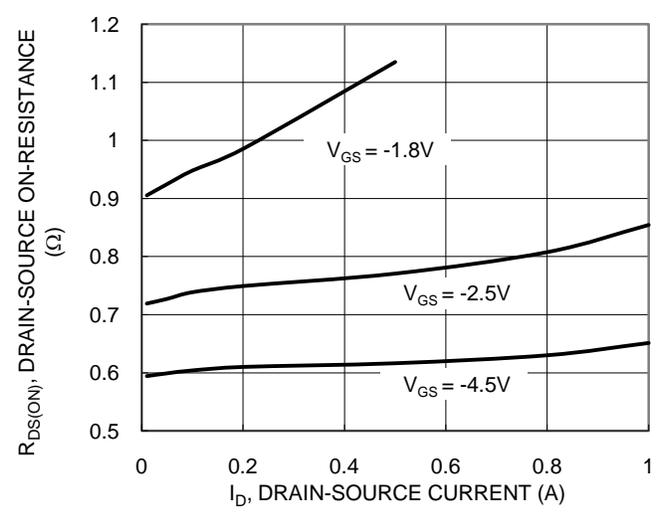


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage

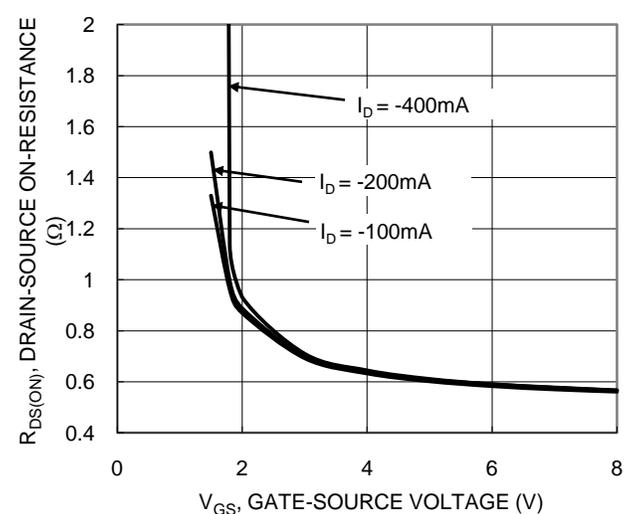


Figure 16. Typical Transfer Characteristic

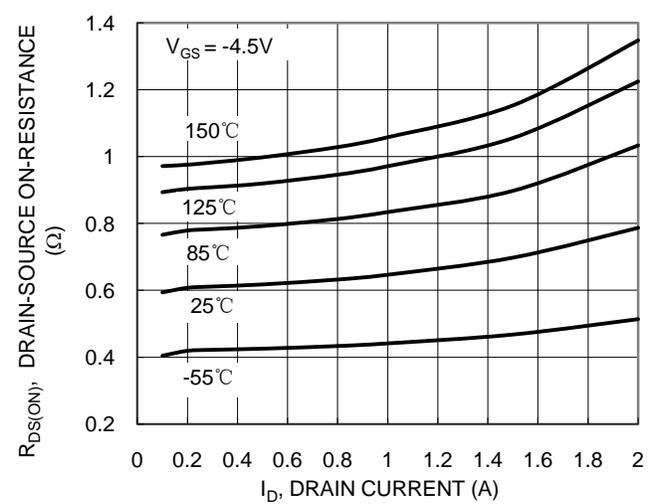


Figure 17. Typical On-Resistance vs. Drain Current and Junction Temperature

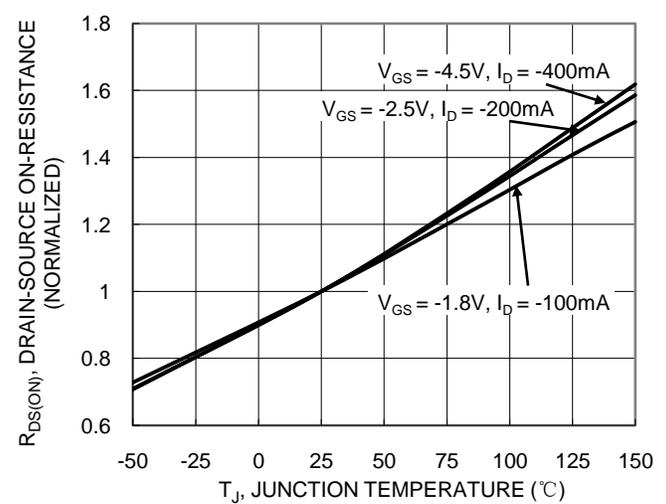


Figure 18. On-Resistance Variation with Junction Temperature

Typical Characteristics - P-CHANNEL (Cont.)

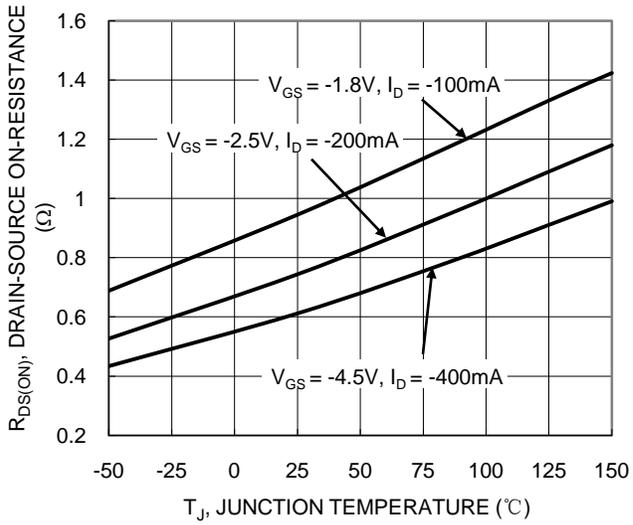


Figure 19. On-Resistance Variation with Junction Temperature

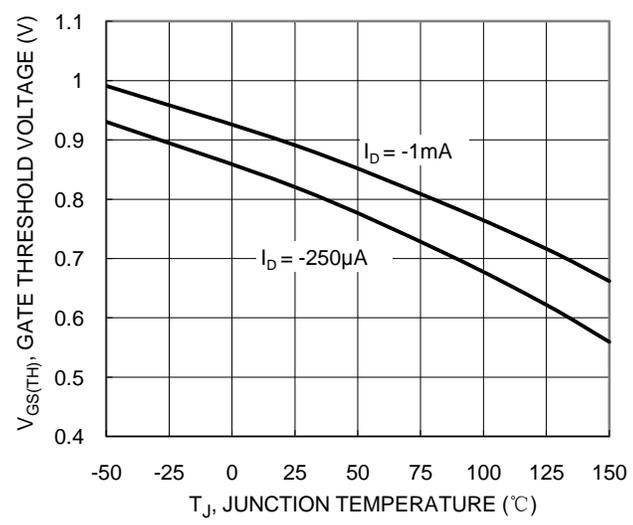


Figure 20. Gate Threshold Variation vs. Junction Temperature

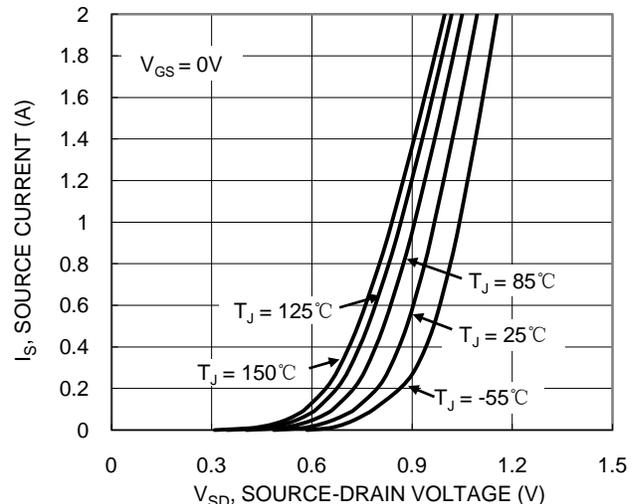


Figure 21. Diode Forward Voltage vs. Current

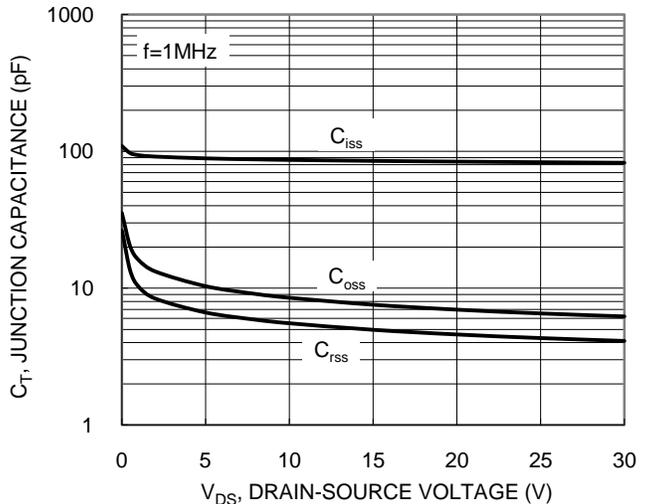


Figure 22. Typical Junction Capacitance

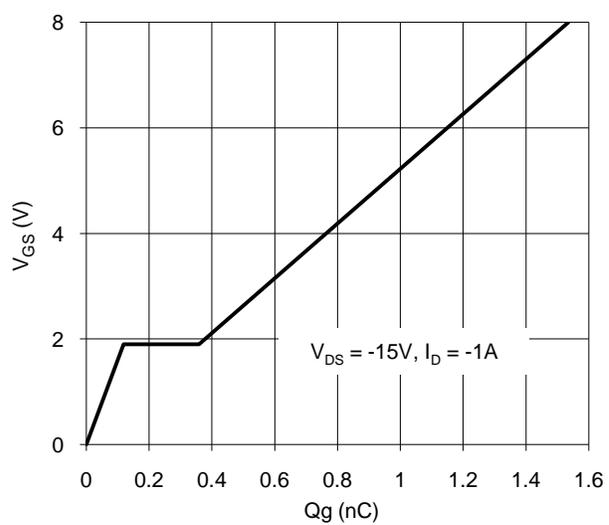


Figure 23. Gate Charge

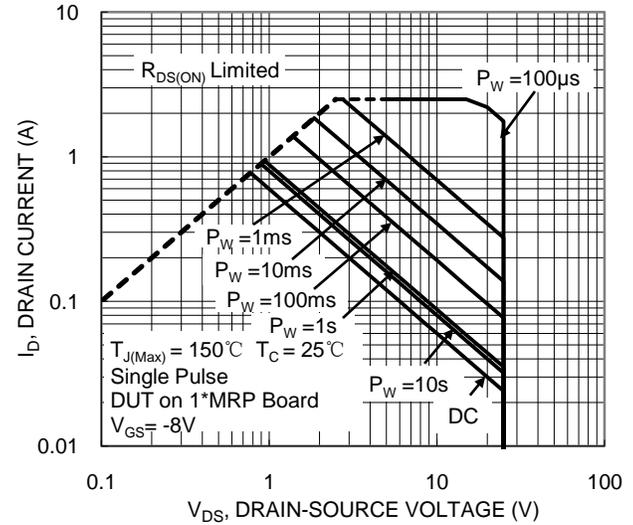


Figure 24. SOA, Safe Operation Area

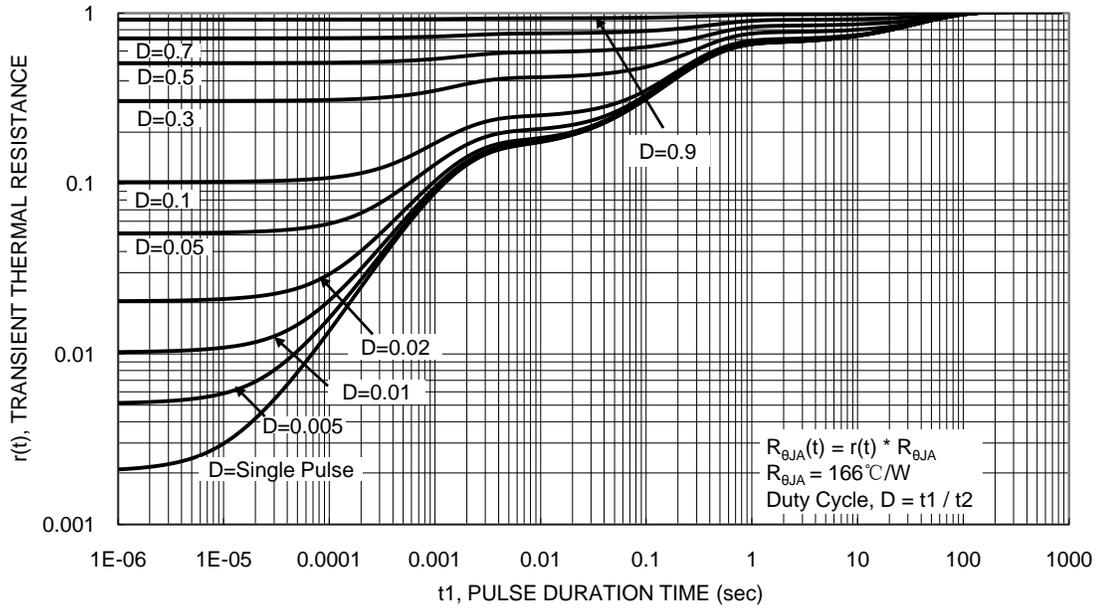
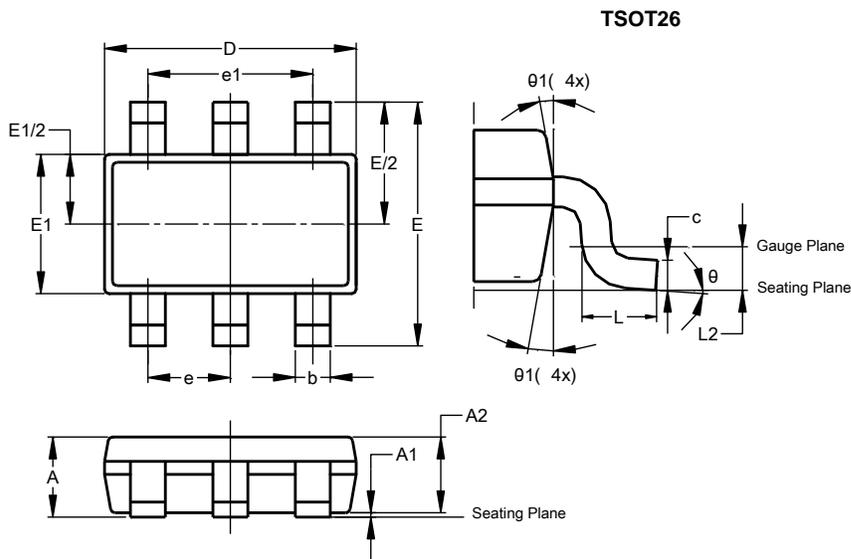


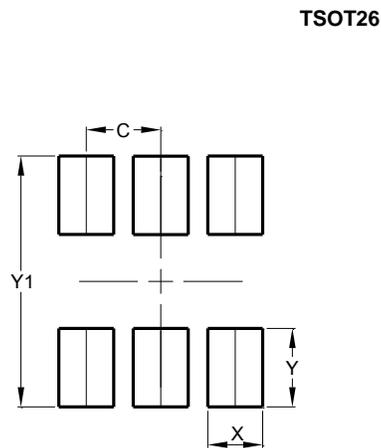
Figure 25. Transient Thermal Resistance

Package Outline Dimensions



TSOT26			
Dim	Min	Max	Typ
A	–	1.00	–
A1	0.010	0.100	–
A2	0.840	0.900	–
D	2.800	3.000	2.900
E	2.800 BSC		
E1	1.500	1.700	1.600
b	0.300	0.450	–
c	0.120	0.200	–
e	0.950 BSC		
e1	1.900 BSC		
L	0.30	0.50	–
L2	0.250 BSC		
θ	0°	8°	4°
θ_1	4°	12°	–
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
C	0.950
X	0.700
Y	1.000
Y1	3.199