



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
20V	25mΩ @ V _{GS} = 4.5V	4.2A
	29mΩ @ V _{GS} = 2.5V	4.0A
	37mΩ @ V _{GS} = 1.8V	3.4A

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- Power-management functions
- DC-DC converters

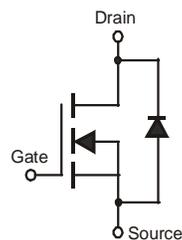
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

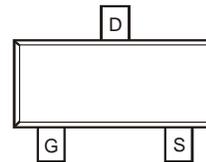
SOT23



Top View



Internal Schematic



Top View

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DS}	20	V
Gate-Source Voltage			V_{GS}	± 8	V
Continuous Drain Current (Note 5)	Steady State	$T_A = +25^\circ\text{C}$	I_D	4.2	A
		$T_A = +70^\circ\text{C}$		3.2	
Pulsed Drain Current (Note 6)			I_{DM}	30	A

Thermal Characteristics

Characteristic			Symbol	Value	Unit
Power Dissipation (Note 5)			P_D	0.78	W
Thermal Resistance, Junction to Ambient @ $T_A = +25^\circ\text{C}$			$R_{\theta JA}$	162	$^\circ\text{C/W}$
Operating and Storage Temperature Range			T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DS}	20	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1.0	μA	$T_J = +25^\circ\text{C}, V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 8\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.5	—	0.9	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	19	25	m Ω	$V_{GS} = 4.5\text{V}, I_D = 8.2\text{A}$
			22	29		$V_{GS} = 2.5\text{V}, I_D = 3.3\text{A}$
			28	37		$V_{GS} = 1.8\text{V}, I_D = 2.0\text{A}$
Forward Transfer Admittance	$ Y_{fs} $	—	7	—	S	$V_{DS} = 10\text{V}, I_D = 4\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	829.9	—	pF	$V_{DS} = 10\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	85.3	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	81.2	—	pF	
Total Gate Charge	Q_g	—	9.6	—	nC	$V_{GS} = 4.5\text{V}, V_{DS} = 10\text{V}, I_D = 8.2\text{A}$
Gate-Source Charge	Q_{gs}	—	1.5	—	nC	
Gate-Drain Charge	Q_{gd}	—	3.5	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	8.1	—	ns	$V_{DD} = 10\text{V}, V_{GS} = 4.5\text{V},$ $R_L = 10\Omega, R_G = 6\Omega, I_D = 1\text{A}$
Turn-On Rise Time	t_R	—	8.3	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	40.1	—	ns	
Turn-Off Fall Time	t_F	—	9.6	—	ns	

- Notes:
- Device mounted on FR-4 PCB with 2oz. copper and test pulse width $t \leq 10\text{s}$.
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

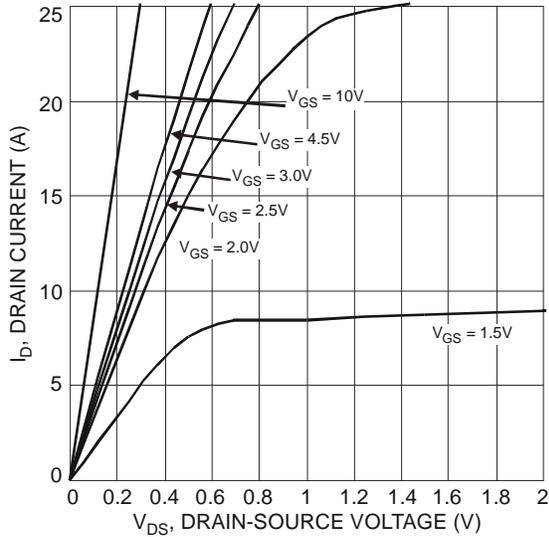


Fig. 1 Typical Output Characteristic

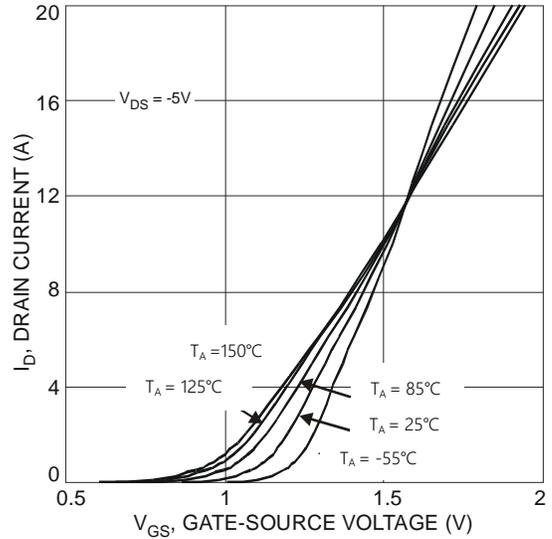


Fig. 2 Typical Transfer Characteristic

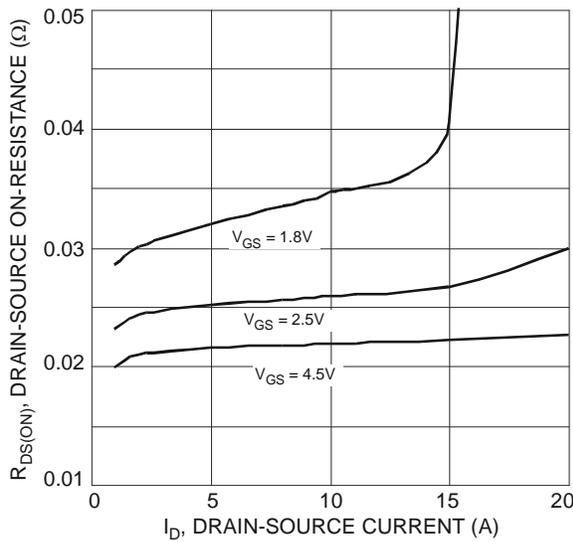


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

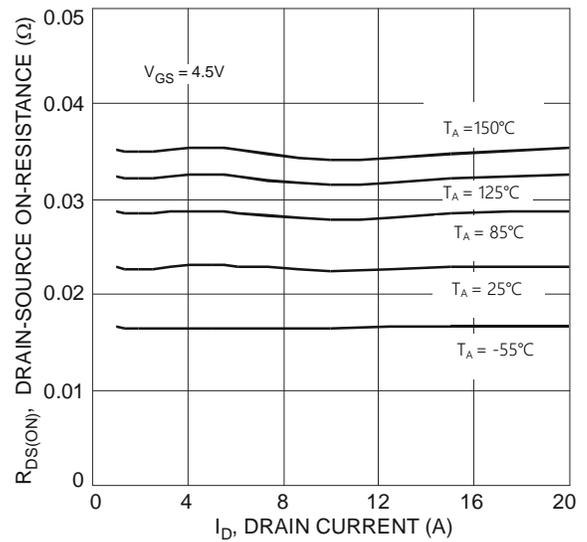


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

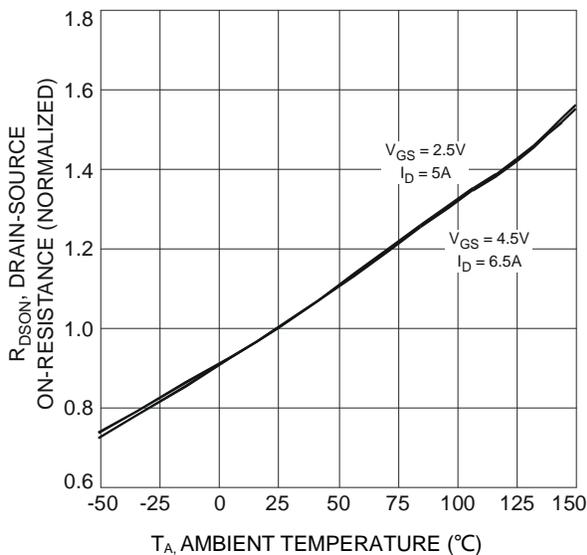


Fig. 5 On-Resistance Variation with Temperature

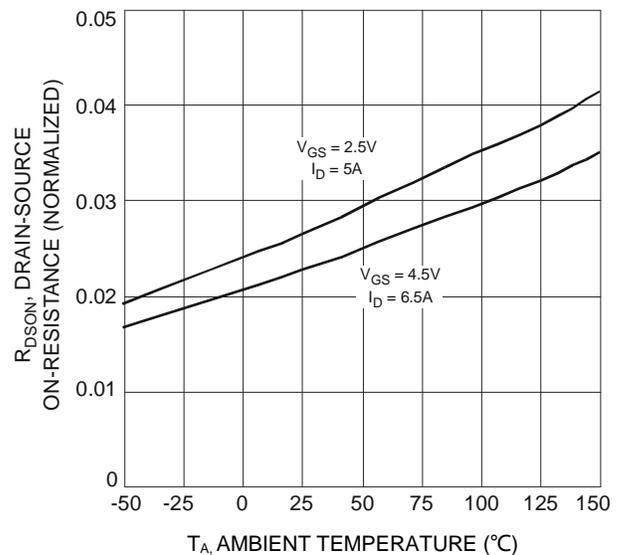


Fig. 6 On-Resistance Variation with Temperature

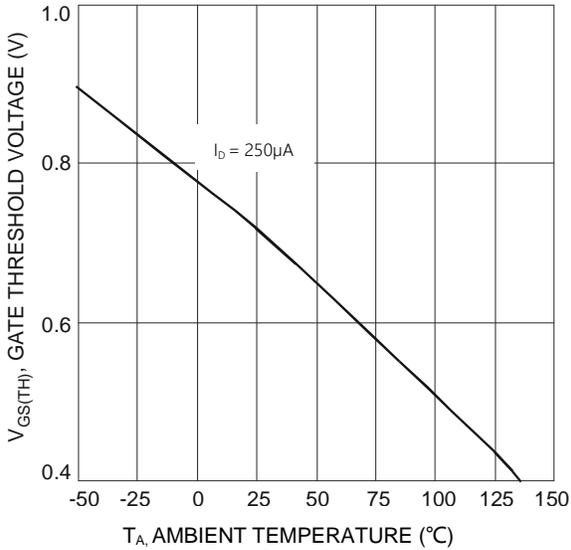


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

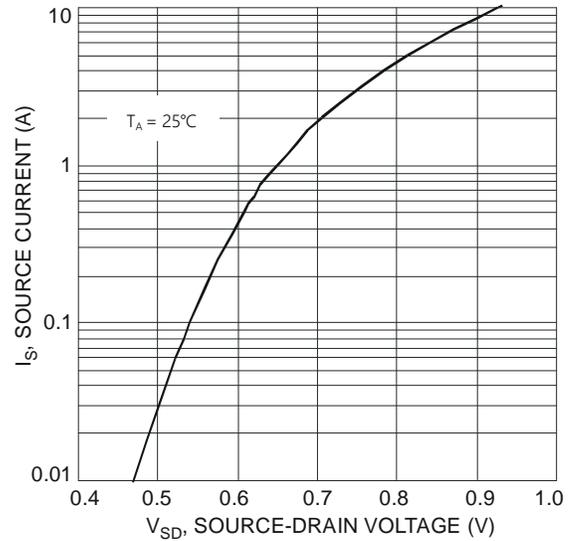


Fig. 8 Diode Forward Voltage vs. Current

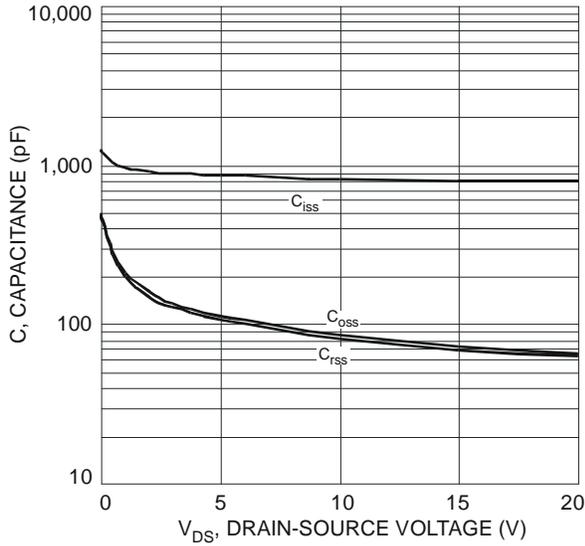


Fig. 9 Typical Total Capacitance

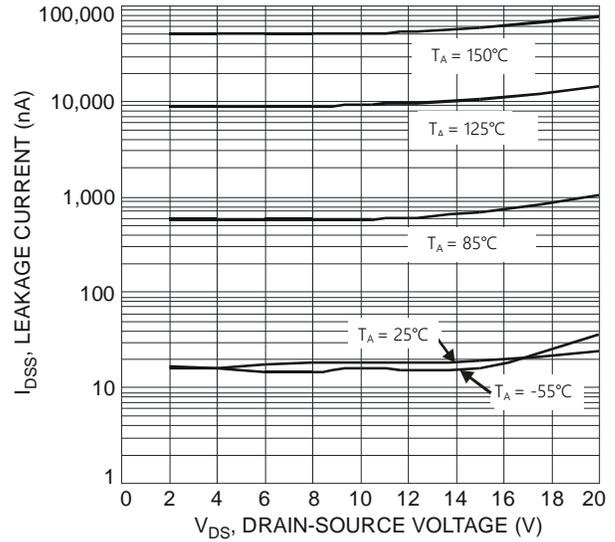


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

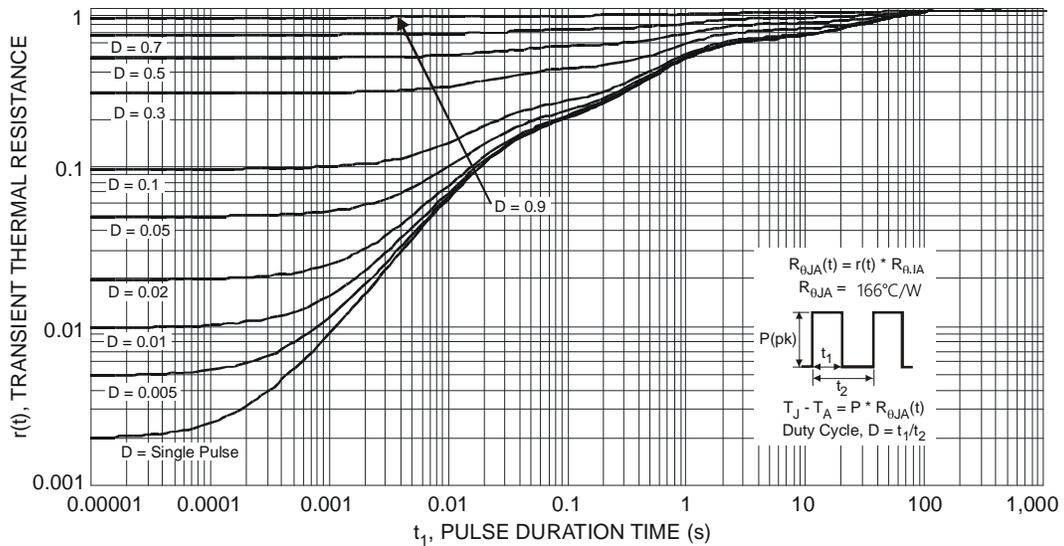
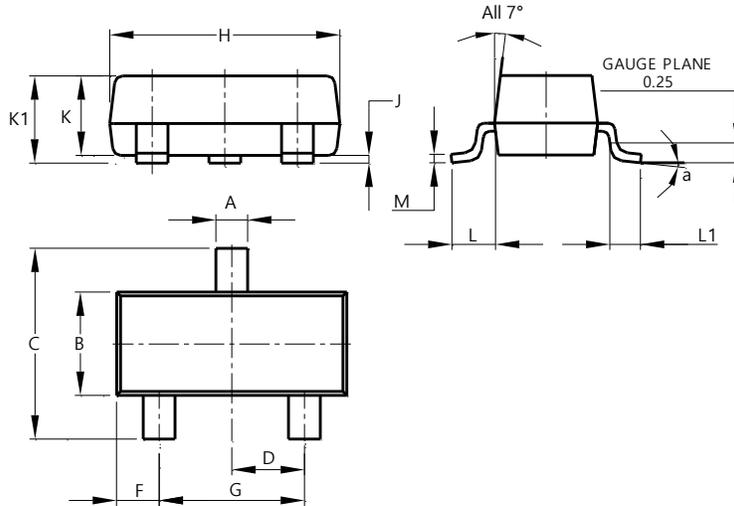


Fig. 11 Transient Thermal Response

Package Outline Dimensions

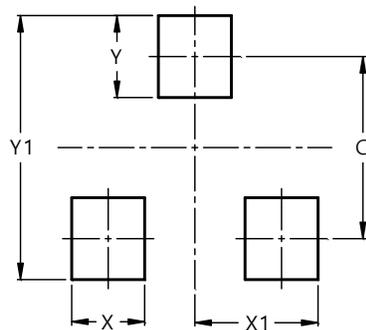
SOT23



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9