



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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企业微信二维码



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Product Summary

BV _{bss}	R _{DS(ON)} MAX	I _D T _A = +25°C
-20V	0.75Ω @ V _{GS} = -4.5V	-0.85A
	1.05Ω @ V _{GS} = -2.5V	-0.7A
	1.5Ω @ V _{GS} = -1.8V	-0.6A

Features and Benefits

- Dual P-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed

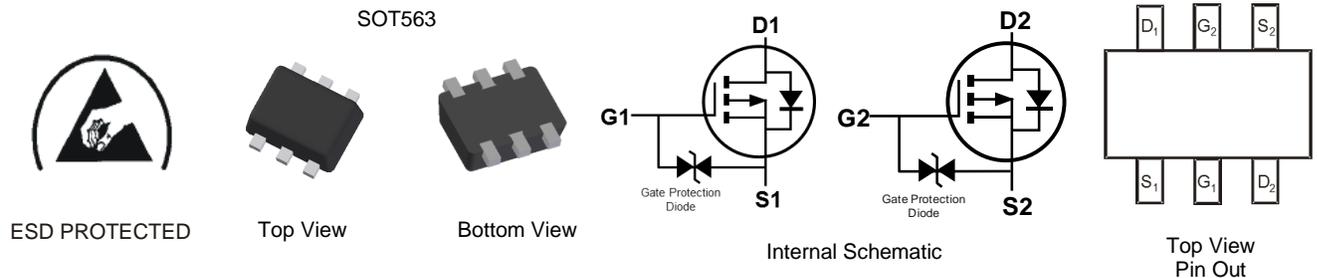
Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- DC-DC Converters
- Load Switch
- Power Management Functions

Mechanical Data

- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ^{e3}
- Weight: 0.006 grams (Approximate)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±6	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	T _A = +25°C	I _D	-0.85	A
		T _A = +70°C		-0.68	
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	-0.9	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	-2.5	A
Pulsed Source Current (10μs Pulse, Duty Cycle = 1%)			I _{SM}	-2.5	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	Steady State	P _D	0.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	236	°C/W
Total Power Dissipation (Note 6)	Steady State	P _D	0.8	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	153	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	-100	nA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±2.0	μA	V _{GS} = ±4.5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	-0.7	-1.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	0.46	0.75	Ω	V _{GS} = -4.5V, I _D = -430mA
			0.56	1.05		V _{GS} = -2.5V, I _D = -300mA
			0.7	1.5		V _{GS} = -1.8V, I _D = -150mA
			0.72	20		V _{GS} = -1.7V, I _D = -100mA
			0.8	25		V _{GS} = -1.5V, I _D = -100mA
Diode Forward Voltage (Note 7)	V _{SD}	—	-0.7	-1.2	V	V _{GS} = 0V, I _S = -150mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	49	—	pF	V _{DS} = -16V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	12	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	3.4	—	pF	
Total Gate Charge	Q _g	—	0.7	—	nC	V _{GS} = -4.5V, V _{DS} = -10V, I _D = -250mA
Gate-Source Charge	Q _{gs}	—	0.1	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.1	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	16	—	ns	V _{DD} = -10V, V _{GS} = -4.5V, R _L = 47Ω, R _θ = 10Ω, I _D = -200mA
Turn-On Rise Time	t _r	—	15	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	213	—	ns	
Turn-Off Fall Time	t _f	—	89	—	ns	
Reverse Recovery Time	t _{RR}	—	10.5	—	ns	
Reverse Recovery Charge	Q _{RR}	—	1.8	—	nC	I _F = -1A, di/dt = 100A/μs

- Notes:
- Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

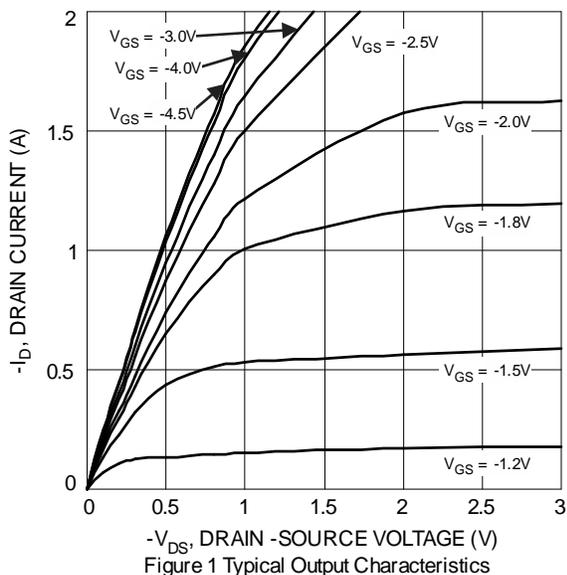


Figure 1 Typical Output Characteristics

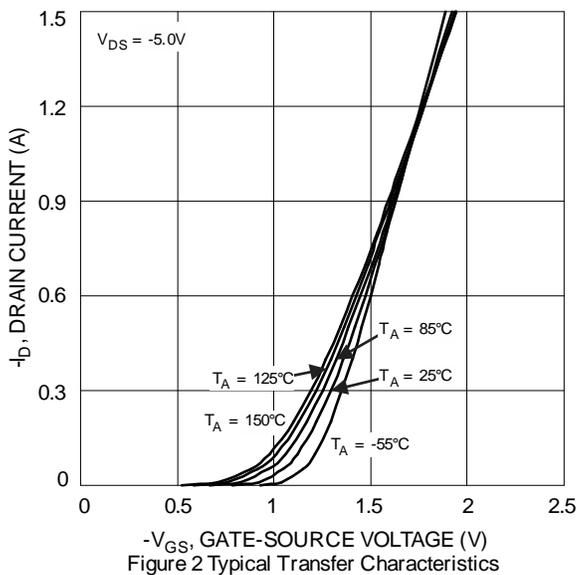


Figure 2 Typical Transfer Characteristics

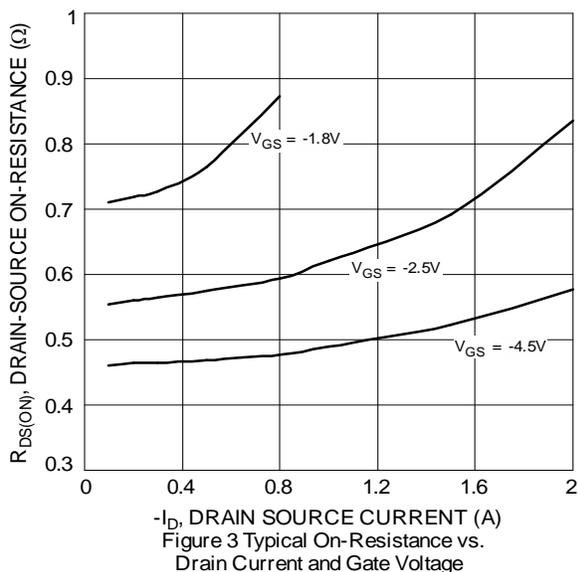


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

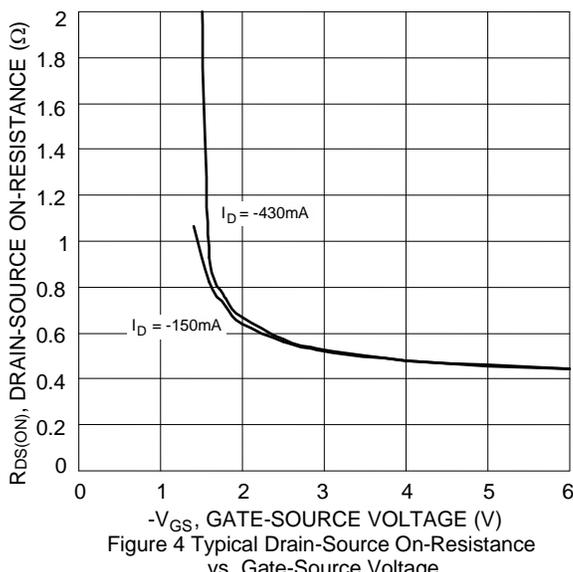


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

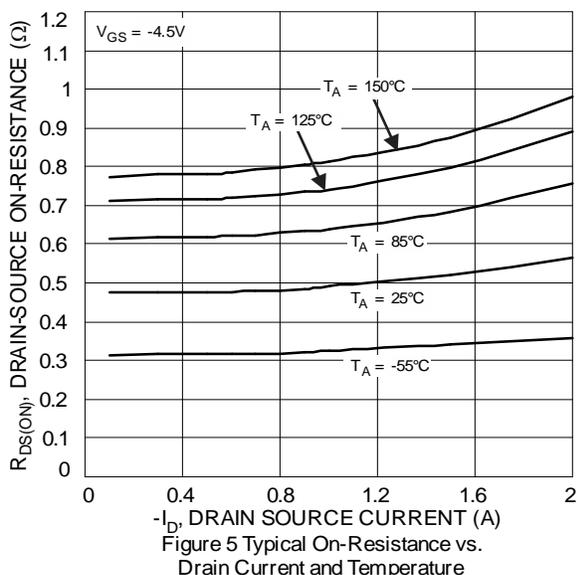


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

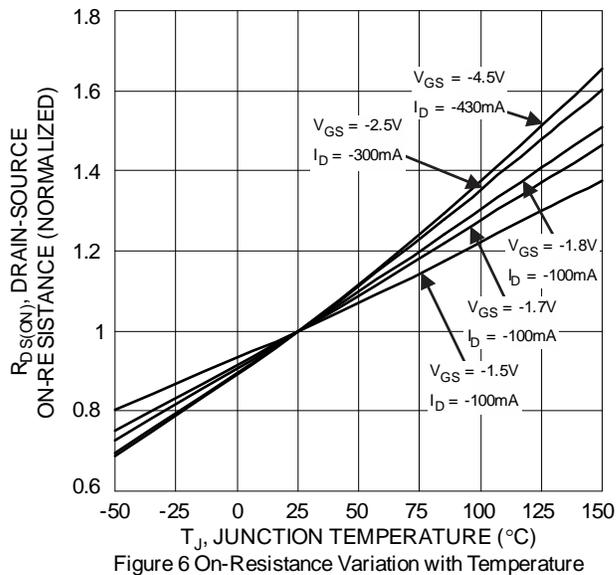


Figure 6 On-Resistance Variation with Temperature

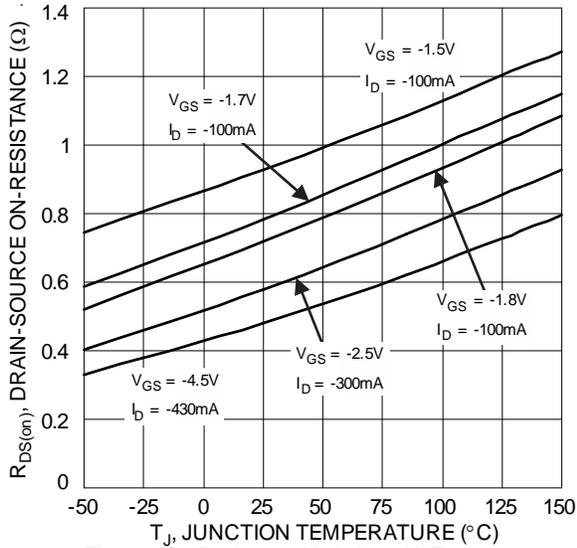


Figure 7 On-Resistance Variation with Temperature

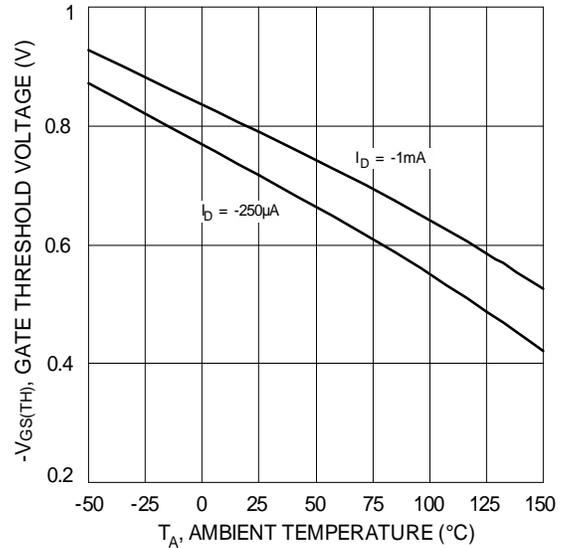


Figure 8 Gate Threshold Variation vs. Ambient Temperature

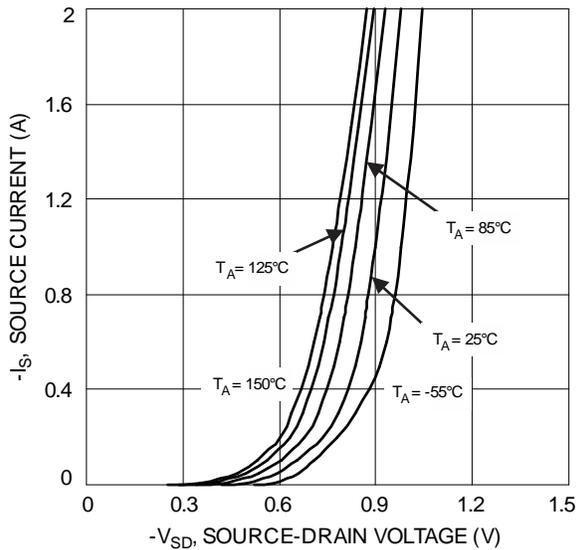


Figure 9 Diode Forward Voltage vs. Current

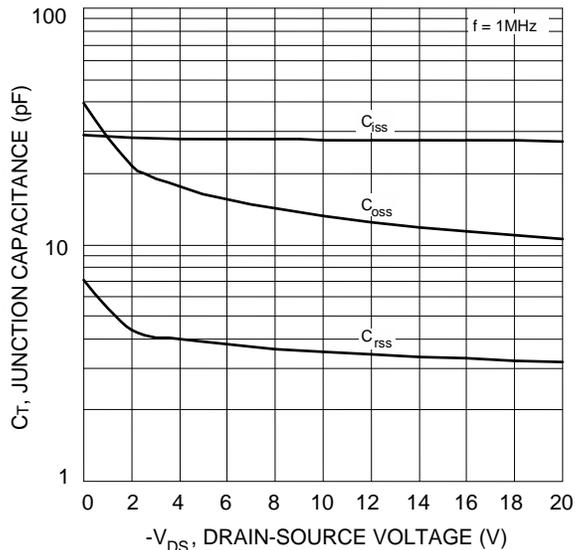


Figure 10 Typical Junction Capacitance

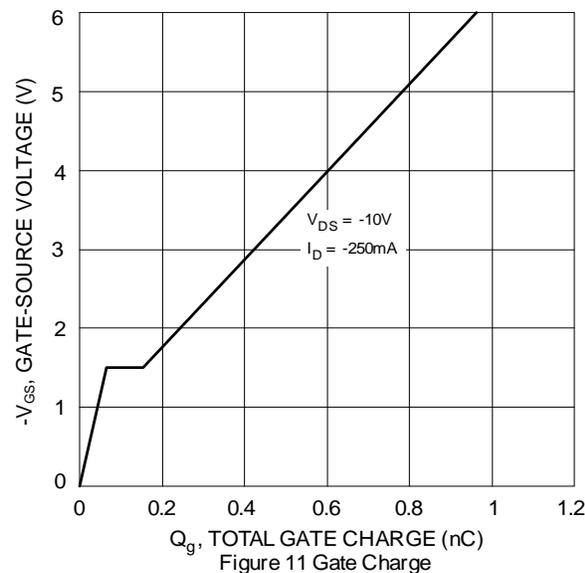


Figure 11 Gate Charge

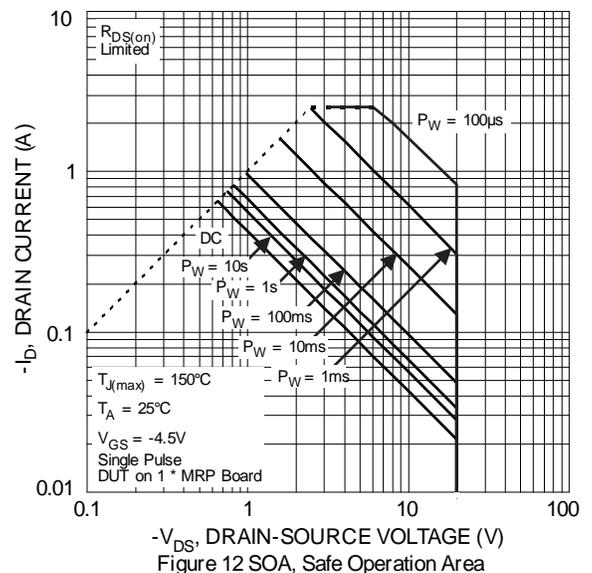


Figure 12 SOA, Safe Operation Area

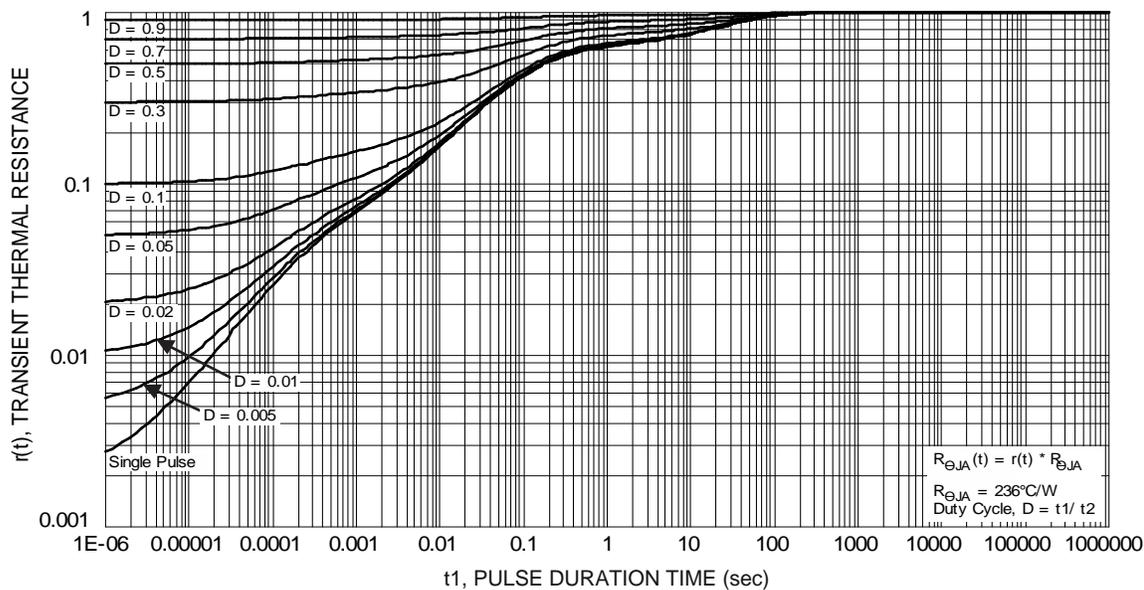
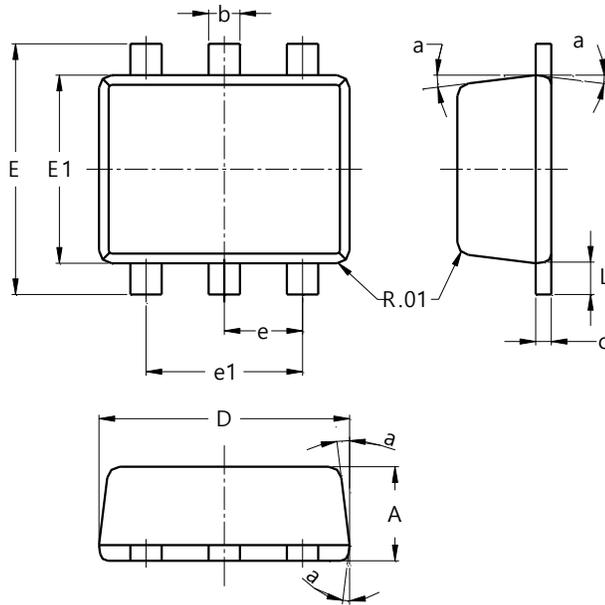


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

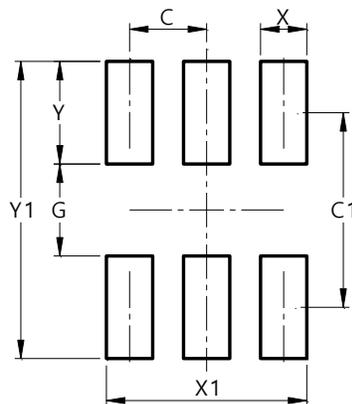
SOT563



SOT563			
Dim	Min	Max	Typ
A	0.55	0.60	--
b	0.15	0.30	0.20
c	0.10	0.18	0.11
D	1.50	1.70	1.60
E	1.55	1.70	1.60
E1	1.10	1.25	1.20
e	--	--	0.50
e1	0.90	1.10	1.00
L	0.10	0.30	0.20
a	8°	9°	7°
All Dimensions in mm			

Suggested Pad Layout

SOT563



Dimensions	Value (in mm)
C	0.500
C1	1.270
G	0.600
X	0.300
X1	1.300
Y	0.670
Y1	1.940