优尚

YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Product Summary

BV_{DSS}	**$R_{DS(ON)}$ Max**	**I_D Max $T_A = +25°C$**
100V	9.5mΩ @ V_{GS} = 10V	12A
	12mΩ @ V_{GS} = 6V	11A
	14.5mΩ @ V_{GS} = 4.5V	10A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Backlighting
- Power management functions
- DC-DC converters

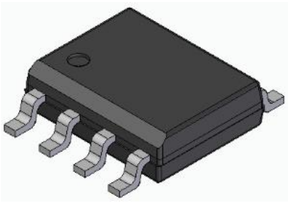
Features and Benefits

- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low $R_{DS(ON)}$ – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed

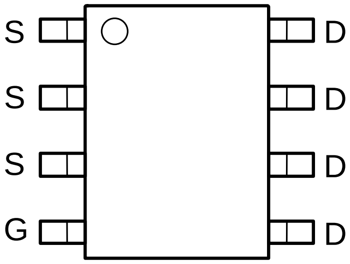
Mechanical Data

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (Approximate)

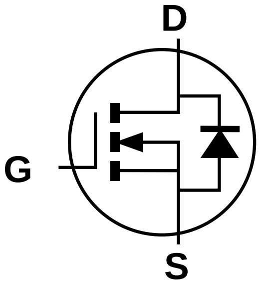
SO-8



Top View



Top View
Internal Schematic



Equivalent Circuit

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	100	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 5), V_{GS} = 10V	Steady State	T_A = +25°C	I_D	12	A
		T_A = +70°C		10	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	111	A
Maximum Continuous Body Diode Forward Current (Note 5)			I_S	12	A
Avalanche Current (Note 6), L = 0.3mH			I_{AS}	10	A
Avalanche Energy (Note 6), L = 0.3mH			E_{AS}	15	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power (Note 7)		P_D	1.9	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{\theta JA}$	66	°C/W
Total Power Dissipation (Note 5)		P_D	2.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	47	°C/W
Thermal Resistance, Junction to Case (Note 5)		$R_{\theta JC}$	3.6	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	100	—	—	V	V_{GS} = 0V, I_D = 1mA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	µA	V_{DS} = 80V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	1.4	—	2.8	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	7.6	9.5	mΩ	V_{GS} = 10V, I_D = 13A
		—	8.9	12		V_{GS} = 6V, I_D = 13A
		—	10.9	14.5		V_{GS} = 4.5V, I_D = 5A
Diode Forward Voltage	V_{SD}	—	0.8	1.3	V	V_{GS} = 0V, I_S = 13A
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C_{iss}	—	4166	—	pF	V_{DS} = 50V, V_{GS} = 0V f = 1MHz
Output Capacitance	C_{oss}	—	764	—		
Reverse Transfer Capacitance	C_{rss}	—	44	—		
Gate Resistance	R_g	—	2	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Q_g	—	58.4	—	nC	V_{DD} = 50V, I_D = 13A V_{GS} = 10V
Gate-Source Charge	Q_{gs}	—	11.4	—		
Gate-Drain Charge	Q_{gd}	—	14.2	—		
Turn-On Delay Time	$t_{D(ON)}$	—	11.6	—	ns	V_{DD} = 50V, V_{GS} = 10V I_D = 13A, R_g = 6Ω
Turn-On Rise Time	t_R	—	14.1	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	42.9	—		
Turn-Off Fall Time	t_F	—	22	—		
Reverse Recovery Time	t_{RR}	—	49.8	—	ns	I_F = 13A, dI/dt = 100A/µs
Reverse Recovery Charge	Q_{RR}	—	85.1	—	nC	

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
6. Guaranteed by design. Not subject to product testing.
7. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
8. Short duration pulse test used to minimize self-heating effect.

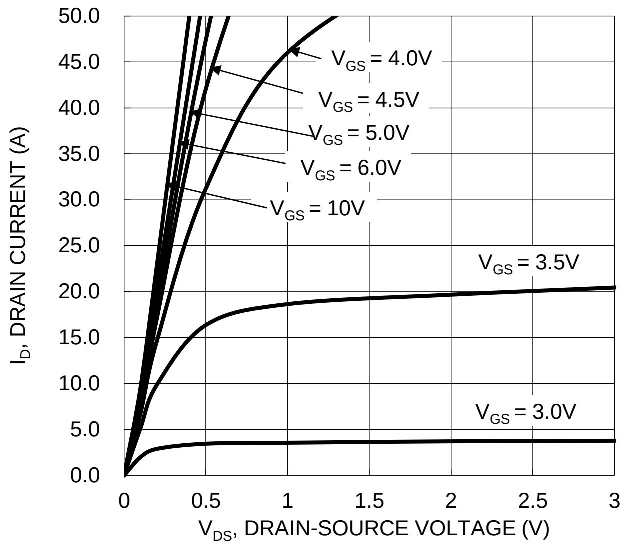


Figure 1. Typical Output Characteristic

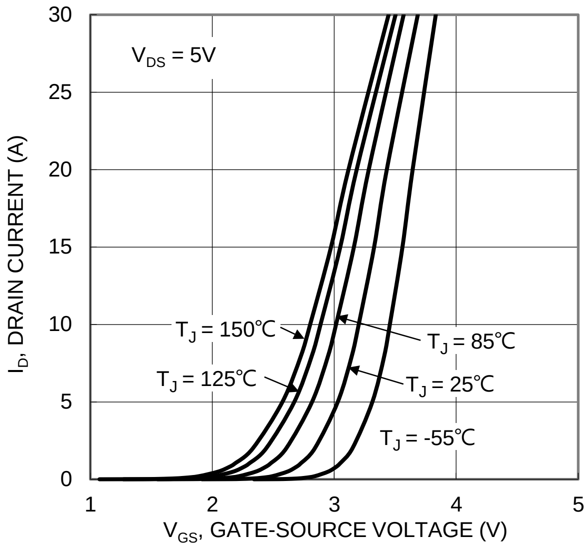


Figure 2. Typical Transfer Characteristic

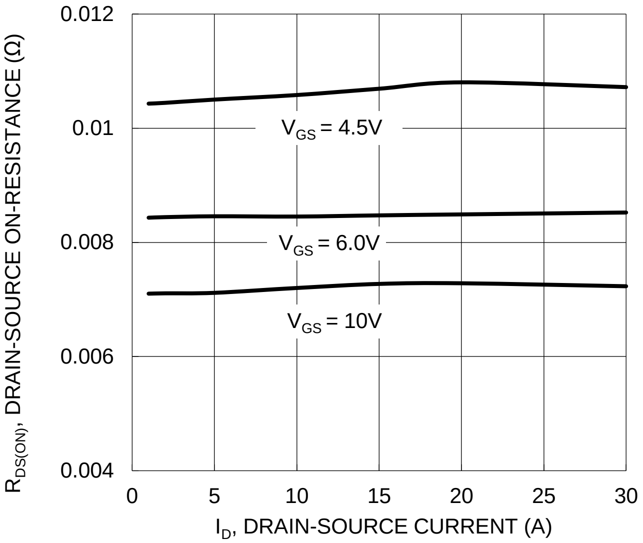


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

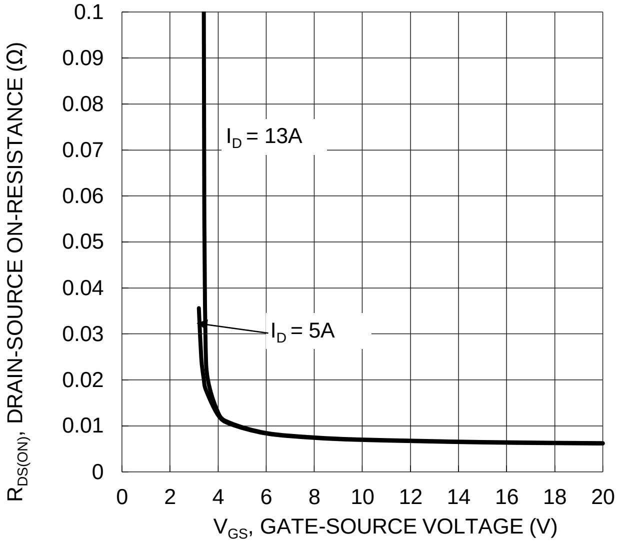


Figure 4. Typical Transfer Characteristic

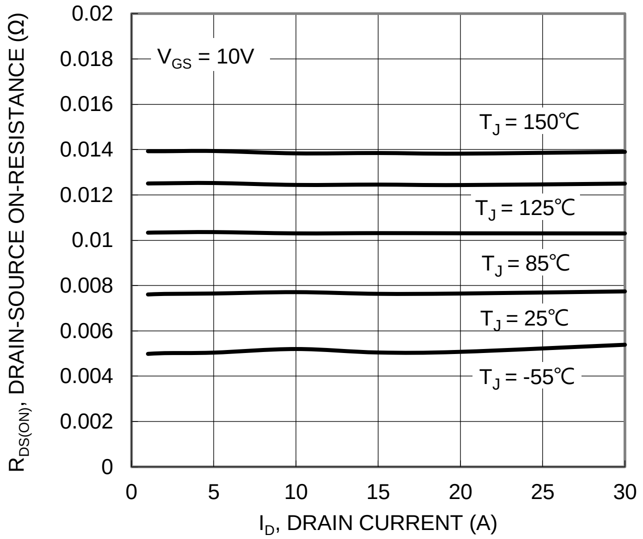


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

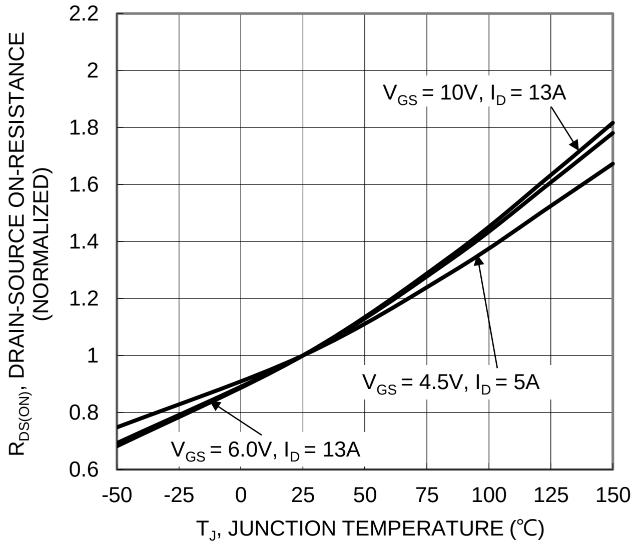


Figure 6. On-Resistance Variation with Junction Temperature

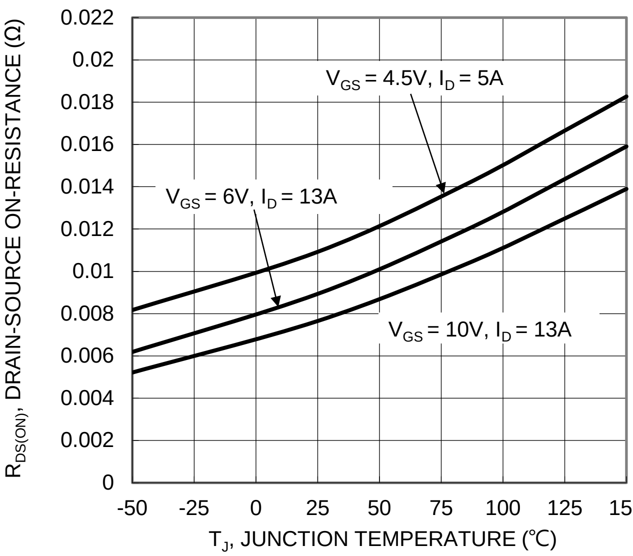


Figure 7. On-Resistance Variation with Junction Temperature

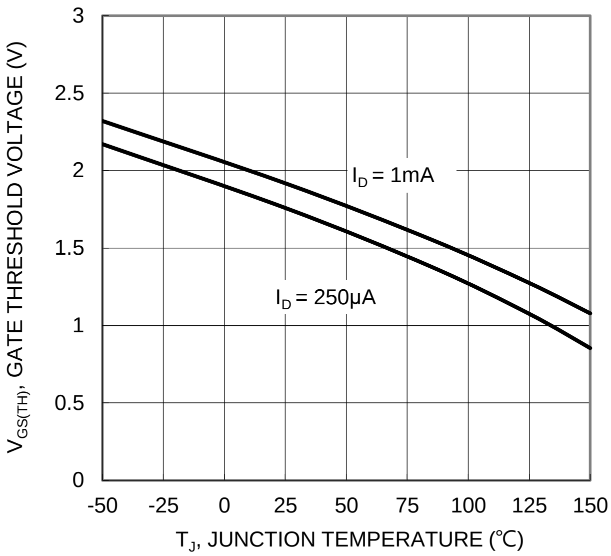


Figure 8. Gate Threshold Variation vs. Junction Temperature

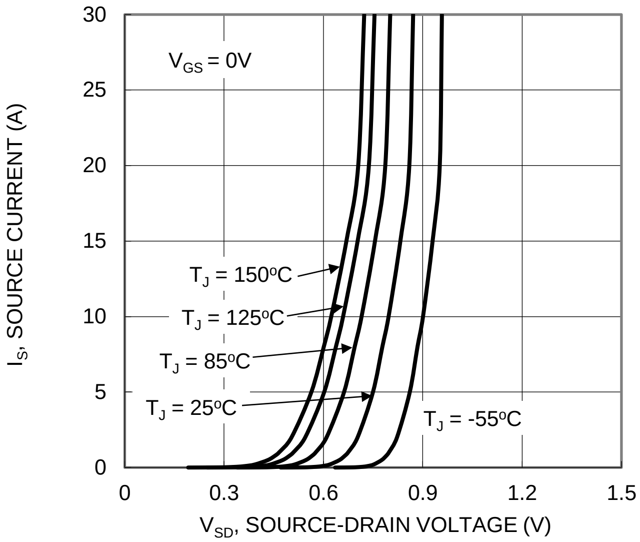


Figure 9. Diode Forward Voltage vs. Current

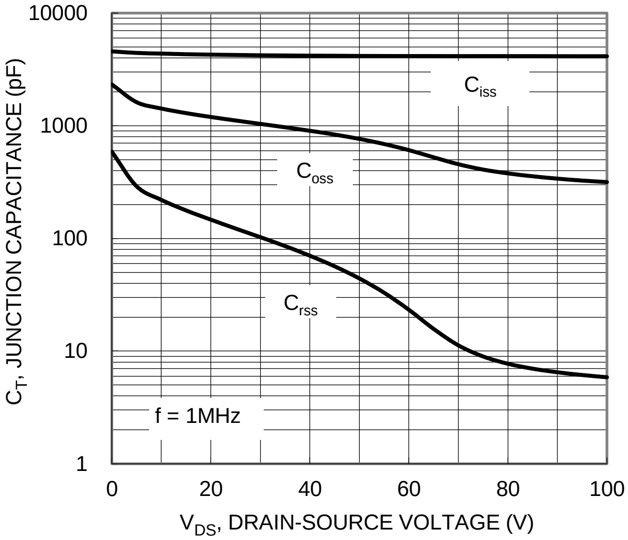


Figure 10. Typical Junction Capacitance

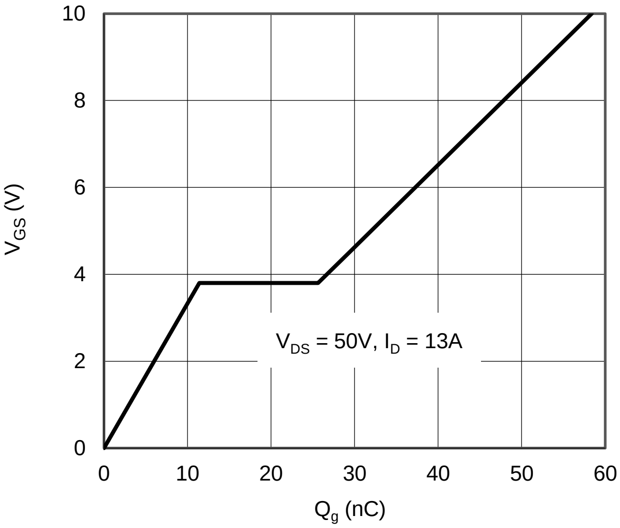


Figure 11. Gate Charge

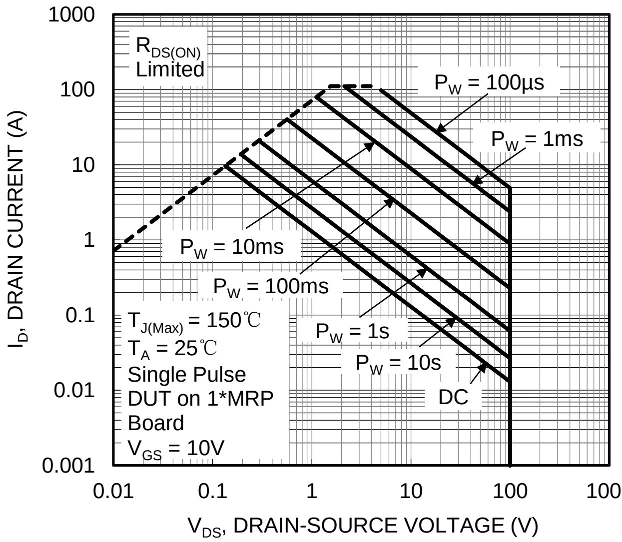


Figure 12. SOA, Safe Operation Area

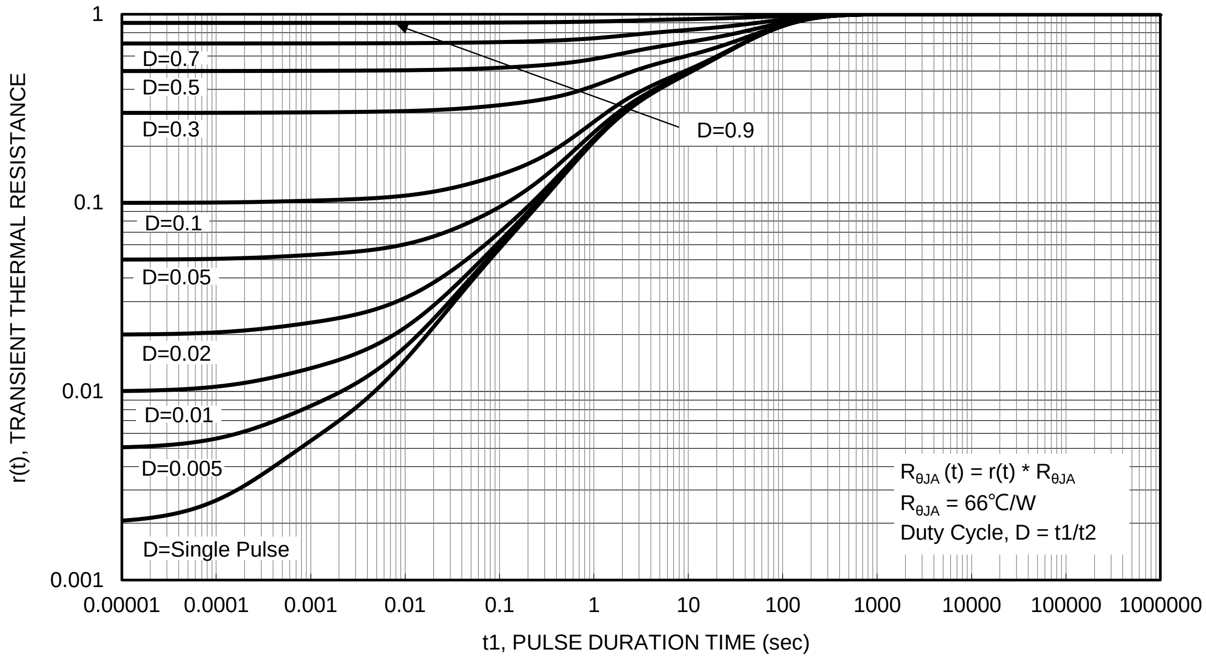
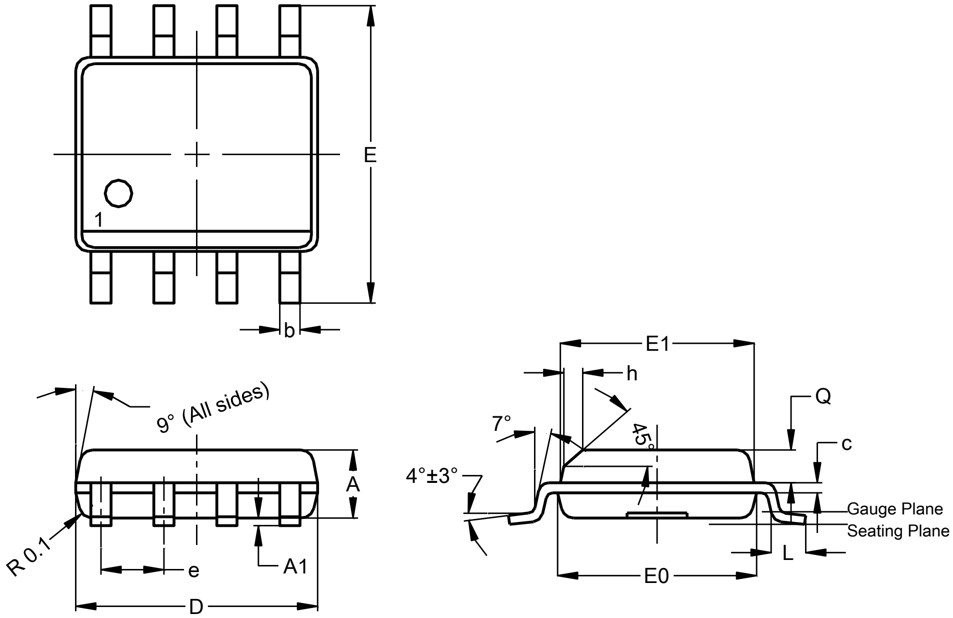


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

SO-8



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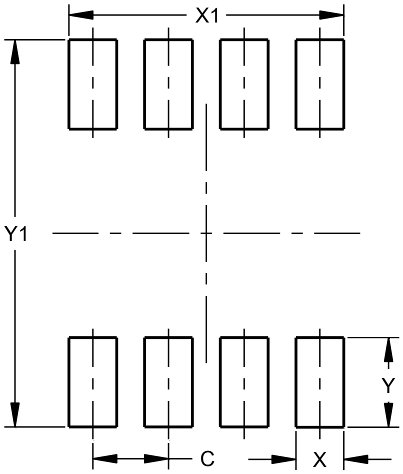
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50