



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

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企业微信二维码



企业QQ二维码

## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$ Max	$I_D$ $T_C = +25^\circ C$ (Note 9)
100V	8.8m $\Omega$ @ $V_{GS} = 10V$	100A
	11.5m $\Omega$ @ $V_{GS} = 6V$	98A

## Description

This new generation N-channel enhancement mode MOSFET is designed to minimize  $R_{DS(ON)}$ , yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switches.

## Applications

- Motor controls
- DC-DC converters
- Power management

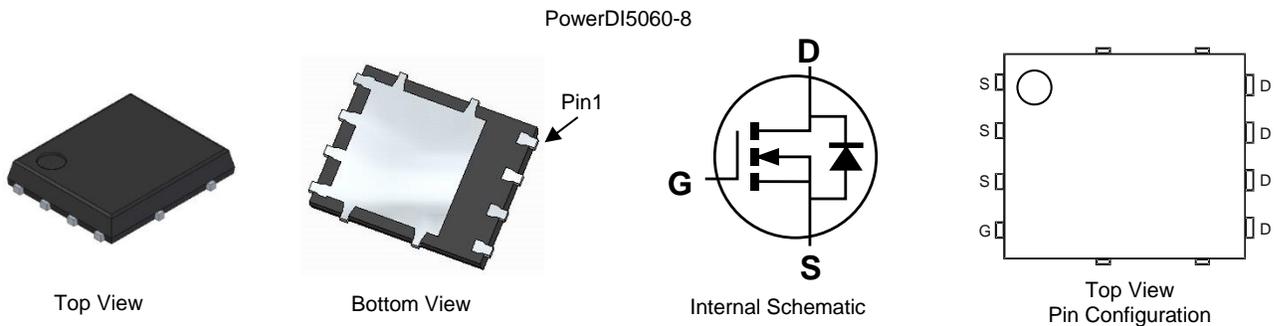
## Features

- 100% Unclamped Inductive Switching – Ensures More Reliable and Robust End Application
- Low  $R_{DS(ON)}$  – Minimizes On-State Losses
- Fast Switching Speed

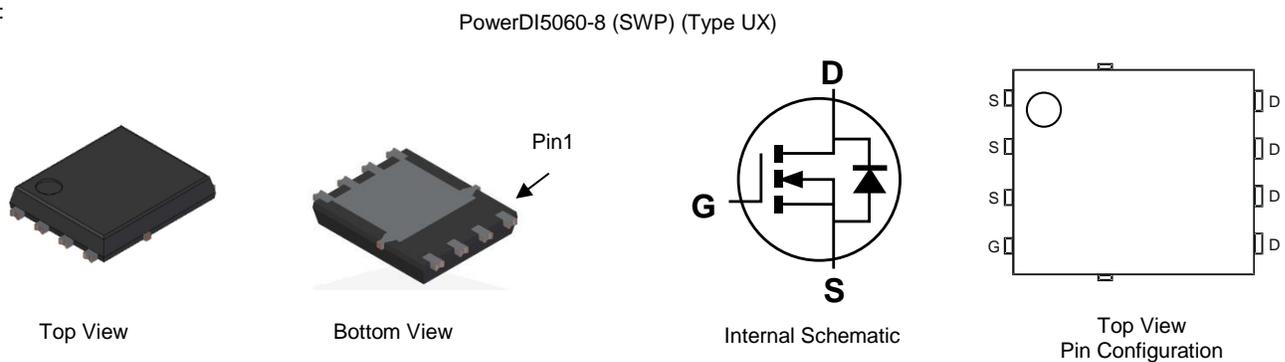
## Mechanical Data

- Package: PowerDI<sup>®</sup> 5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminal Finish - Matte Tin Annealed over Copper Lead-Frame. Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.097 grams (Approximate)

Site 1:



Site 2:



### Maximum Ratings (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		$V_{DSS}$	100	V
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} = 10\text{V}$ (Note 5)	$T_A = +25^\circ\text{C}$	$I_D$	14	A
	$T_A = +70^\circ\text{C}$		11	
Continuous Drain Current, $V_{GS} = 10\text{V}$ (Note 6)	$T_C = +25^\circ\text{C}$ (Note 9)	$I_D$	100	A
	$T_C = +70^\circ\text{C}$		90	
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)		$I_{DM}$	250	A
Maximum Continuous Body Diode Forward Current		$I_S$	100	A
Avalanche Current, $L=0.3\text{mH}$		$I_{AS}$	25	A
Avalanche Energy, $L=0.3\text{mH}$		$E_{AS}$	93.7	mJ
Avalanche Current (Note 8), $L=3\text{mH}$		$I_{AS}$	14.3	A
Avalanche Energy (Note 8), $L=3\text{mH}$		$E_{AS}$	307	mJ

### Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^\circ\text{C}$	$P_D$	2.5	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	49	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	$T_C = +25^\circ\text{C}$	$P_D$	139	W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	0.9	$^\circ\text{C/W}$
Operating and Storage Temperature Range		$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

- Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  6. Thermal resistance from junction to soldering point (on the exposed drain pad).
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.
  9. Package limited.

**Electrical Characteristics** (@  $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 7)						
Drain-Source Breakdown Voltage	$BV_{DSS}$	100	—	—	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	1	$\mu A$	$V_{DS} = 80V, V_{GS} = 0V$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
<b>ON CHARACTERISTICS</b> (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	2	—	4	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	6.6	8.8	m $\Omega$	$V_{GS} = 10V, I_D = 13A$
		—	8.5	11.5		$V_{GS} = 6V, I_D = 13A$
Diode Forward Voltage	$V_{SD}$	—	0.8	1.3	V	$V_{GS} = 0V, I_S = 13A$
<b>DYNAMIC CHARACTERISTICS</b> (Note 8)						
Input Capacitance	$C_{ISS}$	—	4,468	—	pF	$V_{DS} = 50V, V_{GS} = 0V$ $f = 1MHz$
Output Capacitance	$C_{OSS}$	—	746	—		
Reverse Transfer Capacitance	$C_{RSS}$	—	32	—		
Gate Resistance	$R_G$	—	0.91	—	$\Omega$	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	$Q_G$	—	56.4	—	nC	$V_{DD} = 50V, I_D = 13A,$ $V_{GS} = 10V$
Gate-Source Charge	$Q_{GS}$	—	15.4	—		
Gate-Drain Charge	$Q_{GD}$	—	14	—		
Turn-On Delay Time	$t_{D(ON)}$	—	18.6	—	ns	$V_{DD} = 50V, V_{GS} = 10V,$ $I_D = 13A, R_g = 6\Omega$
Turn-On Rise Time	$t_R$	—	22.5	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	44.8	—		
Turn-Off Fall Time	$t_F$	—	29.5	—		
Reverse Recovery Time	$t_{RR}$	—	54.5	—	ns	$I_F = 13A, di/dt = 100A/\mu s$
Reverse Recovery Charge	$Q_{RR}$	—	106.4	—	nC	

Notes: 7. Short duration pulse test used to minimize self-heating effect.  
 8. Guaranteed by design. Not subject to product testing.

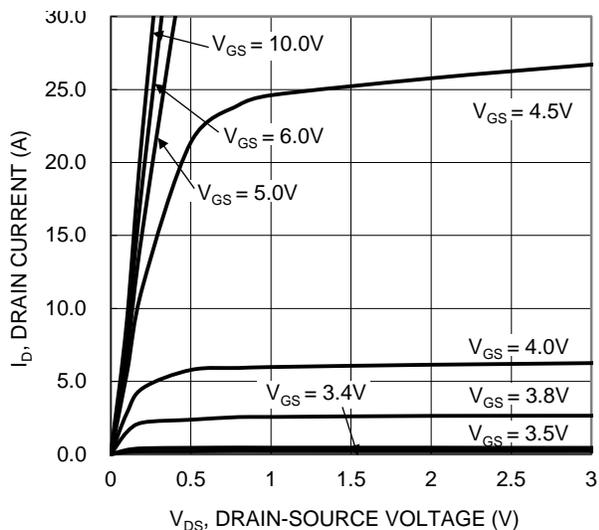


Figure 1. Typical Output Characteristic

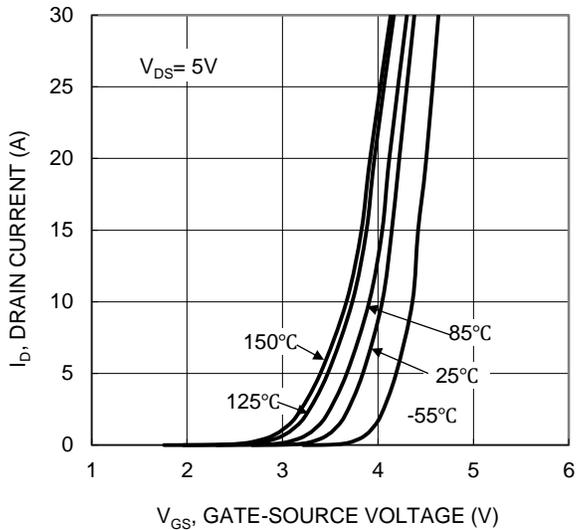


Figure 2. Typical Transfer Characteristic

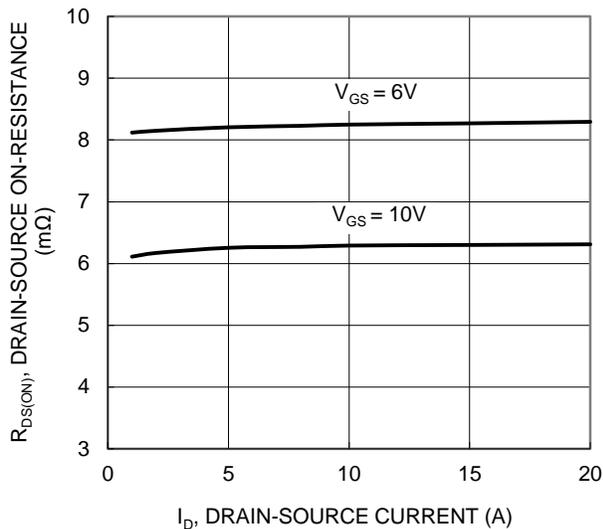


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

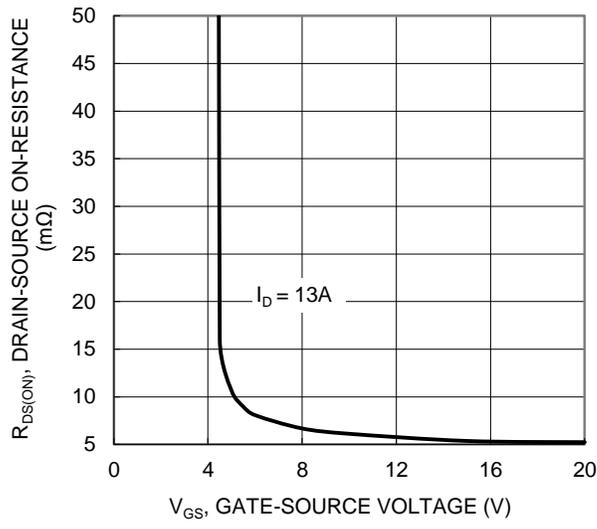


Figure 4. Typical Transfer Characteristic

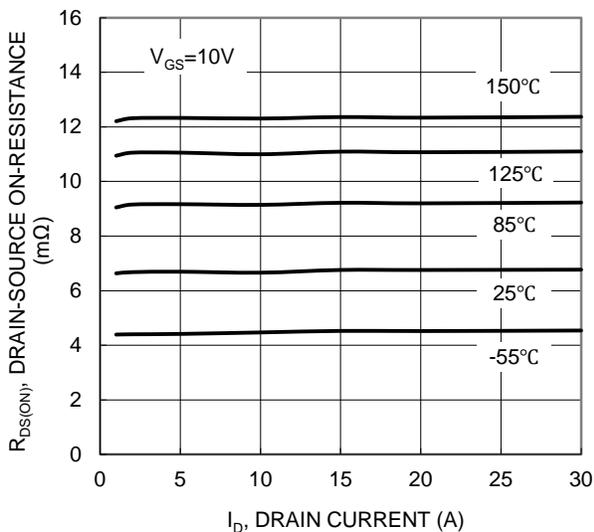


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

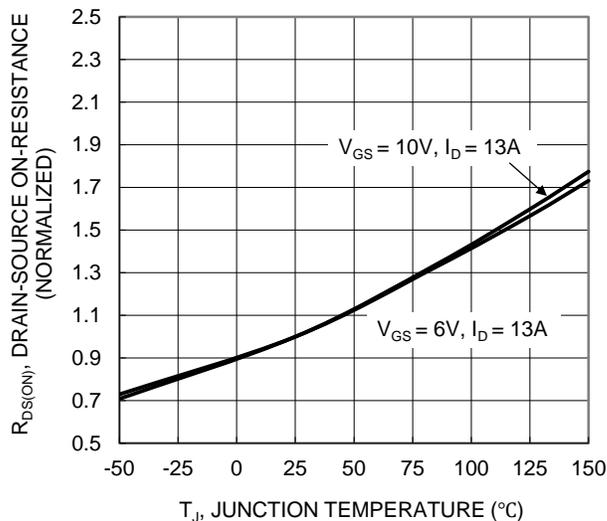


Figure 6. On-Resistance Variation with Junction Temperature

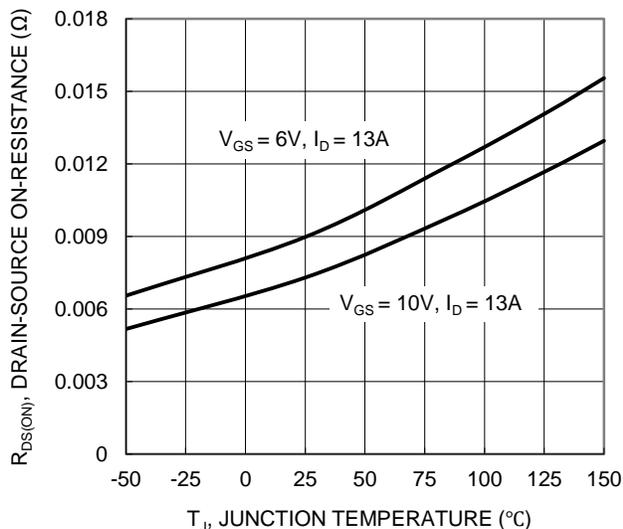


Figure 7. On-Resistance Variation with Junction Temperature

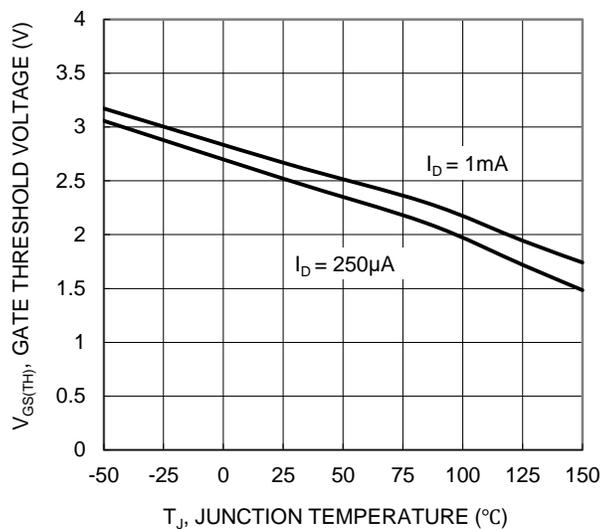


Figure 8. Gate Threshold Variation vs. Junction Temperature

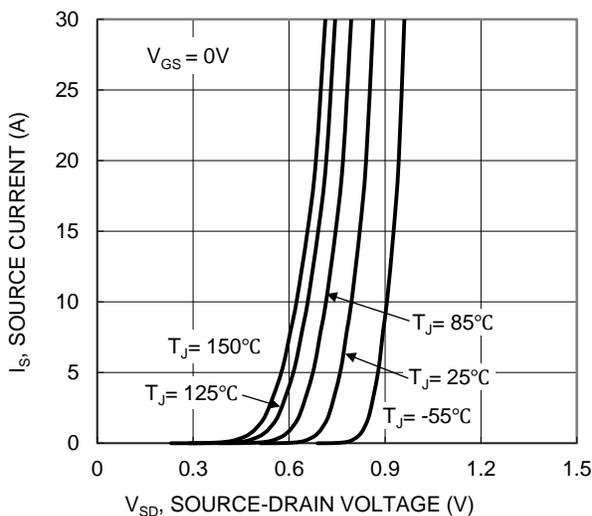


Figure 9. Diode Forward Voltage vs. Current

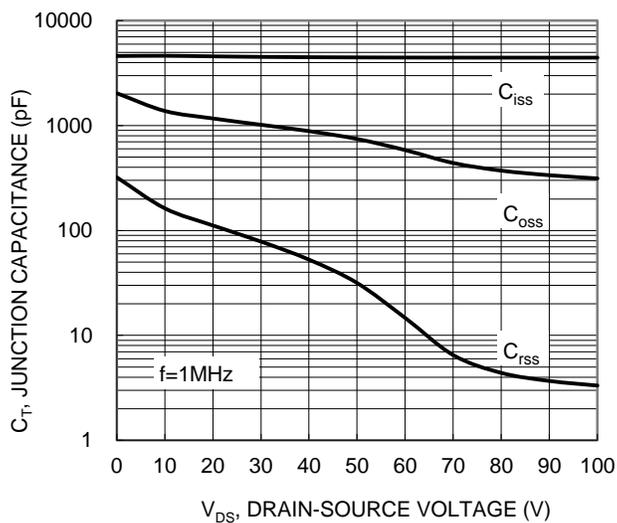


Figure 10. Typical Junction Capacitance

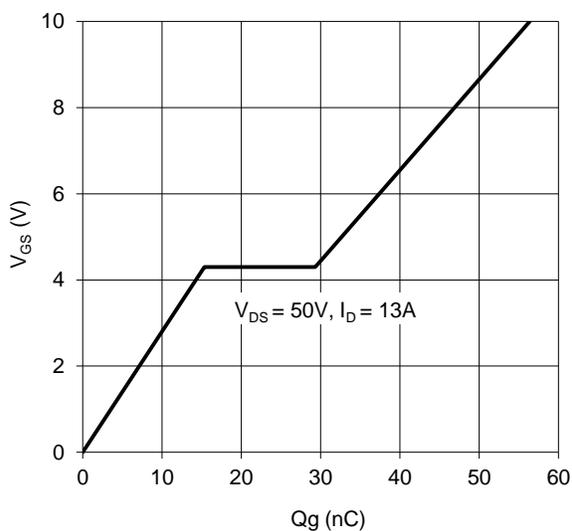


Figure 11. Gate Charge

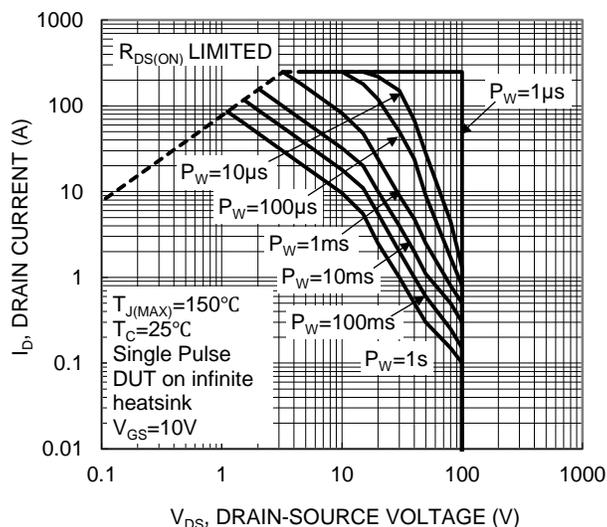


Figure 12. SOA, Safe Operation Area

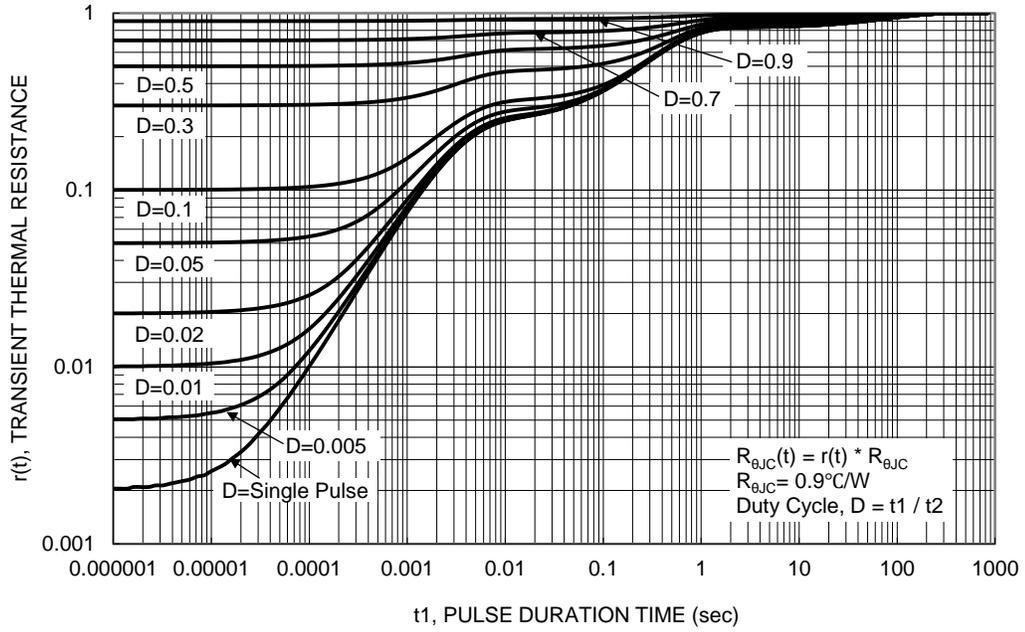
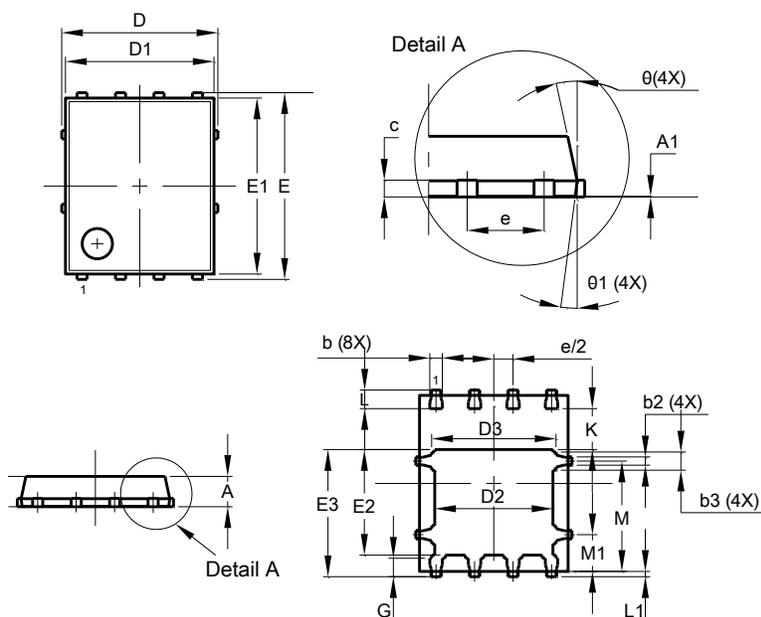


Figure 13. Transient Thermal Resistance

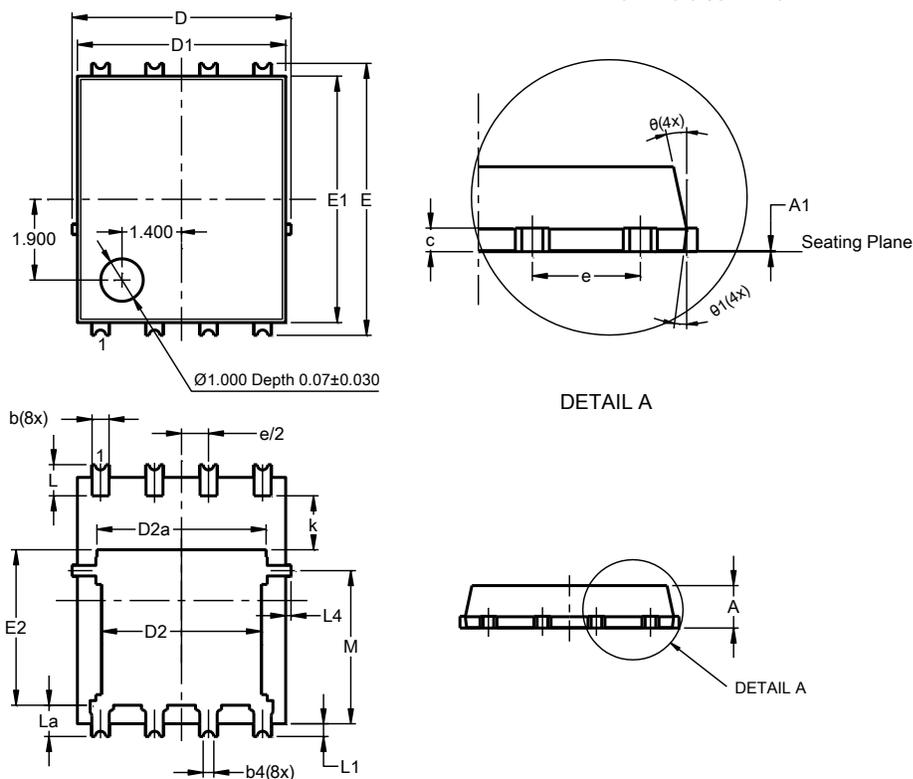
**Package Outline Dimensions**

Site 1:

**PowerDI5060-8**


PowerDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	—
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	—	—
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
$\theta$	10°	12°	11°
$\theta1$	6°	8°	7°
All Dimensions in mm			

Site 2:

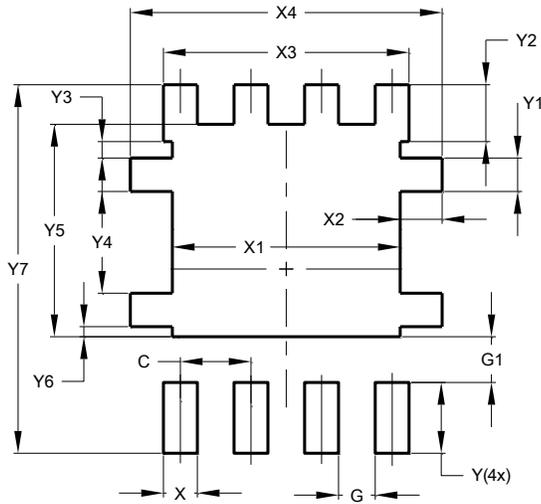
**PowerDI5060-8 (SWP) (Type UX)**


PowerDI5060-8 (SWP) (Type UX)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
$\theta$	10°	12°	11°
$\theta1$	6°	8°	7°
All Dimensions in mm			

**Suggested Pad Layout**

Site 1

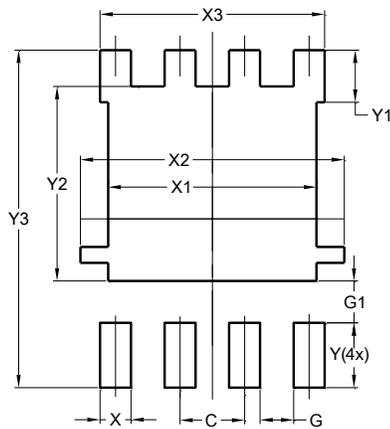
**PowerDI5060-8**



Dimensions	Value (in mm)
<b>C</b>	1.270
<b>G</b>	0.660
<b>G1</b>	0.820
<b>X</b>	0.610
<b>X1</b>	4.100
<b>X2</b>	0.755
<b>X3</b>	4.420
<b>X4</b>	5.610
<b>Y</b>	1.270
<b>Y1</b>	0.600
<b>Y2</b>	1.020
<b>Y3</b>	0.295
<b>Y4</b>	1.825
<b>Y5</b>	3.810
<b>Y6</b>	0.180
<b>Y7</b>	6.610

Site 2:

**PowerDI5060-8 (SWP) (Type UX)**



Dimensions	Value (in mm)
<b>C</b>	1.270
<b>G</b>	0.660
<b>G1</b>	0.820
<b>X</b>	0.610
<b>X1</b>	4.100
<b>X2</b>	5.190
<b>X3</b>	4.420
<b>Y</b>	1.270
<b>Y1</b>	1.020
<b>Y2</b>	3.810
<b>Y3</b>	6.610