



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

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## Product Summary

| BV <sub>DSS</sub> | R <sub>DS(ON)</sub> Max         | I <sub>D</sub> Max<br>T <sub>C</sub> = +25°C |
|-------------------|---------------------------------|--|
| 150V              | 17.5mΩ @ V <sub>GS</sub> = 10V  | 58A  |
|                   | 25.5mΩ @ V <sub>GS</sub> = 4.5V | 48A  |

## Description

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize R<sub>DS(ON)</sub> yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

## Applications

- Synchronous Rectification
- Power Switching
- Class D Audio Amplifier

## Features

- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile – Ideal for Thin Applications (PowerDI®)

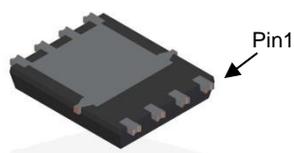
## Mechanical Data

- Case: PowerDI5060-8
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminal Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 Ⓔ
- Weight: 0.097 grams (Approximate)

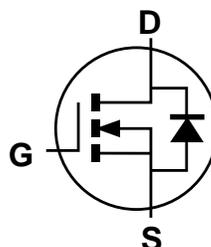
PowerDI5060-8 (SWP) (Type UX)



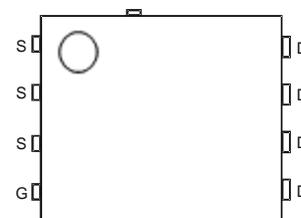
Top View



Bottom View



Internal Schematic



Top View  
Pin Configuration

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

| Characteristic  |              | Symbol                    | Value    | Unit |
|---|--------------|---------------------------|----------|------|
| Drain-Source Voltage  |              | $V_{DSS}$                 | 150      | V    |
| Gate-Source Voltage   |              | $V_{GSS}$                 | $\pm 20$ | V    |
| Continuous Drain Current $V_{GS} = 10\text{V}$ (Note 6)             | Steady State | $T_A = +25^\circ\text{C}$ | 9.4      | A    |
|   |              | $T_A = +70^\circ\text{C}$ | 7.5      |      |
| Continuous Drain Current $V_{GS} = 10\text{V}$ (Note 7)             | Steady State | $T_C = +25^\circ\text{C}$ | 58       | A    |
|   |              | $T_C = +70^\circ\text{C}$ | 46       |      |
| Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)      |              | $I_{DM}$                  | 230      | A    |
| Maximum Continuous Body Diode Forward Current                       |              | $I_S$                     | 74       | A    |
| Pulsed Body Diode Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%) |              | $I_{SM}$                  | 230      | A    |
| Avalanche Current (Note 8), $L = 3\text{mH}$                        |              | $I_{AS}$                  | 14.5     | A    |
| Avalanche Energy (Note 8), $L = 3\text{mH}$                         |              | $E_{AS}$                  | 315.4    | mJ   |

**Thermal Characteristics**

| Characteristic                                   |                           | Symbol          | Value       | Unit               |
|--|---------------------------|-----------------|-------------|--------------------|
| Total Power Dissipation (Note 5)                 | $T_A = +25^\circ\text{C}$ | $P_D$           | 1.3         | W                  |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State              | $R_{\theta JA}$ | 99          | $^\circ\text{C/W}$ |
| Total Power Dissipation (Note 6)                 | $T_A = +25^\circ\text{C}$ | $P_D$           | 2.3         | W                  |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State              | $R_{\theta JA}$ | 53          | $^\circ\text{C/W}$ |
| Total Power Dissipation (Note 7)                 | $T_C = +25^\circ\text{C}$ | $P_D$           | 89          | W                  |
| Thermal Resistance, Junction to Case (Note 7)    |                           | $R_{\theta JC}$ | 1.4         | $^\circ\text{C/W}$ |
| Operating and Storage Temperature Range          |                           | $T_J, T_{STG}$  | -55 to +150 | $^\circ\text{C}$   |

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25^\circ\text{C}$ .

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic                           | Symbol              | Min | Typ  | Max  | Unit | Test Condition   |
|--|---------------------|-----|------|------|------|--|
| <b>OFF CHARACTERISTICS</b> (Note 9)      |                     |     |      |      |      |  |
| Drain-Source Breakdown Voltage           | BV <sub>DSS</sub>   | 150 | —    | —    | V    | V <sub>GS</sub> = 0V, I <sub>D</sub> = 10mA  |
| Zero Gate Voltage Drain Current          | I <sub>DSS</sub>    | —   | —    | 1    | μA   | V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V   |
| Gate-Source Leakage                      | I <sub>GSS</sub>    | —   | —    | ±100 | nA   | V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V   |
| <b>ON CHARACTERISTICS</b> (Note 9)       |                     |     |      |      |      |  |
| Gate Threshold Voltage                   | V <sub>GS(TH)</sub> | 1.3 | —    | 2.6  | V    | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA                                 |
| Static Drain-Source On-Resistance        | R <sub>DS(ON)</sub> | —   | 14   | 17.5 | mΩ   | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A  |
|  |                     | —   | 18   | 25.5 |      | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A   |
| Diode Forward Voltage                    | V <sub>SD</sub>     | —   | 0.8  | 1.2  | V    | V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A   |
| <b>DYNAMIC CHARACTERISTICS</b> (Note 10) |                     |     |      |      |      |  |
| Input Capacitance                        | C <sub>iss</sub>    | —   | 3369 | —    | pF   | V <sub>DS</sub> = 75V, V <sub>GS</sub> = 0V<br>f = 1MHz                                    |
| Output Capacitance                       | C <sub>oss</sub>    | —   | 211  | —    |      |  |
| Reverse Transfer Capacitance             | C <sub>rss</sub>    | —   | 6.7  | —    |      |  |
| Gate Resistance                          | R <sub>g</sub>      | —   | 1.9  | —    | Ω    | V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz                                       |
| Total Gate Charge                        | Q <sub>g</sub>      | —   | 50   | —    | nC   | V <sub>DD</sub> = 75V, I <sub>D</sub> = 20A,<br>V <sub>GS</sub> = 10V                      |
| Gate-Source Charge                       | Q <sub>gs</sub>     | —   | 12.8 | —    |      |  |
| Gate-Drain Charge                        | Q <sub>gd</sub>     | —   | 9.4  | —    |      |  |
| Turn-On Delay Time                       | t <sub>D(ON)</sub>  | —   | 10.5 | —    | ns   | V <sub>DD</sub> = 75V, V <sub>GS</sub> = 10V,<br>I <sub>D</sub> = 20A, R <sub>g</sub> = 6Ω |
| Turn-On Rise Time                        | t <sub>r</sub>      | —   | 16.3 | —    |      |  |
| Turn-Off Delay Time                      | t <sub>D(OFF)</sub> | —   | 44.6 | —    |      |  |
| Turn-Off Fall Time                       | t <sub>f</sub>      | —   | 17.7 | —    |      |  |
| Reverse Recovery Time                    | t <sub>RR</sub>     | —   | 72   | —    | ns   | I <sub>F</sub> = 20A, di/dt = 100A/μs  |
| Reverse Recovery Charge                  | Q <sub>RR</sub>     | —   | 215  | —    | nC   |  |

Notes: 9. Short duration pulse test used to minimize self-heating effect.  
 10. Guaranteed by design. Not subject to product testing.

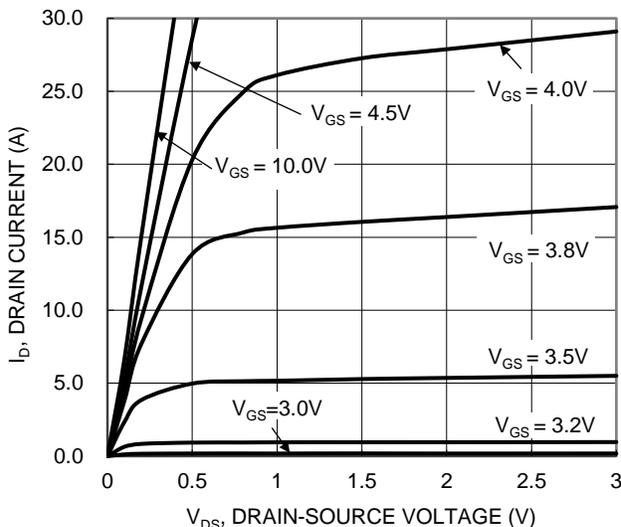


Figure 1. Typical Output Characteristic

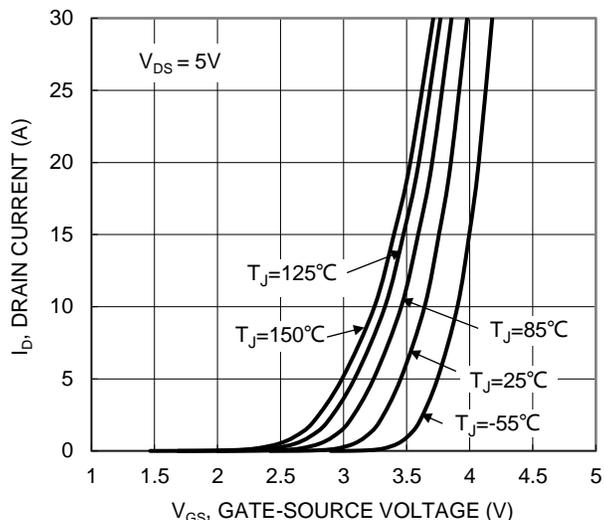


Figure 2. Typical Transfer Characteristic

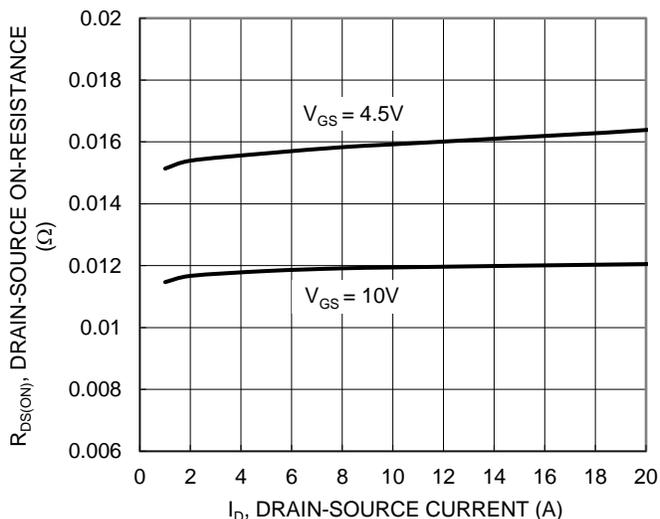


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

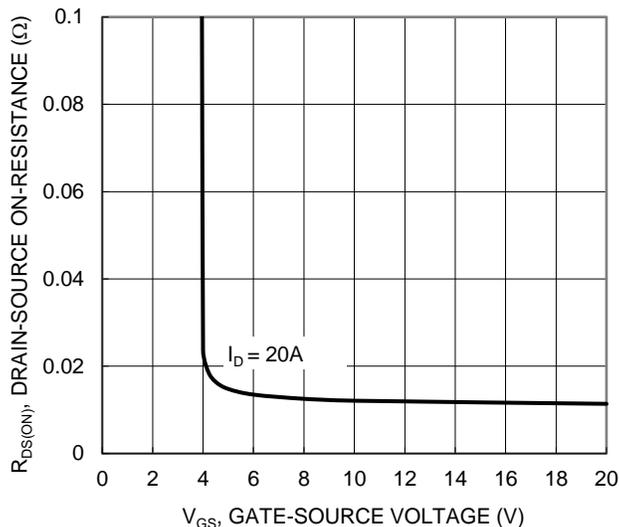


Figure 4. Typical Transfer Characteristic

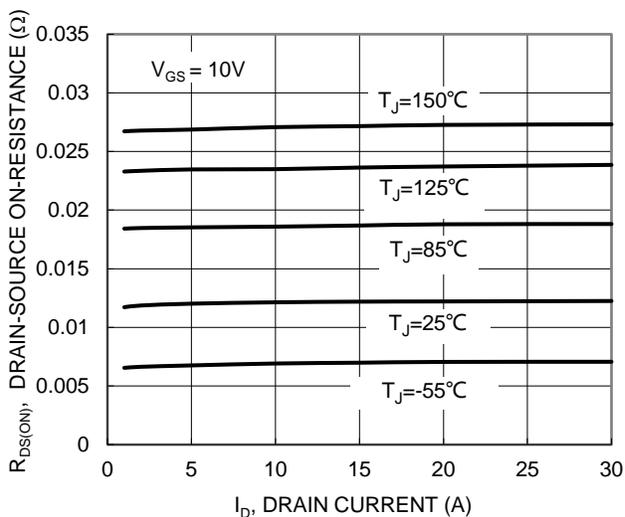


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

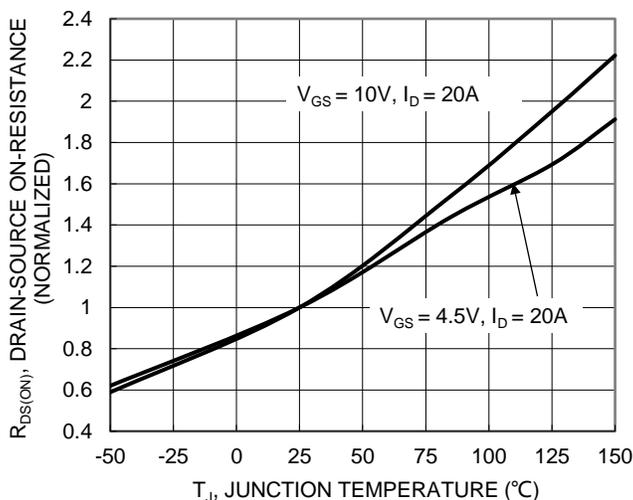


Figure 6. On-Resistance Variation with Junction Temperature

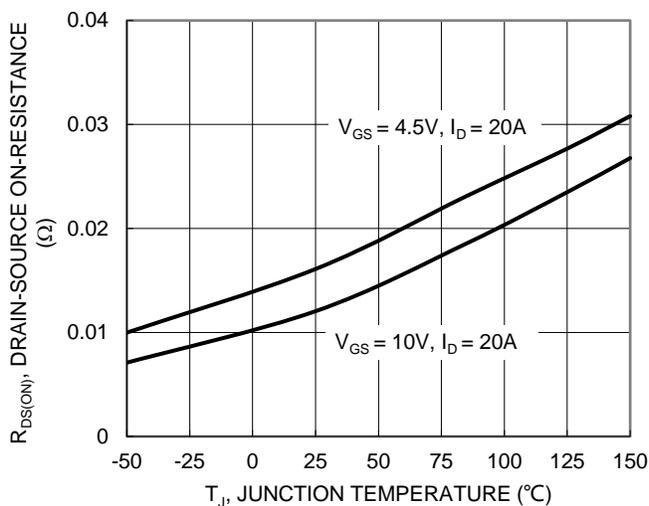


Figure 7. On-Resistance Variation with Junction Temperature

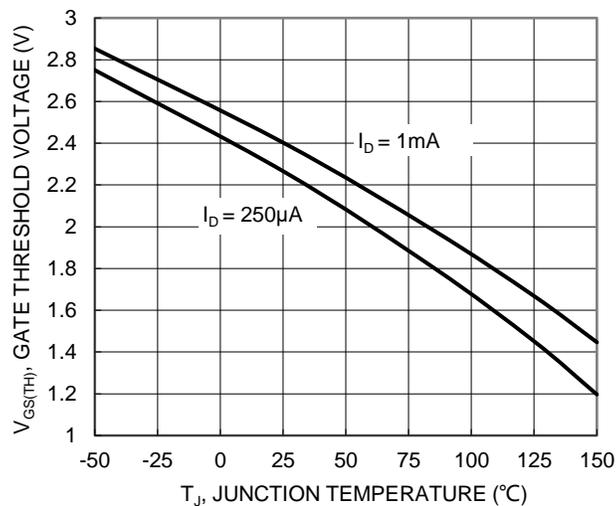


Figure 8. Gate Threshold Variation vs. Junction Temperature

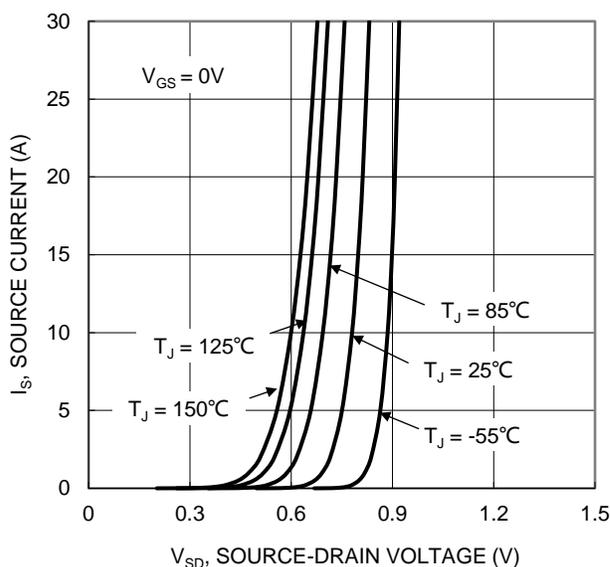


Figure 9. Diode Forward Voltage vs. Current

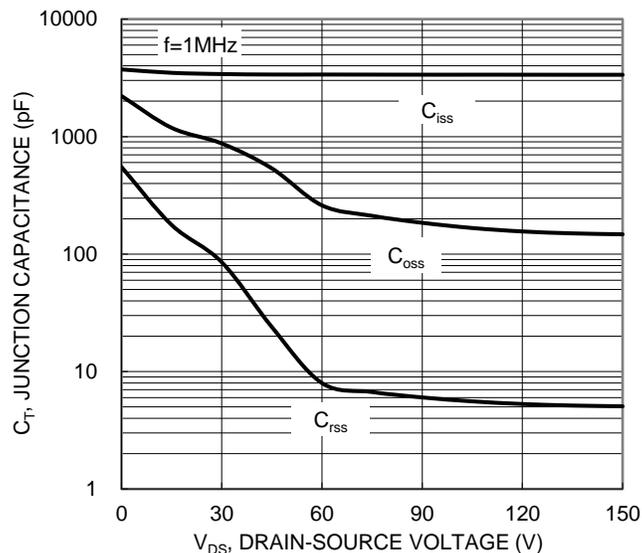


Figure 10. Typical Junction Capacitance

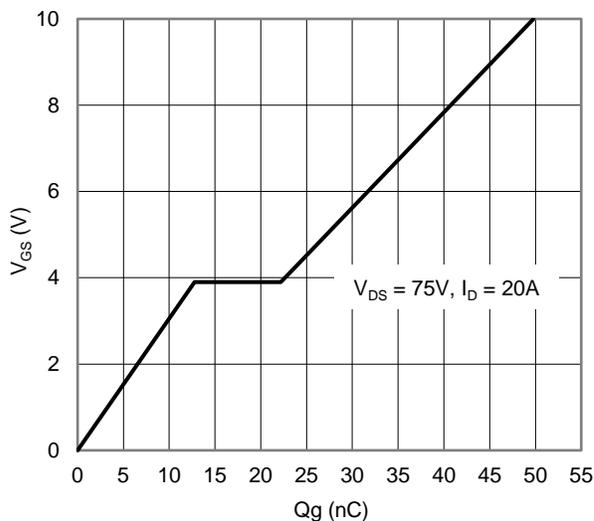


Figure 11. Gate Charge

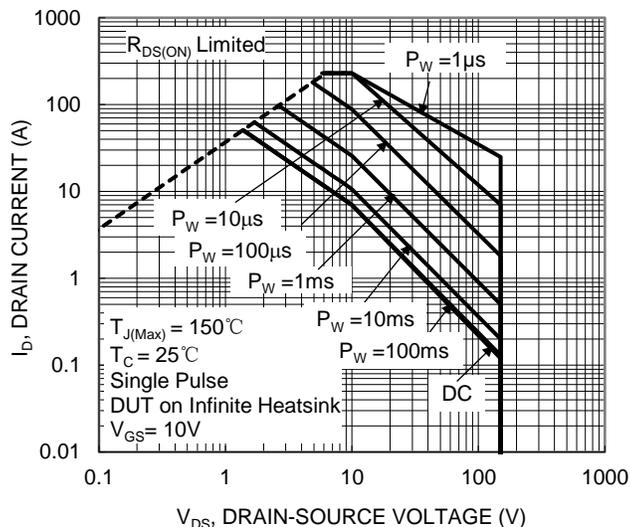


Figure 12. SOA, Safe Operation Area

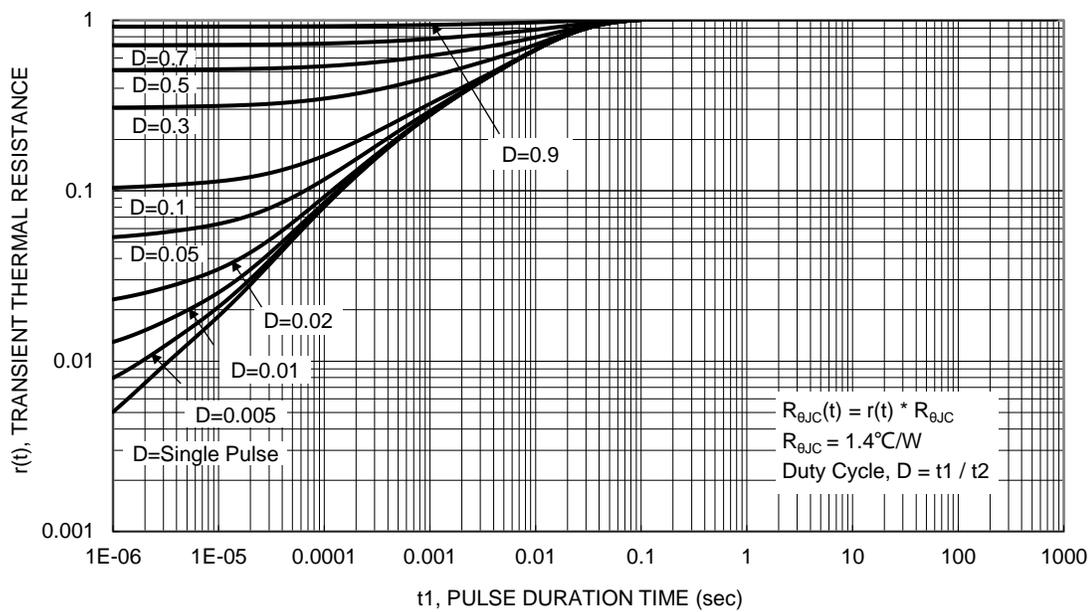
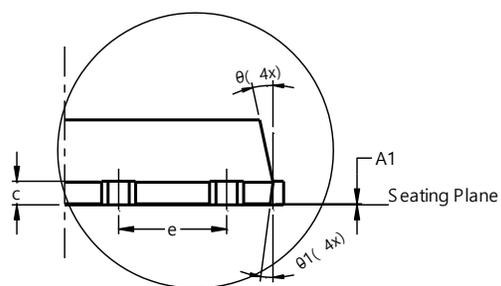
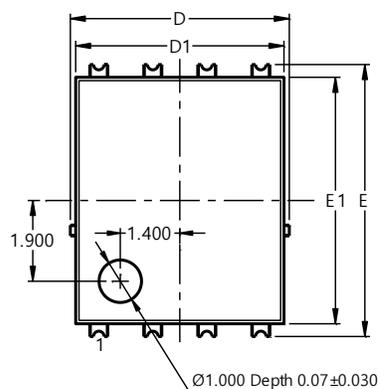
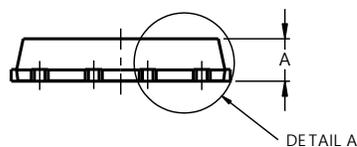
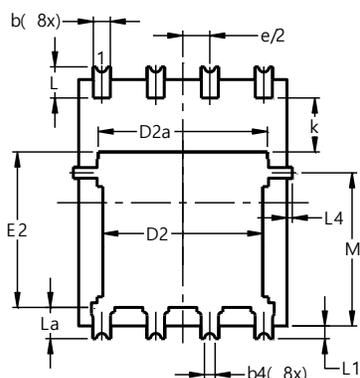


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

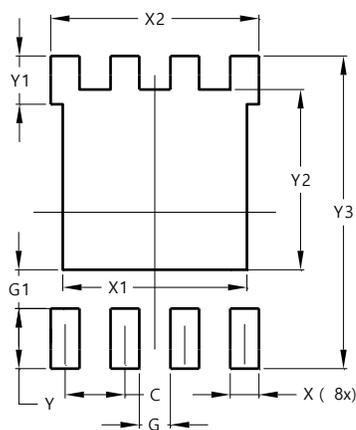
**PowerDI5060-8 (SWP) (Type UX)**


DETAIL A



| PowerDI5060-8 (SWP)<br>(Type UX) |          |       |       |
|----------------------------------|----------|-------|-------|
| Dim                              | Min      | Max   | Typ   |
| A                                | 0.90     | 1.10  | 1.00  |
| A1                               | 0        | 0.05  | --    |
| b                                | 0.30     | 0.50  | 0.41  |
| b2                               | 0.20     | 0.35  | 0.25  |
| b4                               | 0.25REF  |       |       |
| c                                | 0.230    | 0.330 | 0.277 |
| D                                | 5.15 BSC |       |       |
| D1                               | 4.70     | 5.10  | 4.90  |
| D2                               | 3.56     | 3.96  | 3.76  |
| D2a                              | 3.78     | 4.18  | 3.98  |
| E                                | 6.40 BSC |       |       |
| E1                               | 5.60     | 6.00  | 5.80  |
| E2                               | 3.46     | 3.86  | 3.66  |
| E2a                              | 4.195    | 4.595 | 4.395 |
| e                                | 1.27BSC  |       |       |
| k                                | 1.05     | --    | --    |
| L                                | 0.635    | 0.835 | 0.735 |
| La                               | 0.635    | 0.835 | 0.735 |
| L1                               | 0.200    | 0.400 | 0.300 |
| L1a                              | 0.050REF |       |       |
| L4                               | 0.025    | 0.225 | 0.125 |
| M                                | 3.205    | 4.005 | 3.605 |
| $\theta$                         | 10°      | 12°   | 11°   |
| $\theta_1$                       | 6°       | 8°    | 7°    |
| All Dimensions in mm             |          |       |       |

## Suggested Pad Layout

**PowerDI5060-8 (SWP) (Type UX)**


| Dimensions | Value<br>(in mm) |
|------------|------------------|
| C          | 1.270            |
| G          | 0.660            |
| G1         | 0.820            |
| X          | 0.610            |
| X1         | 4.100            |
| X2         | 4.420            |
| Y          | 1.270            |
| Y1         | 1.020            |
| Y2         | 3.810            |
| Y3         | 6.610            |