



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Product Summary

BV_{DSS}	$R_{DS(ON)}$	I_D $T_C = +25^\circ C$
30V	2.0m Ω @ $V_{GS} = 10V$	100A
	3.0m Ω @ $V_{GS} = 4.5V$	100A

Features

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Thermally Efficient Package-Cooler Running Applications
- <1.1mm Package Profile – Ideal for Thin Applications
- High Conversion Efficiency
- Low $R_{DS(ON)}$ – Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection

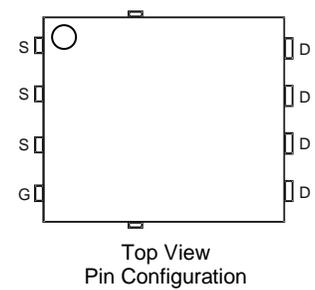
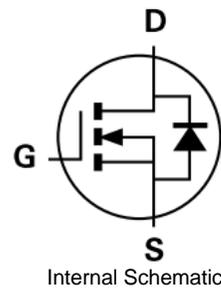
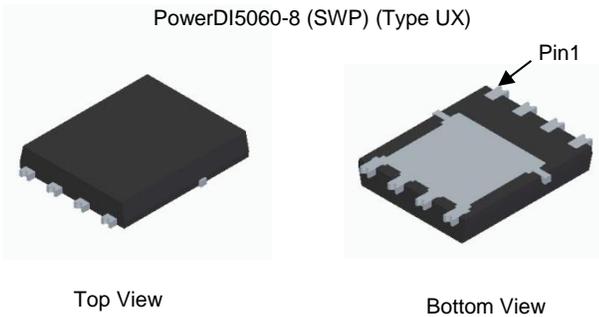
Description and Applications

This new generation MOSFET is designed to minimize $R_{DS(ON)}$ yet maintain superior switching performance. This device is ideal for use in power management and load switch.

- DC-DC Converters
- Load Switch

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 
- Weight: 0.097 grams (Approximate)



Maximum Ratings (@T_C = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 6)	Steady State	T _C = +25°C	I _D	100	A
		T _C = +100°C		100	
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	80	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	350	A
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)			I _{SM}	350	A
Avalanche Current, L=0.1mH			I _{AS}	50	A
Avalanche Energy, L=0.1mH			E _{AS}	140	mJ

Thermal Characteristics (@T_C = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{θJA}	54	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	P _D	100	W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	1.5	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_C = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 24V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	V _{DS} = V _{GS} , I _D = 1mA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.6	2.0	mΩ	V _{GS} = 10V, I _D = 30A
		—	2.3	3.0		V _{GS} = 4.5V, I _D = 30A
Diode Forward Voltage	V _{SD}	—	0.8	1.1	V	V _{GS} = 0V, I _S = 30A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{ISS}	—	4389	—	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{OSS}	—	1529	—		
Reverse Transfer Capacitance	C _{RSS}	—	180	—		
Gate Resistance	R _G	—	0.6	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _G	—	34	—	nC	V _{DS} = 15V, I _D = 20A
Total Gate Charge (V _{GS} = 10V)	Q _G	—	68	—		
Gate-Source Charge	Q _{GS}	—	8	—		
Gate-Drain Charge	Q _{GD}	—	15	—		
Turn-On Delay Time	t _{D(ON)}	—	7.2	—	ns	V _{DD} = 15V, V _{GS} = 10V, I _D = 15A, R _G = 3Ω
Turn-On Rise Time	t _R	—	13.2	—		
Turn-Off Delay Time	t _{D(OFF)}	—	37.5	—		
Turn-Off Fall Time	t _F	—	23.9	—		
Body Diode Reverse Recovery Time	t _{RR}	—	28.7	—	ns	I _S = 15A, di/dt = 500A/μs
Body Diode Reverse Recovery Charge	Q _R	—	45.8	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

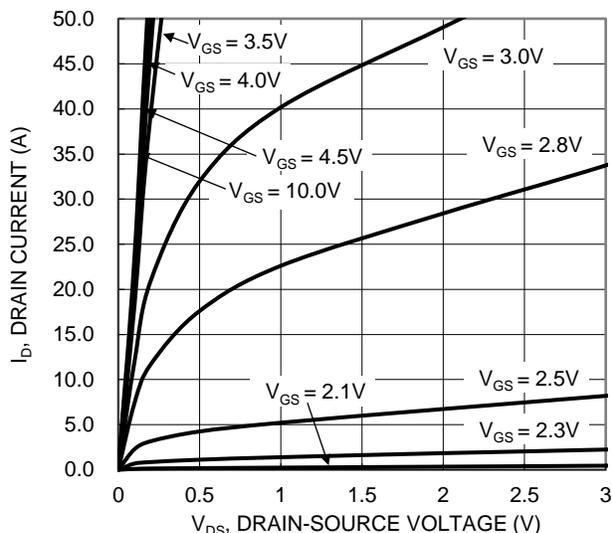


Figure 1. Typical Output Characteristic

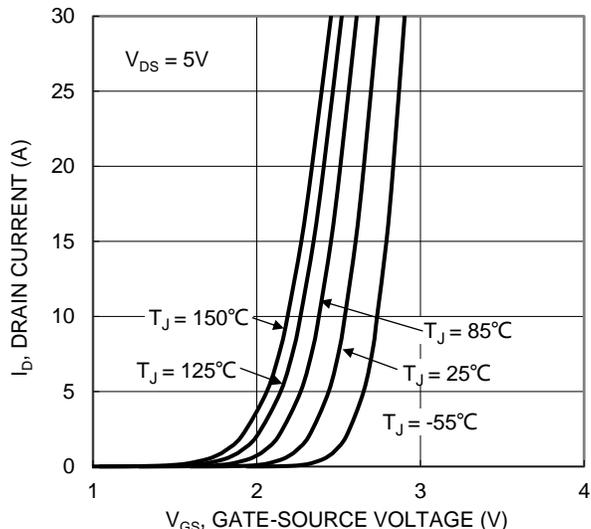


Figure 2. Typical Transfer Characteristic

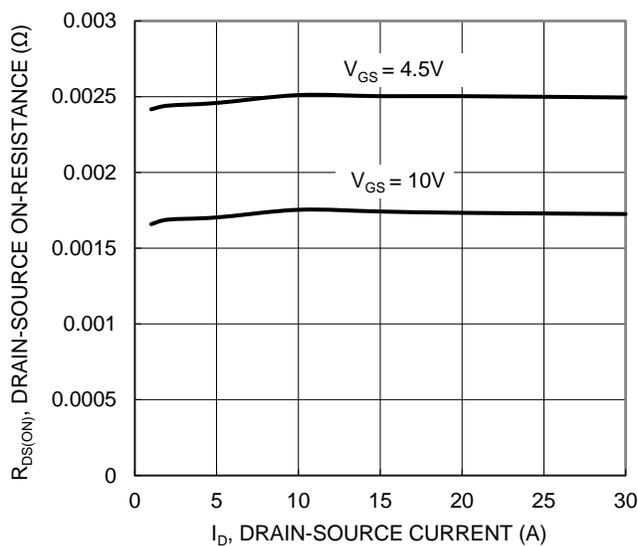


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

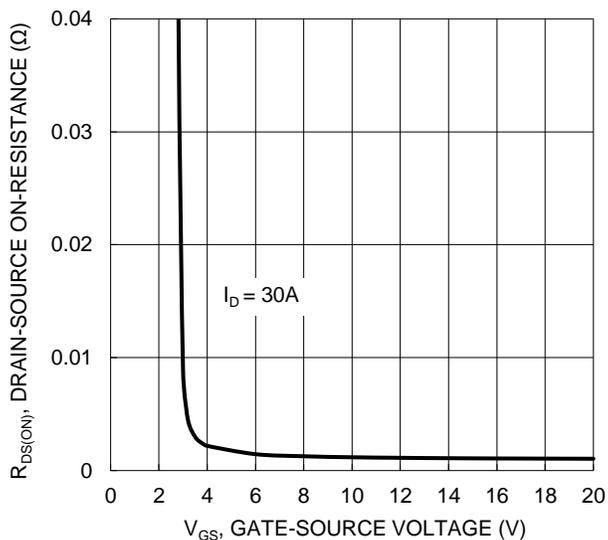


Figure 4. Typical Transfer Characteristic

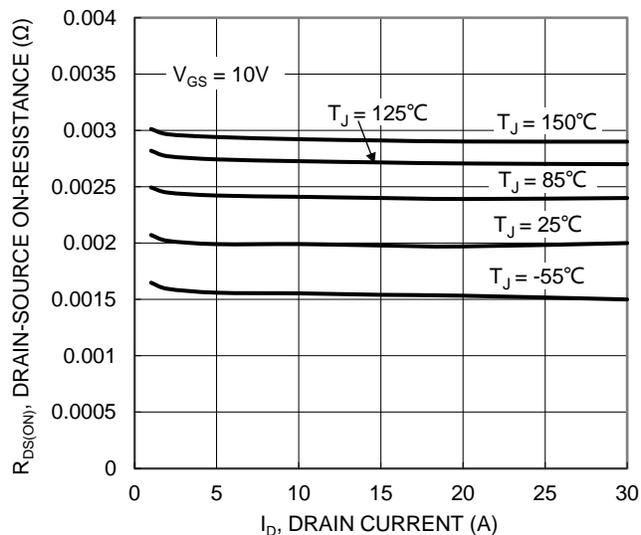


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

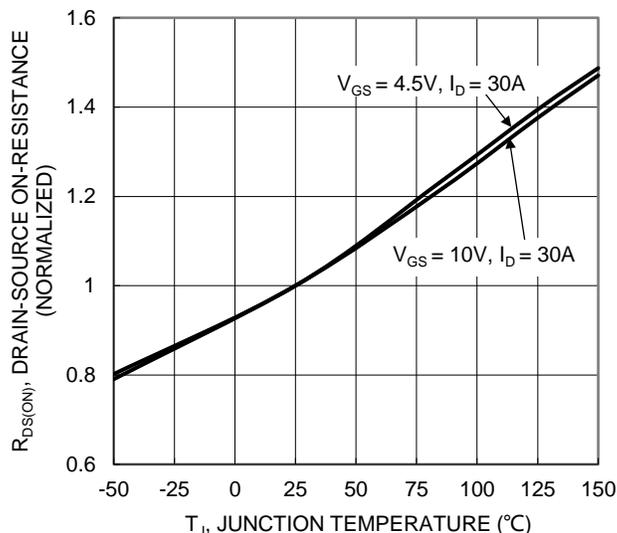


Figure 6. On-Resistance Variation with Temperature

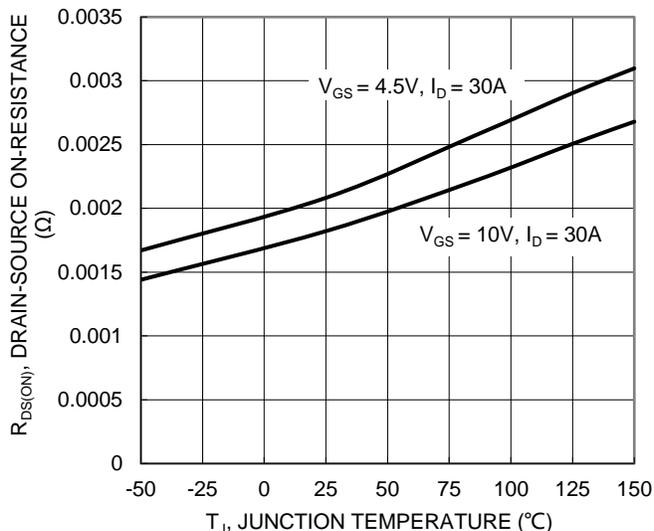


Figure 7. On-Resistance Variation with Temperature

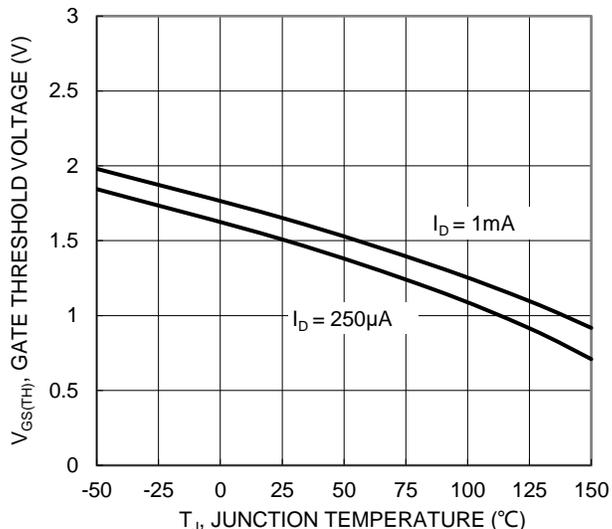


Figure 8. Gate Threshold Variation vs. Junction Temperature

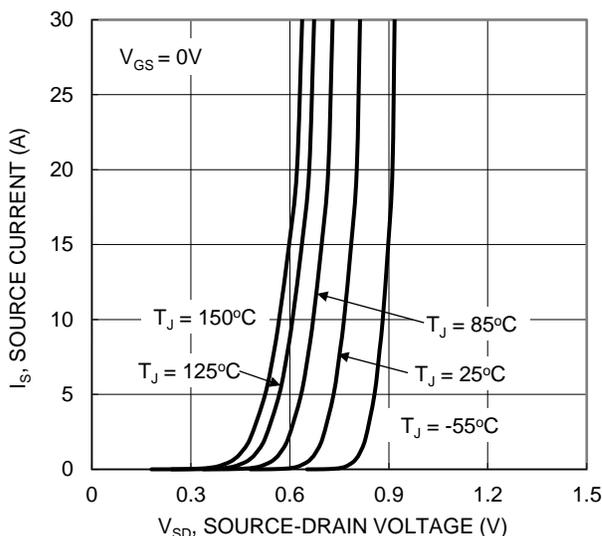


Figure 9. Diode Forward Voltage vs. Current

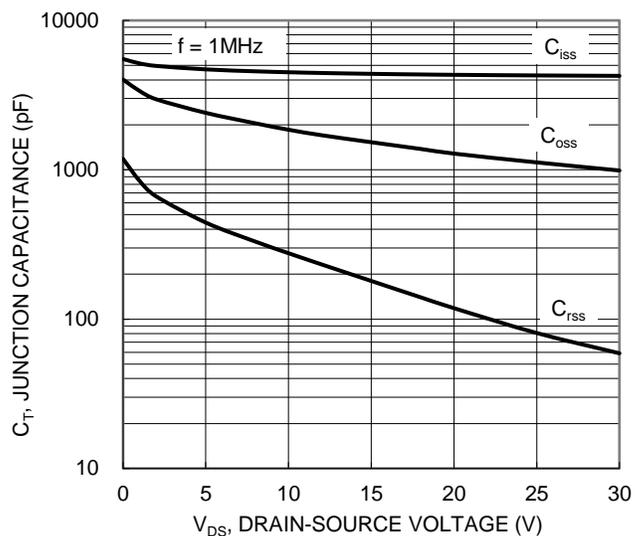


Figure 10. Typical Junction Capacitance

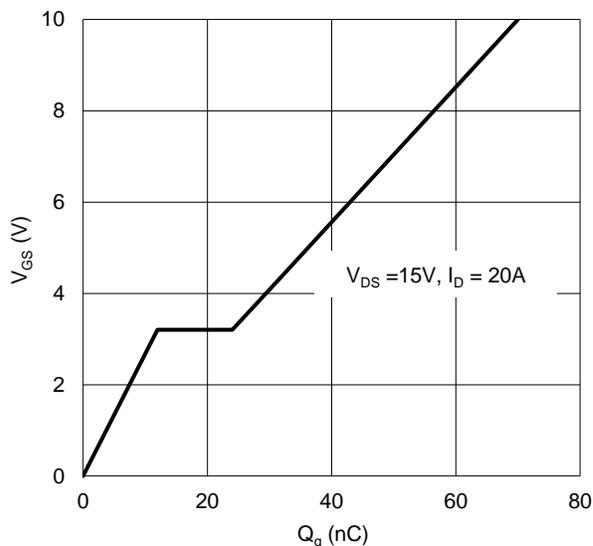


Figure 11. Gate Charge

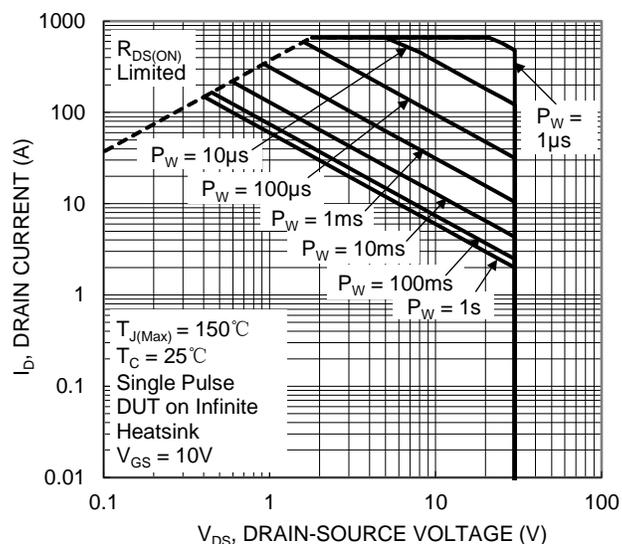


Figure 12. SOA, Safe Operation Area

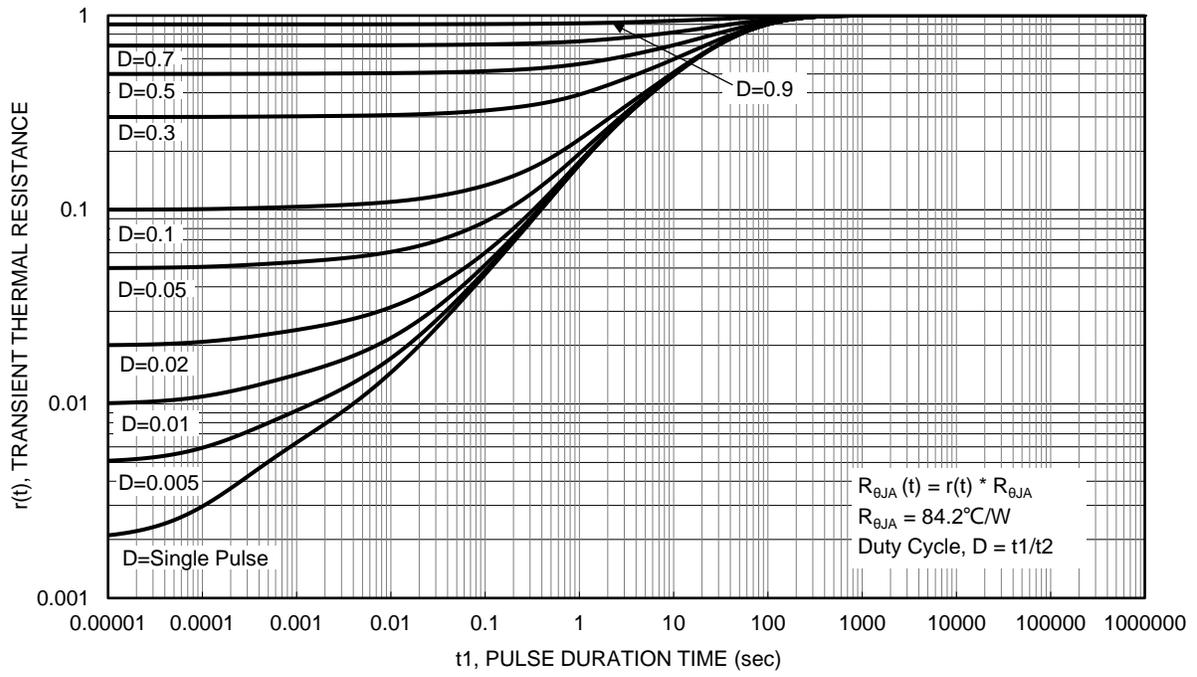
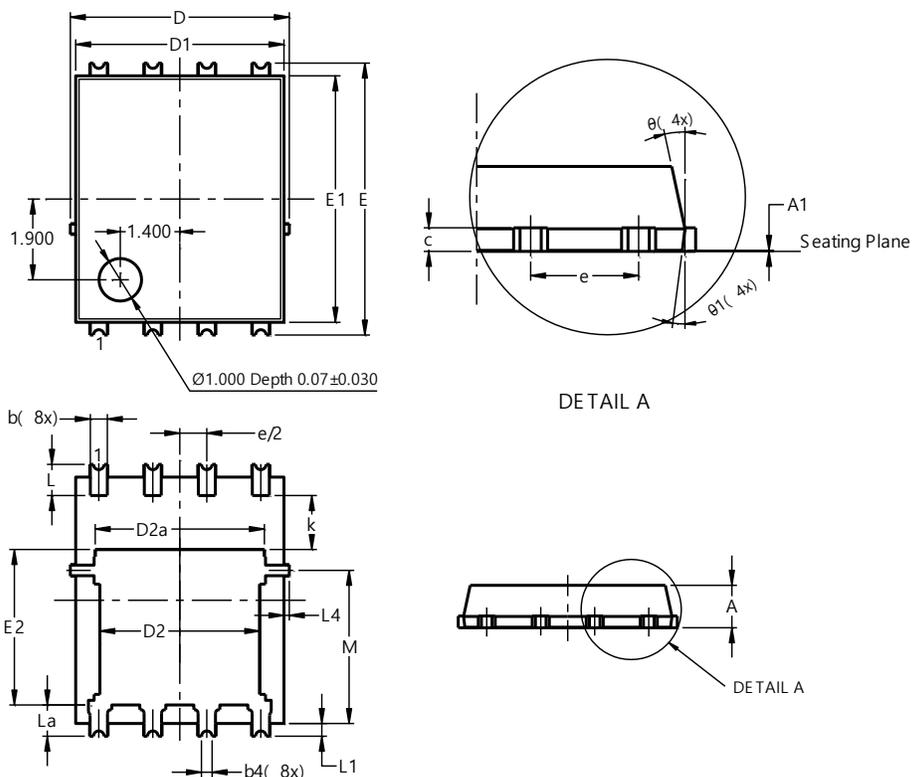


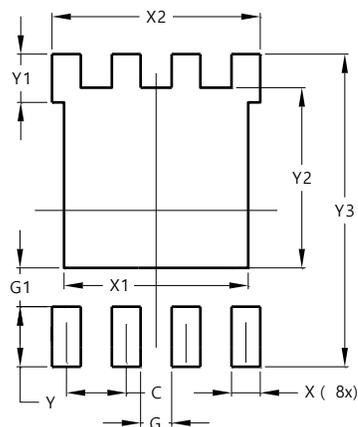
Figure 13. Transient Thermal Resistance

Package Outline Dimensions

PowerDI5060-8 (SWP) (Type UX)


PowerDI5060-8 (SWP) (Type UX)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

PowerDI5060-8 (SWP) (Type UX)


Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610