优尚

YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max $T_A = +25°C$
60V	11mΩ @ V_{GS} = 10V	10.6A
	14.5mΩ @ V_{GS} = 4.5V	9.5A

Features and Benefits

- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low $R_{DS(ON)}$ – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate

Description and Applications

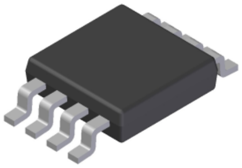
This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

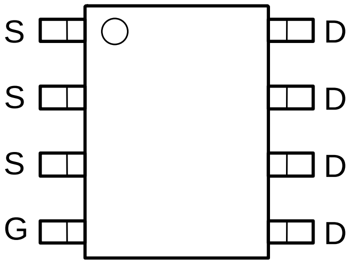
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ⓔ3
- Weight: 0.074 grams (Approximate)

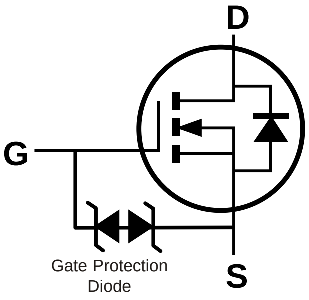
SO-8



Top View



Top View
Pin-Out



Equivalent Circuit

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 6) V_{GS} = 10V	T_A = +25°C T_A = +70°C	I_D	10.6 8.5	A
Maximum Continuous Body Diode Forward Current (Note 6)		I_S	2.1	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	85	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I_{SM}	85	A
Avalanche Current, L = 0.3mH		I_{AS}	16.2	A
Avalanche Energy, L = 0.3mH		E_{AS}	39.4	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	89.2	°C/W
Total Power Dissipation (Note 6)	T_A = +25°C	P_D	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	58.3	°C/W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	7.7	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	V_{GS} = 0V, I_D = 1mA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	µA	V_{DS} = 48V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±10	µA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1.4	—	2.5	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	8.1	11	mΩ	V_{GS} = 10V, I_D = 10A
		—	11.5	14.5		V_{GS} = 4.5V, I_D = 5A
Diode Forward Voltage	V_{SD}	—	0.7	1.2	V	V_{GS} = 0V, I_S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	1072	—	pF	V_{DS} = 30V, V_{GS} = 0V, f = 1MHz
Output Capacitance	C_{oss}	—	382	—		
Reverse Transfer Capacitance	C_{rss}	—	38	—		
Gate Resistance	R_g	—	1.4	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = 4.5V)	Q_g	—	11.8	—	nC	V_{DS} = 30V, I_D = 10A
Total Gate Charge (V_{GS} = 10V)	Q_g	—	22.2	—		
Gate-Source Charge	Q_{gs}	—	3.8	—		
Gate-Drain Charge	Q_{gd}	—	5.0	—		
Turn-On Delay Time	$t_{D(ON)}$	—	8.2	—	ns	V_{GS} = 10V, V_{DS} = 30V, R_G = 6Ω, I_D = 10A
Turn-On Rise Time	t_R	—	3.9	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	21.2	—		
Turn-Off Fall Time	t_F	—	15.7	—		
Body Diode Reverse Recovery Time	t_{RR}	—	30.6	—	ns	I_F = 10A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}	—	21.9	—	nC	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

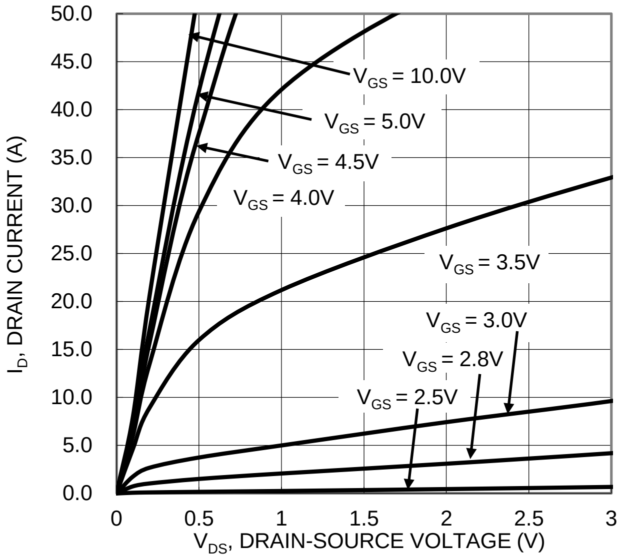


Figure 1. Typical Output Characteristic

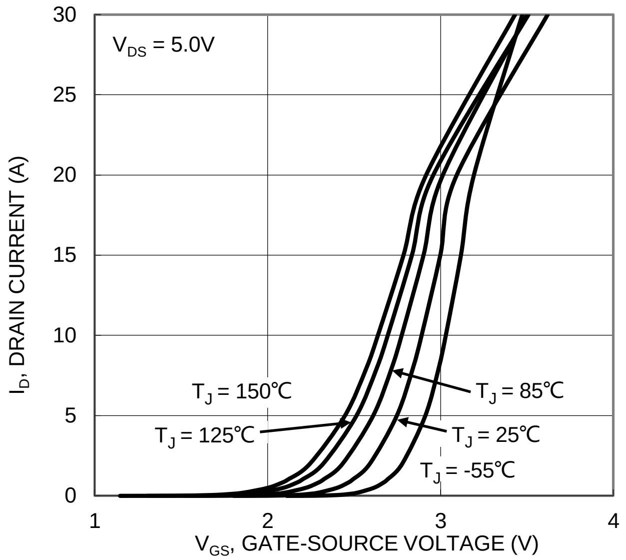


Figure 2. Typical Transfer Characteristic

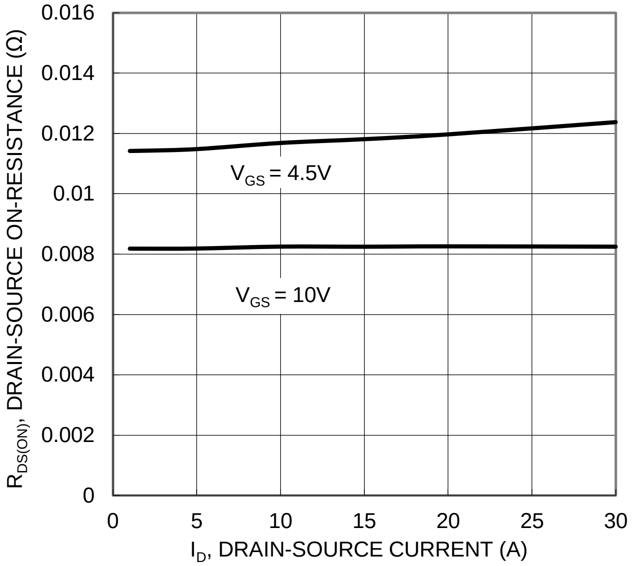


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

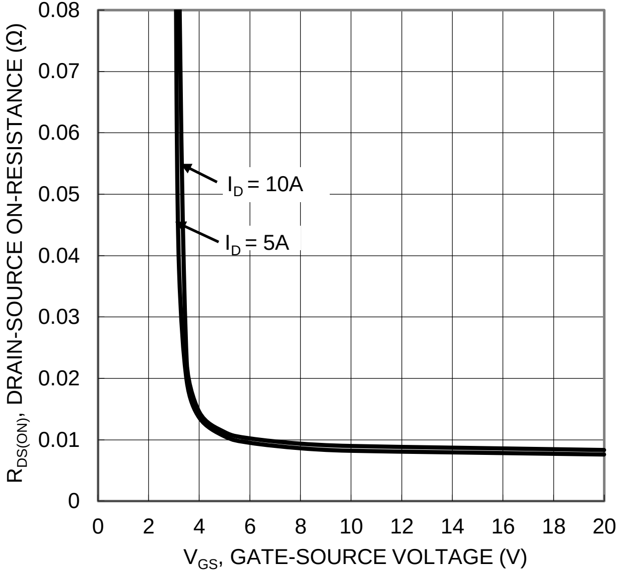


Figure 4. Typical Transfer Characteristic

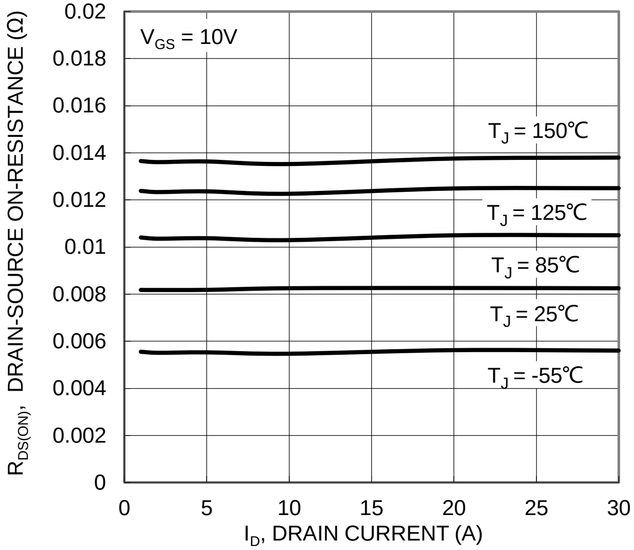


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

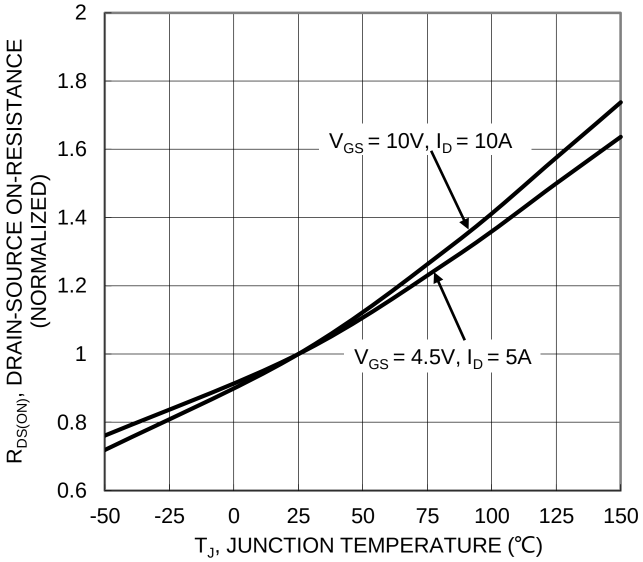


Figure 6. On-Resistance Variation with Temperature

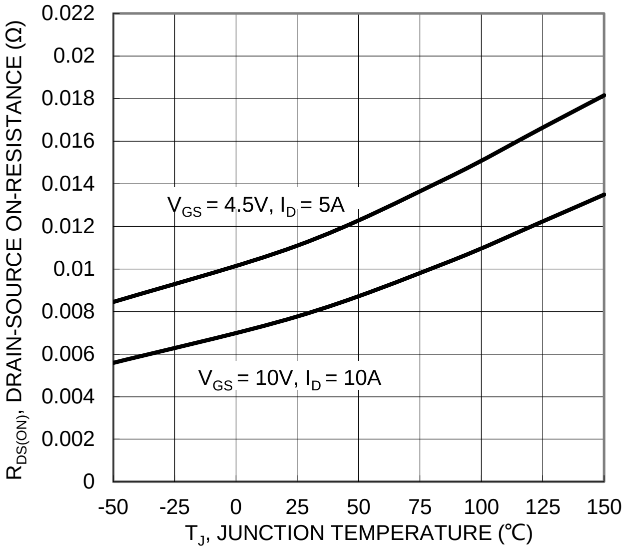


Figure 7. On-Resistance Variation with Temperature

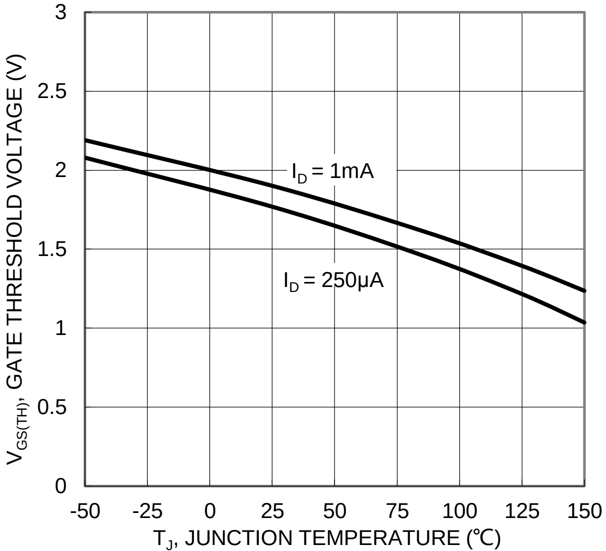


Figure 8. Gate Threshold Variation vs. Junction Temperature

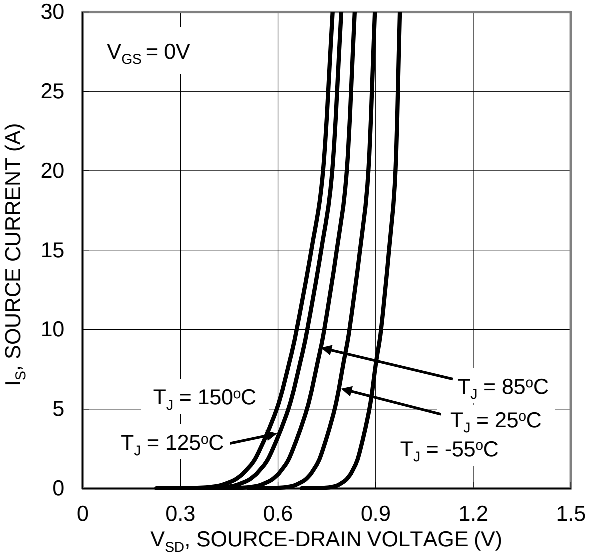


Figure 9. Diode Forward Voltage vs. Current

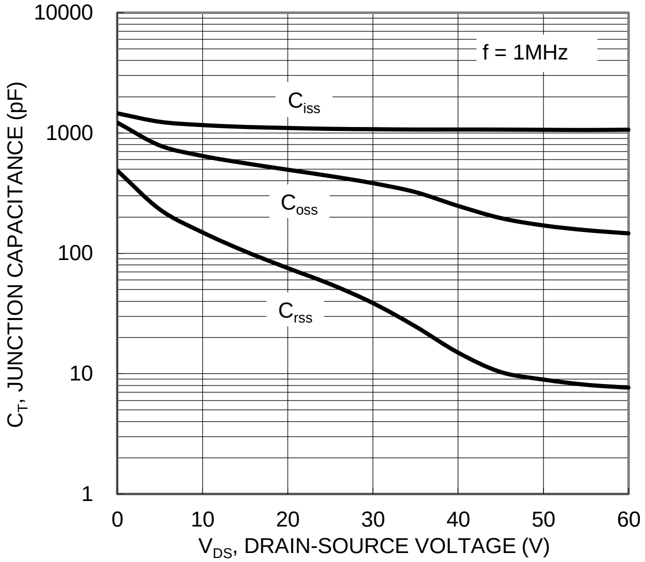


Figure 10. Typical Junction Capacitance

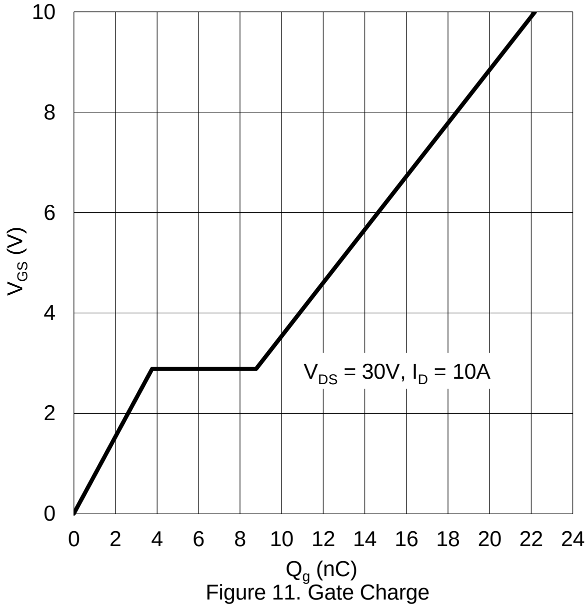


Figure 11. Gate Charge

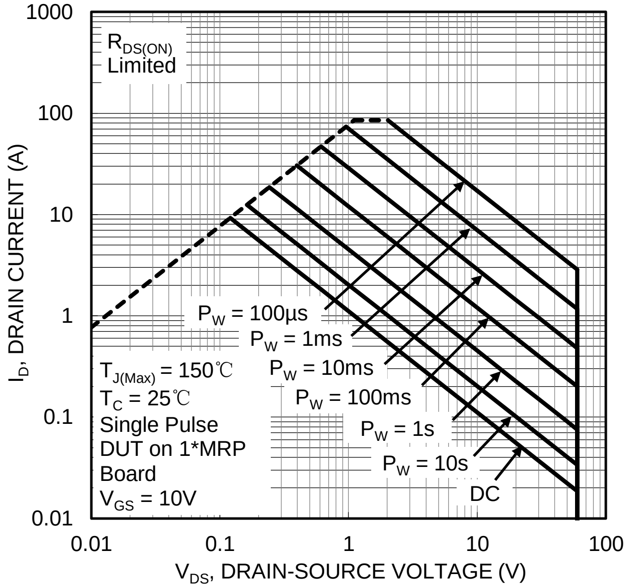


Figure 12. SOA, Safe Operation Area

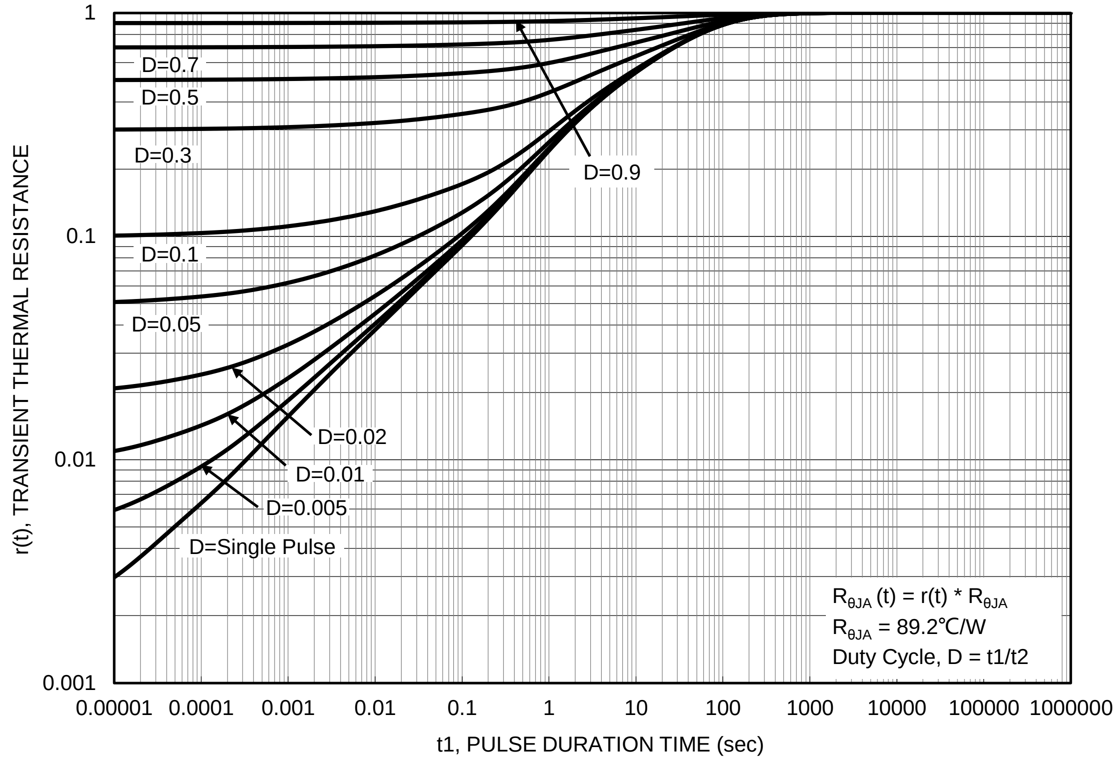
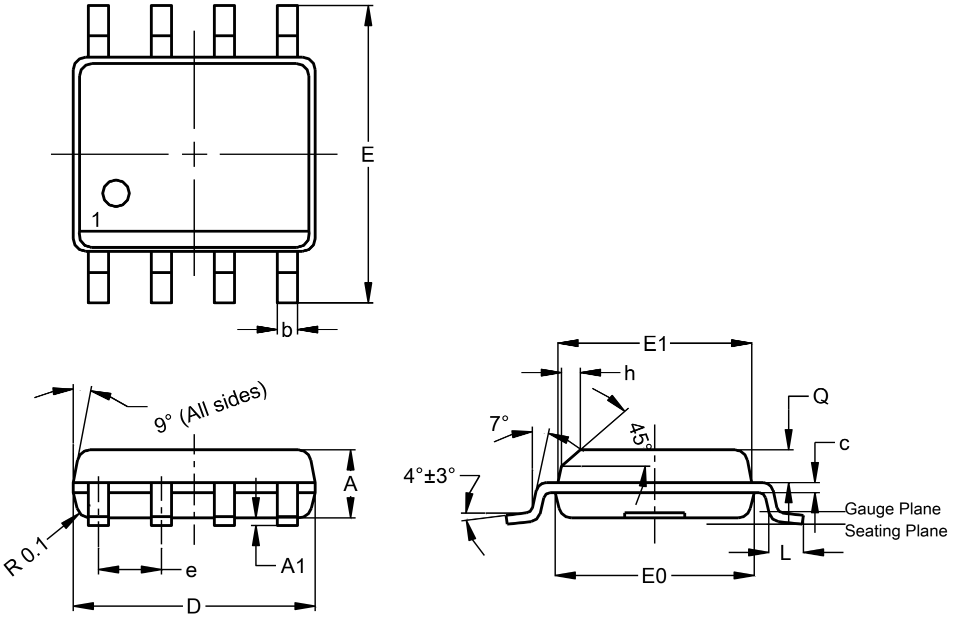


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

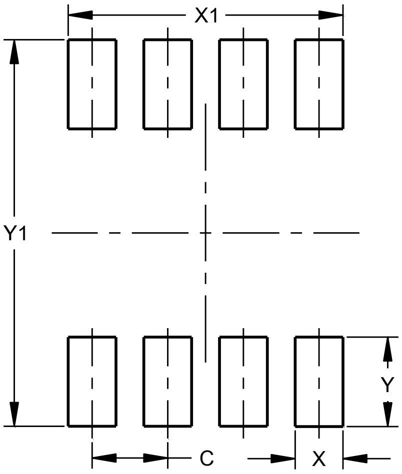
SO-8



SO-8			
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	--	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50