



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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企业微信二维码



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Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
100V	32mΩ @ V _{GS} = 10V	7.2A
	50mΩ @ V _{GS} = 4.5V	6.1A

Features and Benefits


- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} – Ensures On-State Losses are Minimized
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection

Description and Applications

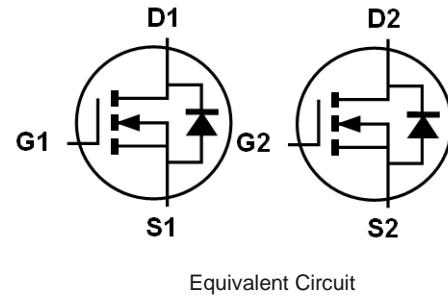
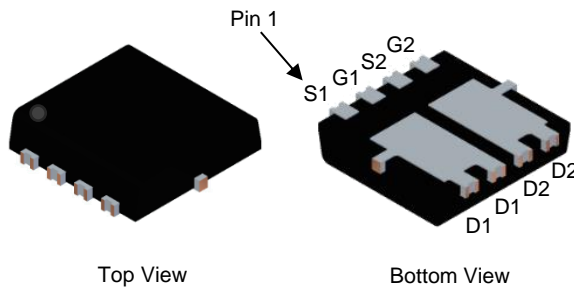
This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Wireless charging
- DC-DC converters
- Power managements

Mechanical Data

- Package: POWERDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 
- Weight: 0.03 grams (Approximate)

POWERDI[®]3333-8/SWP (Type UXD)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current, V _{GS} = 10V (Note 5)	Steady State I _D	T _A = +25°C	7.2	A
		T _A = +100°C	5.1	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	39	A	
Maximum Continuous Body Diode Forward Current (Note 5)	I _S	3.1	A	
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)	I _{SM}	39	A	
Avalanche Current, L = 0.3mH	I _{AS}	13	A	
Avalanche Energy, L = 0.3mH	E _{AS}	25.3	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P _D	1.5	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	102	°C/W
Total Power Dissipation (Note 5)	P _D	3.3	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	45	°C/W
Thermal Resistance, Junction to Case (Note 5)	R _{θJC}	4	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 80V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.3	—	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	25	32	mΩ	V _{GS} = 10V, I _D = 5A
		—	36	50	mΩ	V _{GS} = 4.5V, I _D = 5A
Diode Forward Voltage	V _{SD}	—	0.8	1.0	V	V _{GS} = 0V, I _S = 5A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{ISS}	—	683	—	pF	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{OSS}	—	165	—	pF	
Reverse Transfer Capacitance	C _{RSS}	—	6.9	—	pF	
Gate Resistance	R _g	—	1.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	6.3	—	nC	V _{DS} = 50V, I _D = 6A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	11.9	—	nC	
Gate-Source Charge	Q _{gs}	—	2.0	—	nC	
Gate-Drain Charge	Q _{gd}	—	3.1	—	nC	
Turn-On Delay Time	t _{d(ON)}	—	4.1	—	ns	V _{DS} = 50V, I _D = 5.85A V _{GS} = 10V, R _{GEN} = 3Ω
Turn-On Rise Time	t _r	—	4.5	—	ns	
Turn-Off Delay Time	t _{d(OFF)}	—	12.5	—	ns	
Turn-Off Fall Time	t _f	—	9.3	—	ns	I _F = 6A, di/dt = 500A/μs
Reverse Recovery Time	t _{RR}	—	31.5	—	ns	
Reverse Recovery Charge	Q _{RR}	—	94.6	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

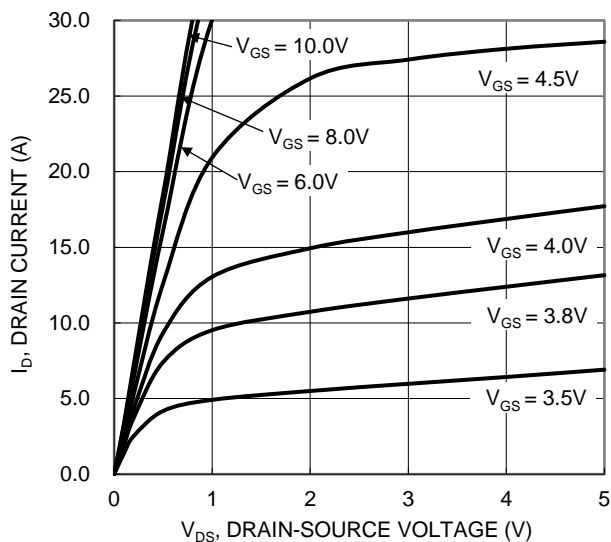


Figure 1. Typical Output Characteristic

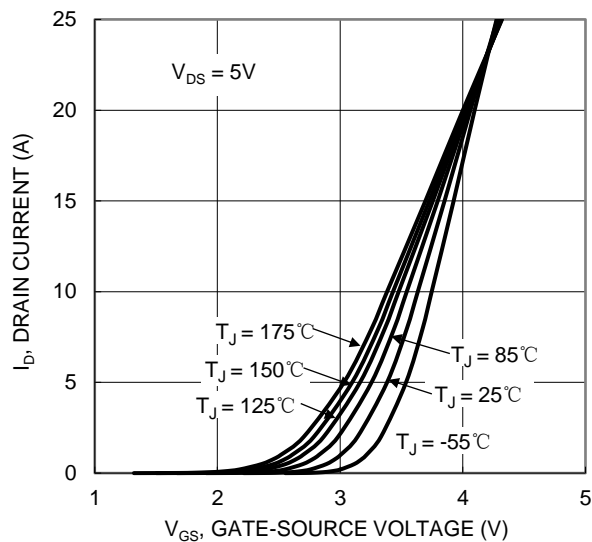


Figure 2. Typical Transfer Characteristic

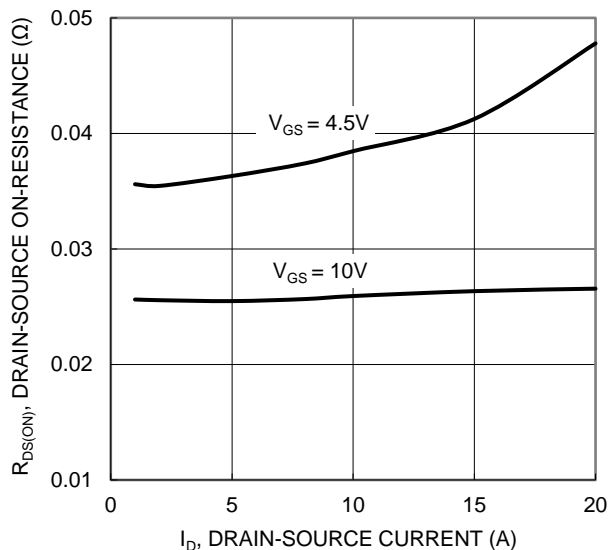


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

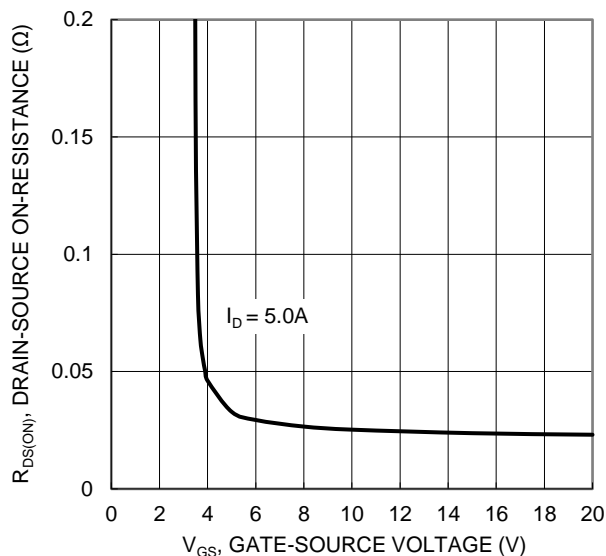


Figure 4. Typical Transfer Characteristic

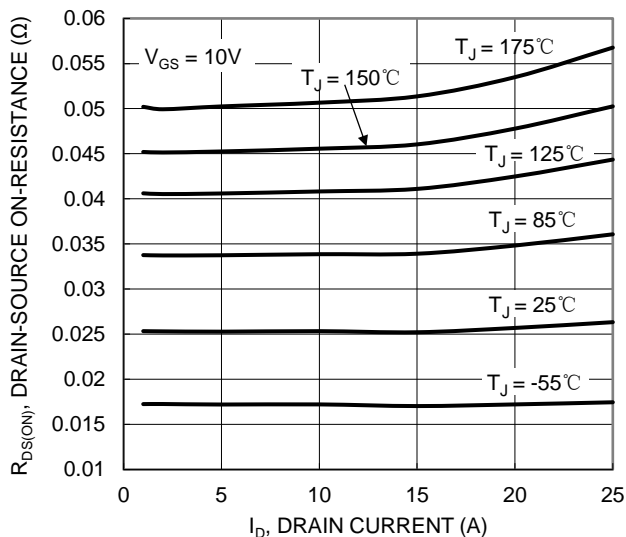


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

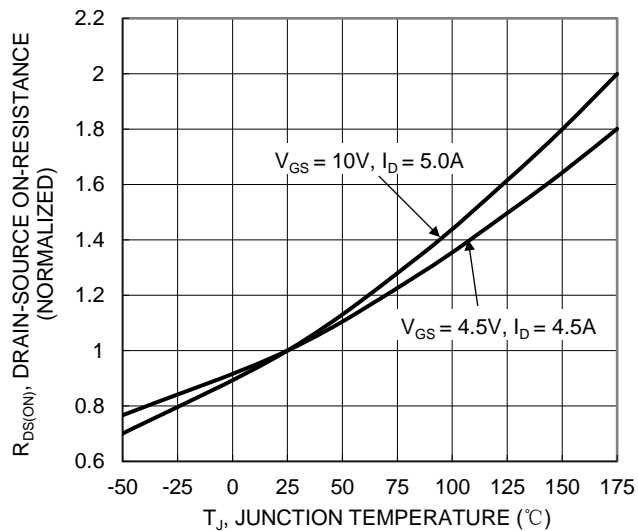


Figure 6. On-Resistance Variation with Temperature

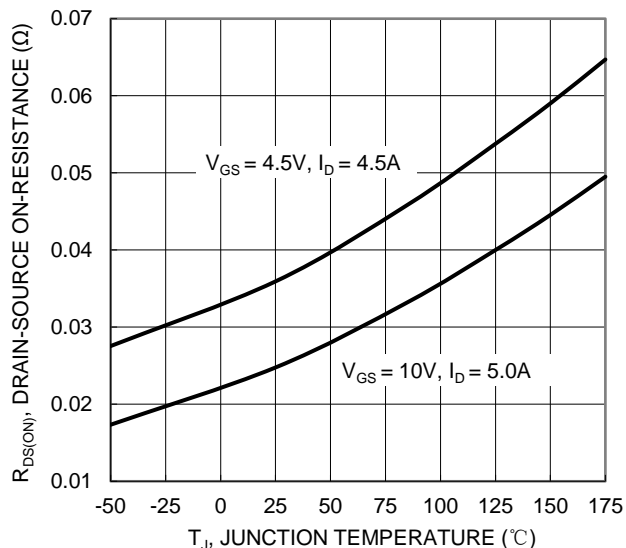


Figure 7. On-Resistance Variation with Temperature

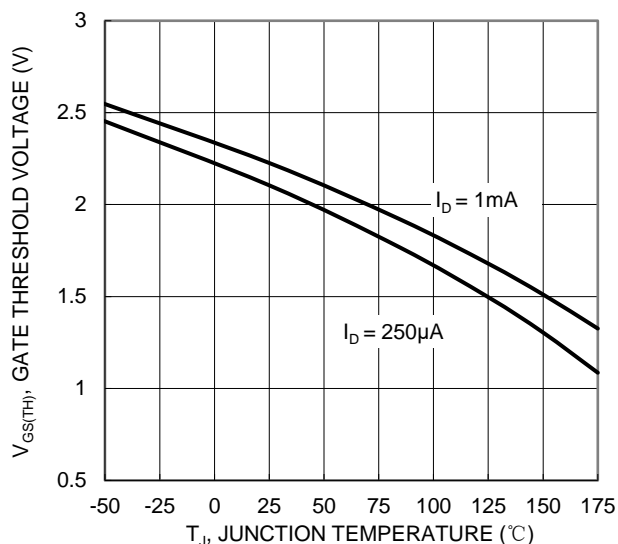


Figure 8. Gate Threshold Variation vs. Junction Temperature

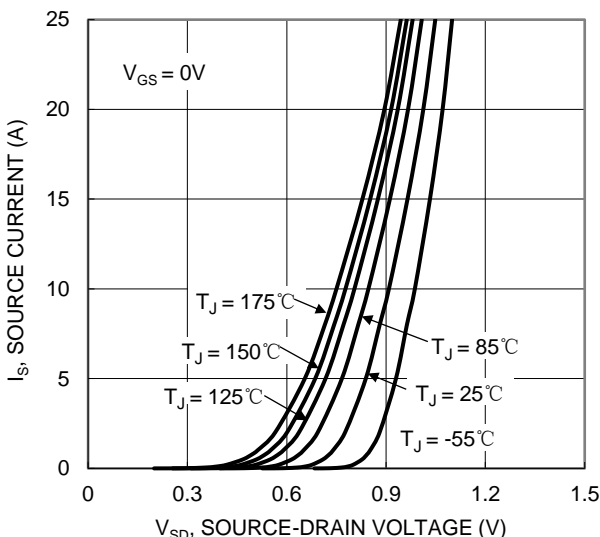


Figure 9. Diode Forward Voltage vs. Current

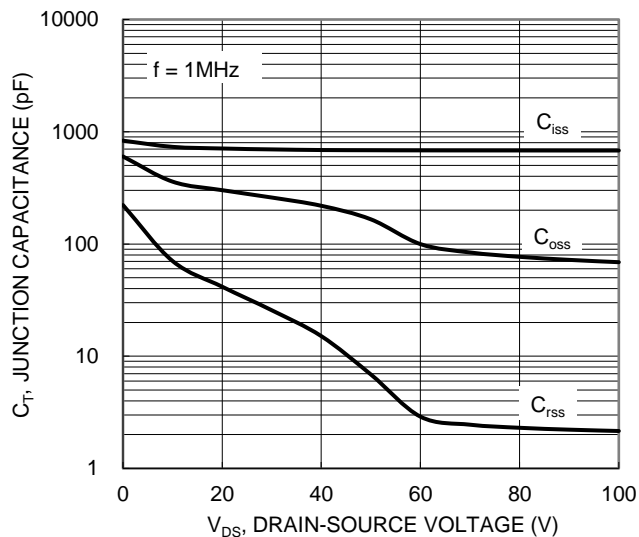


Figure 10. Typical Junction Capacitance

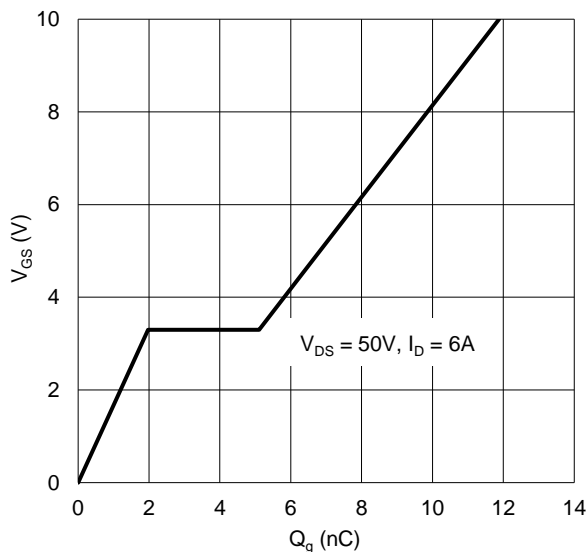


Figure 11. Gate Charge

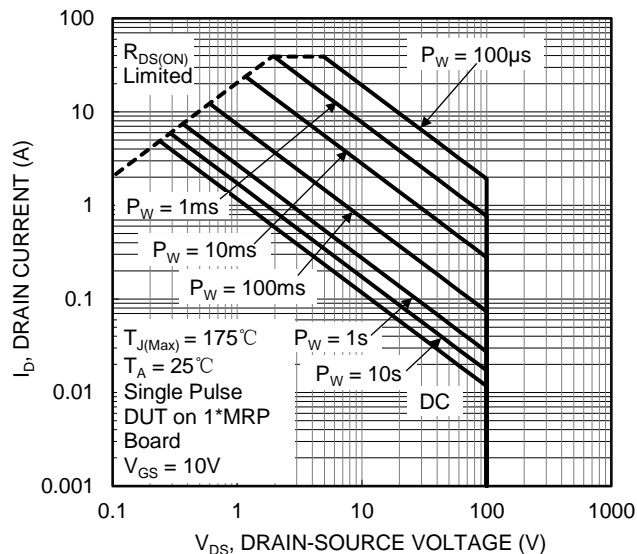


Figure 12. SOA, Safe Operation Area

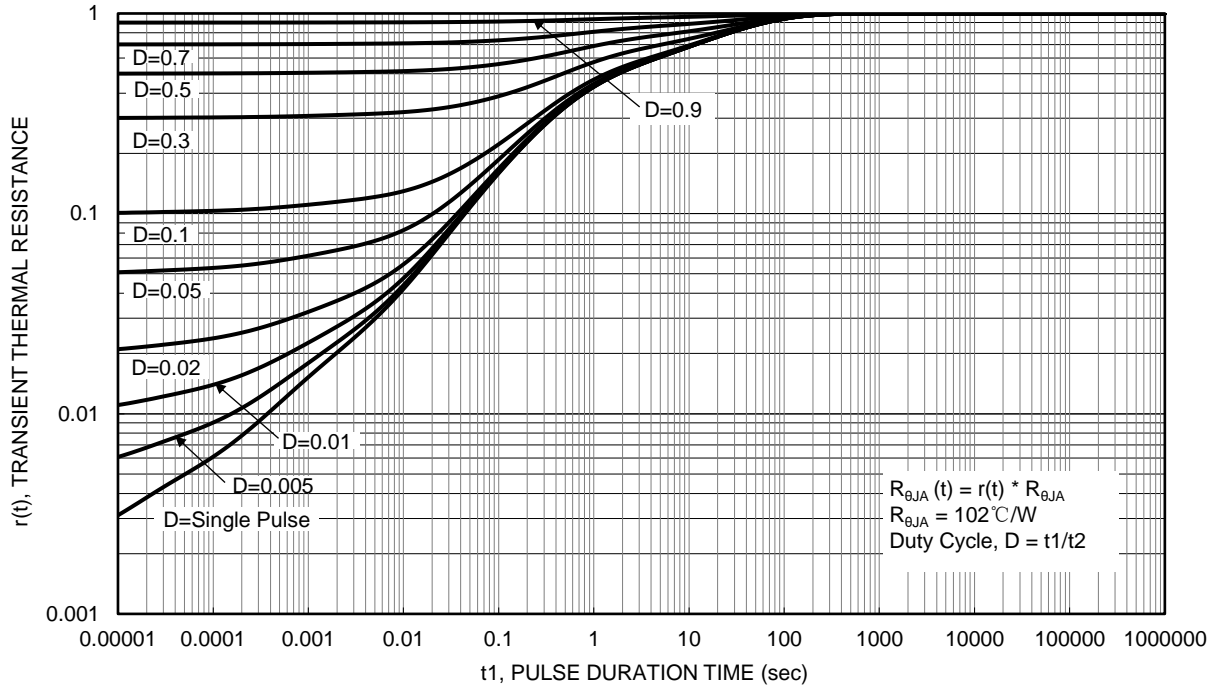
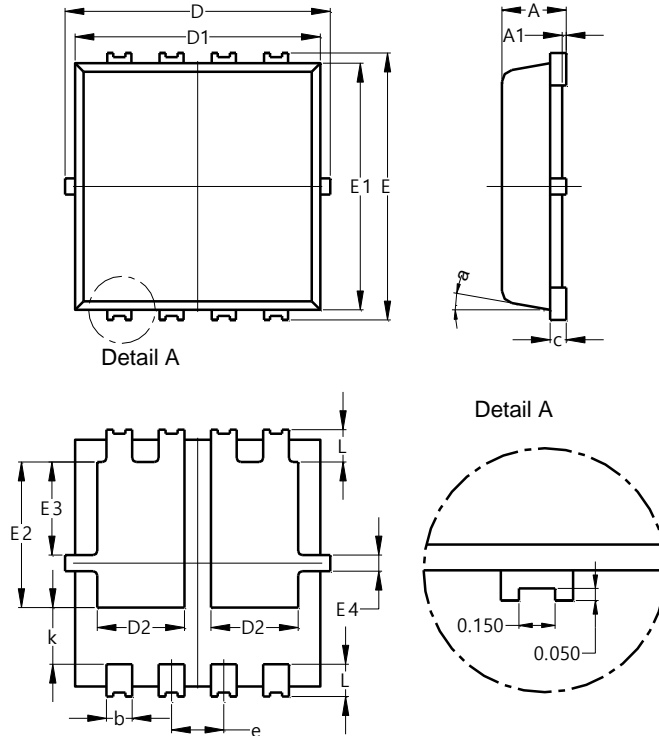


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

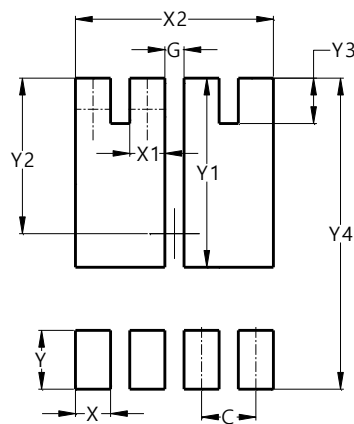
POWERDI®3333-8/SWP (Type UXD)



POWERDI®3333-8/SWP (Type UXD)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	—
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	1.00	1.20	1.10
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E3	0.95	1.35	1.15
E4	0.10	0.30	0.20
e	—	—	0.65
L	0.30	0.50	0.40
k	0.50	0.90	0.70
a	0°	12°	10°
All Dimensions in mm			

Suggested Pad Layout

POWERDI®3333-8/SWP (Type UXD)



Dimensions	Value (in mm)
C	0.650
G	0.230
X	0.420
X1	0.420
X2	2.370
Y	0.700
Y1	2.250
Y2	1.850
Y3	0.540
Y4	3.700