



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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企业微信二维码



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Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D $T_c = +25^\circ C$ (Note 10)
120V	8.9m Ω @ $V_{GS} = 10V$	84A
	16m Ω @ $V_{GS} = 6V$	70A

Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Thermally Efficient Package – Cooler Running Applications
- High Conversion Efficiency
- Low $R_{DS(ON)}$ – Minimizes On-State Losses
- <1.1mm Package Profile – Ideal for Thin Applications

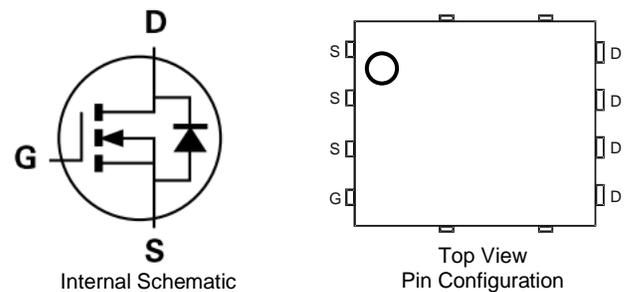
Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Switchings
- DC-DC Converters

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 
- Weight: 0.097 grams (Approximate)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	120	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 6)	T _C = +25°C (Note 10)	I _D	84	A
	T _C = +100°C		60	
Pulsed Drain Current (10μs Pulse, T _C = +25°C, Package Limited)		I _{DM}	336	A
Continuous Body Diode Forward Current (Note 6)		I _S	84	A
Pulsed Body Diode Current (10μs Pulse, T _C = +25°C, Package Limited)		I _{SM}	336	A
Avalanche Current, L = 3mH (Note 9)		I _{AS}	15.5	A
Avalanche Energy, L = 3mH (Note 9)		E _{AS}	360.4	mJ

Thermal Characteristics

Characteristic	Symbol	Value (Typ.)	Unit
Total Power Dissipation (Note 5)	P _D	3.5	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	43	°C/W
Total Power Dissipation (Note 6)	P _D	125	W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	1.2	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	120	—	—	V	V _{GS} = 0V, I _D = 10mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 96V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	7.5	8.9	mΩ	V _{GS} = 10V, I _D = 30A
		—	12	16		V _{GS} = 6V, I _D = 10A
Diode Forward Voltage	V _{SD}	—	0.8	1.2	V	V _{GS} = 0V, I _S = 30A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	3142	—	pF	V _{DS} = 60V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	665	—		
Reverse Transfer Capacitance	C _{rss}	—	29	—		
Gate Resistance	R _G	—	1.9	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 10V)	Q _g	—	44	—	nC	V _{DS} = 60V, I _D = 25A
Gate-Source Charge	Q _{gs}	—	15	—		
Gate-Drain Charge	Q _{gd}	—	9	—		
Turn-On Delay Time	t _{D(ON)}	—	12.5	—	ns	V _{DD} = 60V, V _{GS} = 10V, I _D = 25A, R _G = 2.7Ω
Turn-On Rise Time	t _r	—	13.7	—		
Turn-Off Delay Time	t _{D(OFF)}	—	24.4	—		
Turn-Off Fall Time	t _f	—	10.9	—		
Reverse Recovery Time	t _{RR}	—	55	—	ns	I _F = 25A, di/dt = 100A/μs
Reverse Recovery Charge	Q _{RR}	—	105	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.
 - I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Package limited.

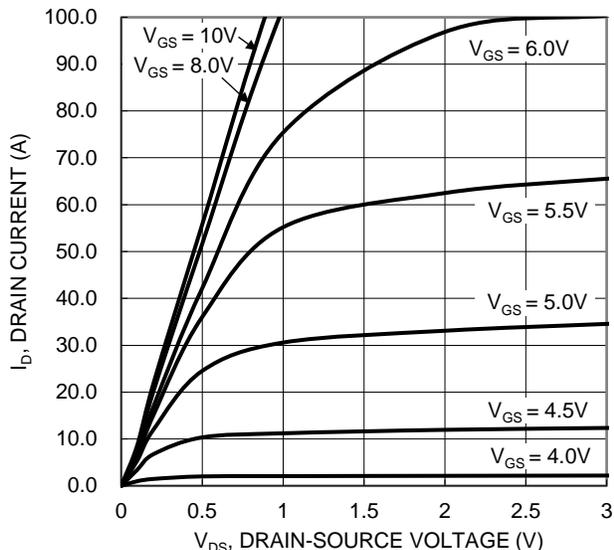


Figure 1. Typical Output Characteristic

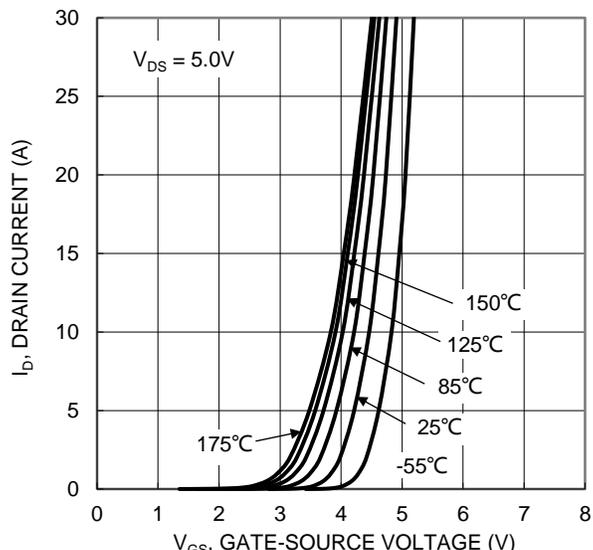


Figure 2. Typical Transfer Characteristic

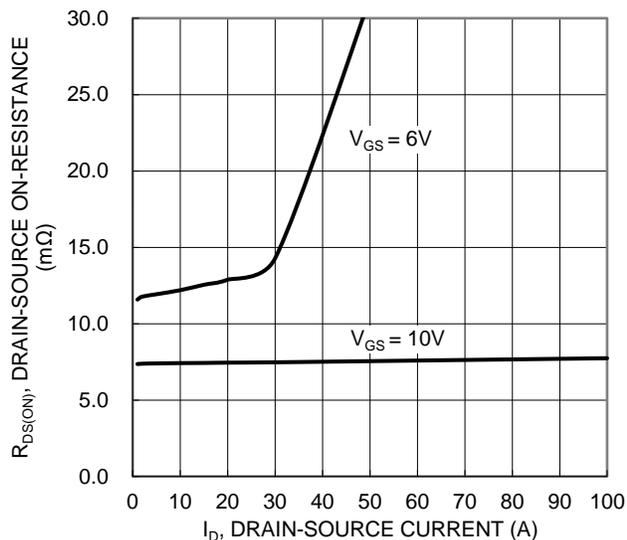


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

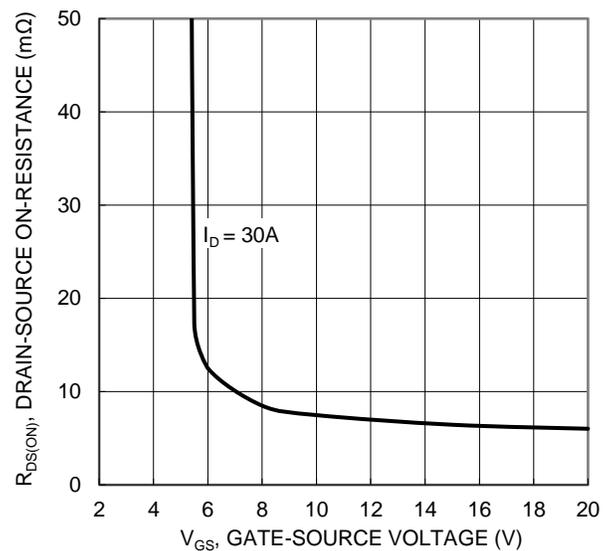


Figure 4. Typical Transfer Characteristic

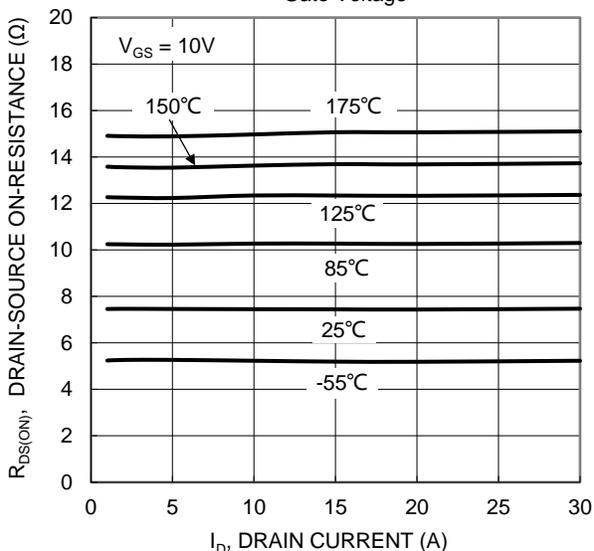


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

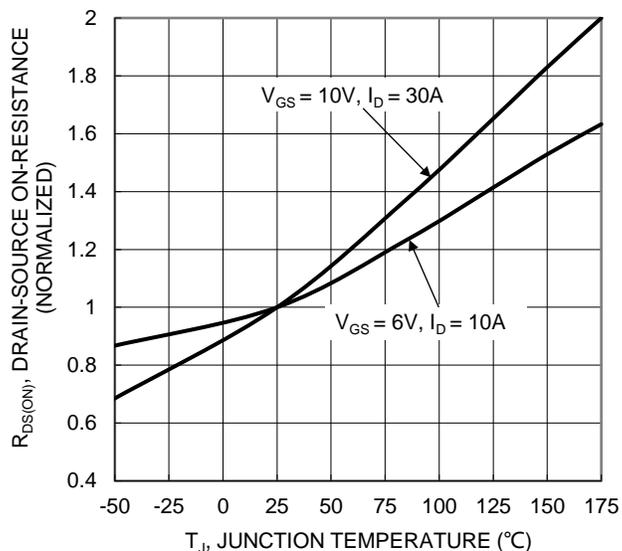


Figure 6. On-Resistance Variation with Temperature

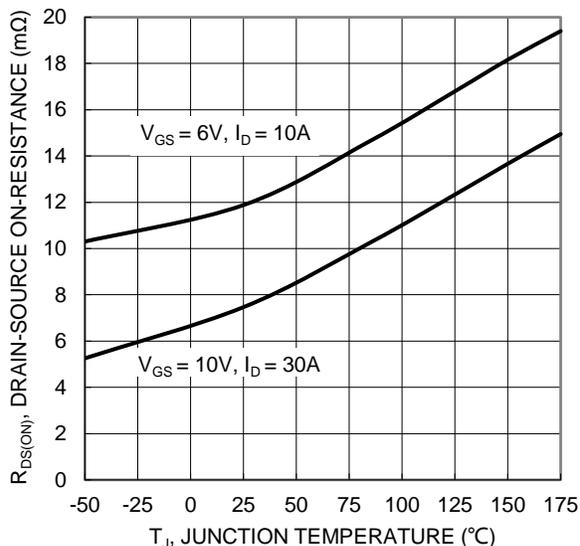


Figure 7. On-Resistance Variation with Temperature

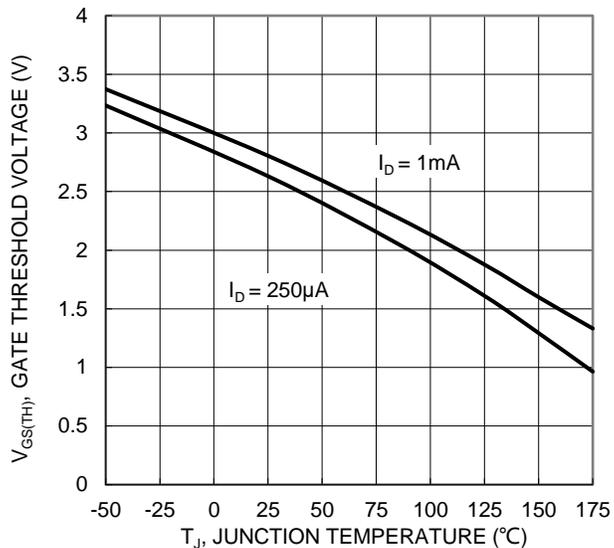


Figure 8. Gate Threshold Variation vs. Junction Temperature

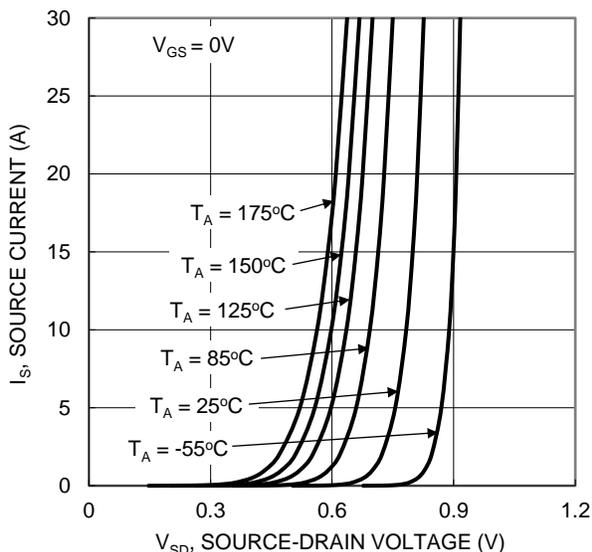


Figure 9. Diode Forward Voltage vs. Current

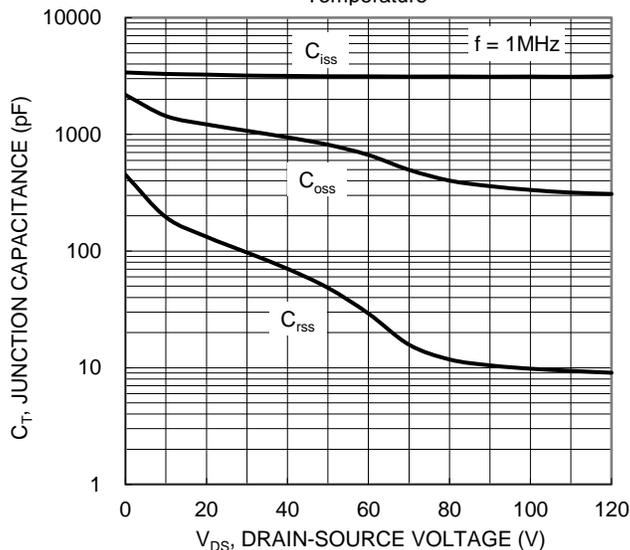


Figure 10. Typical Junction Capacitance

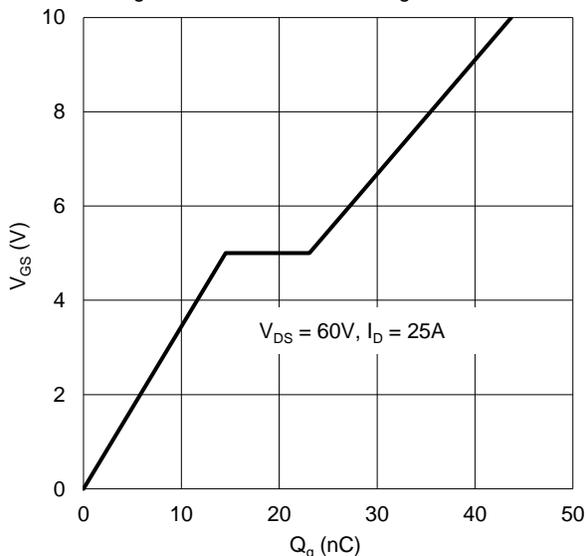


Figure 11. Gate Charge

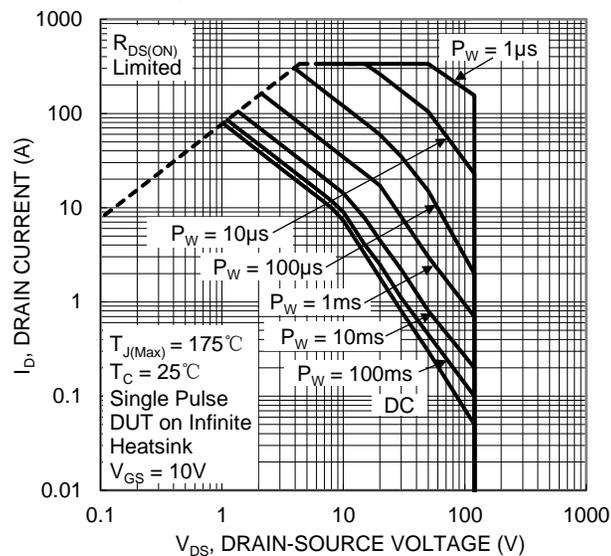


Figure 12. SOA, Safe Operation Area

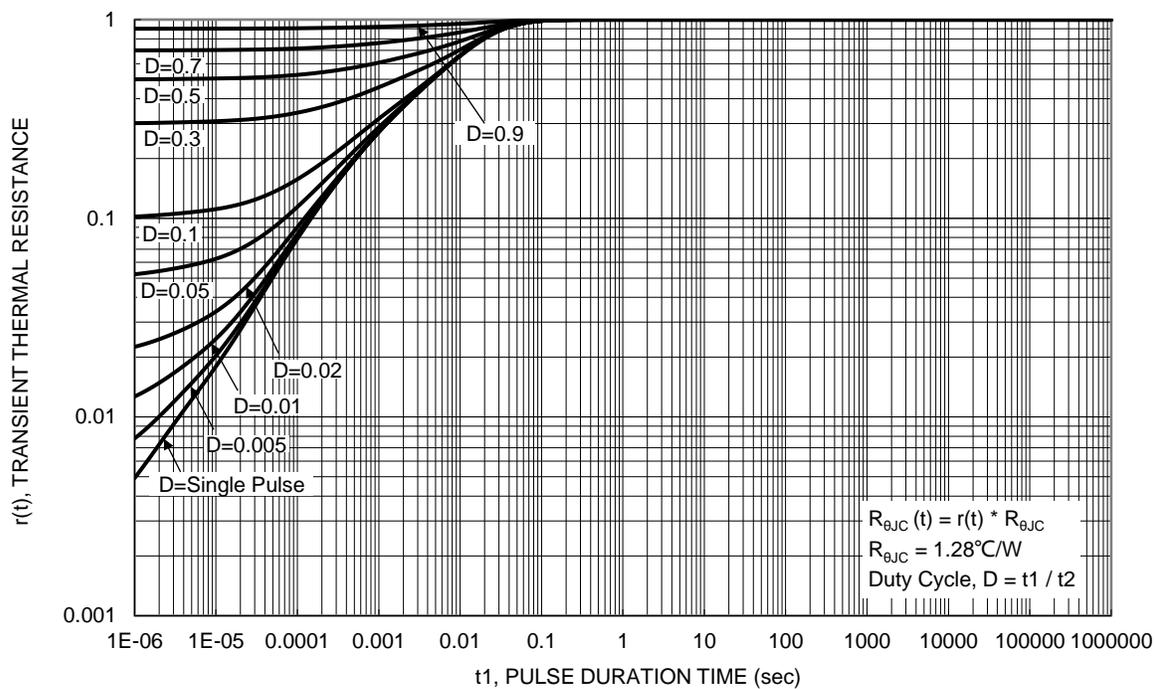
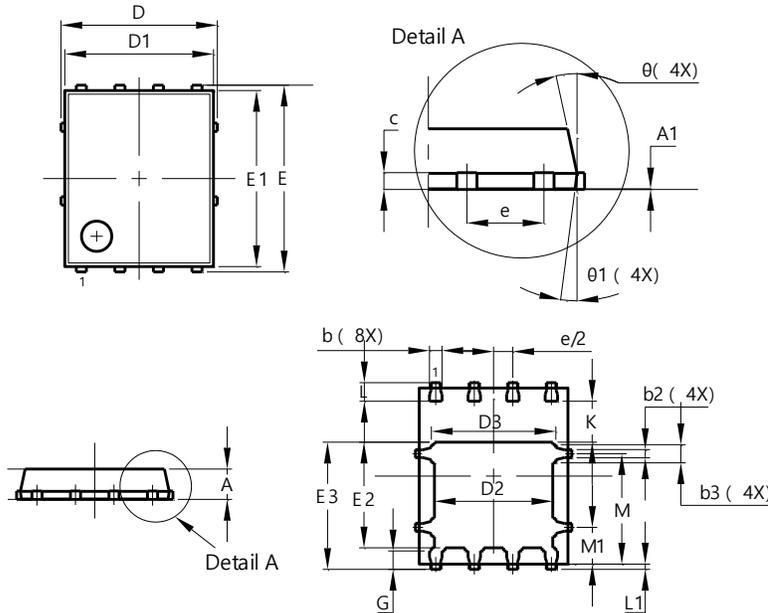


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

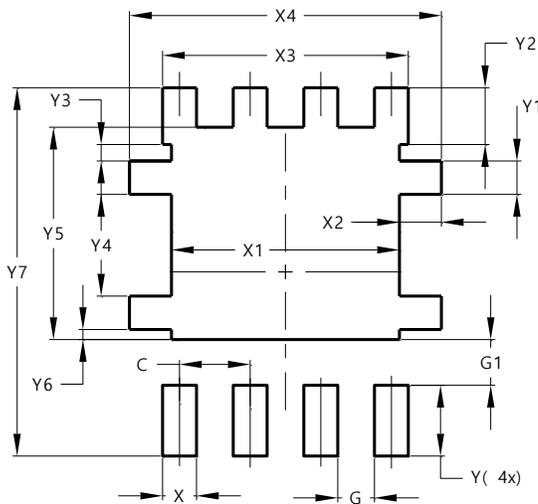
PowerDI5060-8



PowerDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	—
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	—	—
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
θ	10°	12°	11°
θ_1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

PowerDI5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610