



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

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## Product Summary

$BV_{DSS}$	$R_{DS(ON)} \text{ Max}$	$I_D$ $T_C = +25^\circ\text{C}$
60V	$5.6\text{m}\Omega @ V_{GS} = 10\text{V}$	90A

## Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching – Ensures More Reliable and Robust End Application
- Low  $R_{DS(ON)}$  – Minimizes Power Losses
- Low  $Q_g$  – Minimizes Switching Losses

## Description and Applications

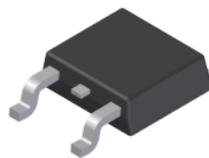
This MOSFET has been designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Engine management systems
- Body control electronics
- DC-DC converters

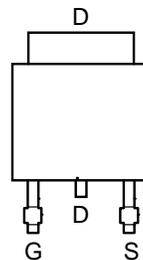
## Mechanical Data

- Package: TO252
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 **e3**
- Weight: 0.33 grams (Approximate)

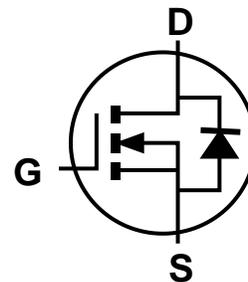
TO252 (DPAK)



Top View



Top View  
Pin Out



Internal Schematic

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6)	I <sub>D</sub>	T <sub>C</sub> = +25°C (Note 9)	90
		T <sub>C</sub> = +100°C	70
Maximum Body Diode Forward Current (Note 6)	I <sub>S</sub>	90	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)	I <sub>DM</sub>	150	A
Avalanche Current, L=1mH	I <sub>AS</sub>	14.8	A
Avalanche Energy, L=1mH	E <sub>AS</sub>	98	mJ

**Thermal Characteristics**

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P <sub>D</sub>	3.9	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	38	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	100	W
Thermal Resistance, Junction to Case (Note 6)	R <sub>θJC</sub>	1.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	—	3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	4.5	5.6	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 50A
		—	5.6	7.2		V <sub>GS</sub> = 6V, I <sub>D</sub> = 20A
		—	7.9	10		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 12.5A
Diode Forward Voltage	V <sub>SD</sub>	—	—	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	2962	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	965.2	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	59.8	—		
Gate Resistance	R <sub>G</sub>	—	0.66	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	47.1	—	nC	V <sub>DD</sub> = 30V, I <sub>D</sub> = 50A
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	23.1	—		
Gate-Source Charge	Q <sub>gs</sub>	—	10.2	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	12.5	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	8.3	—	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 30A, R <sub>G</sub> = 3.3Ω
Turn-On Rise Time	t <sub>r</sub>	—	9.4	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	22	—		
Turn-Off Fall Time	t <sub>f</sub>	—	8.9	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	40.4	—	ns	I <sub>F</sub> = 30A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	49.7	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.
  - Package limited.

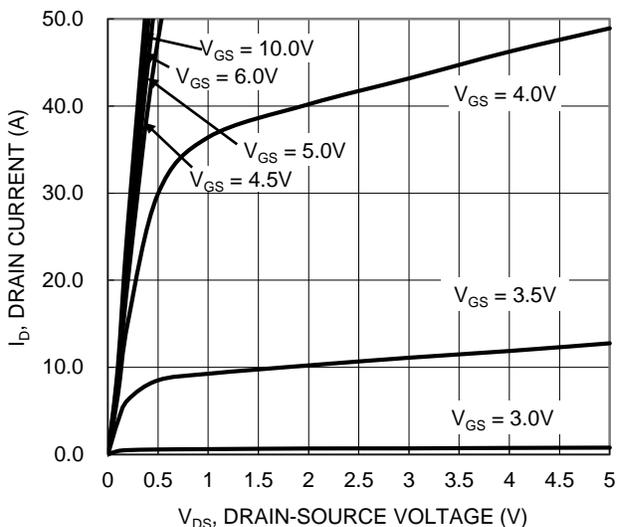


Figure 1. Typical Output Characteristic

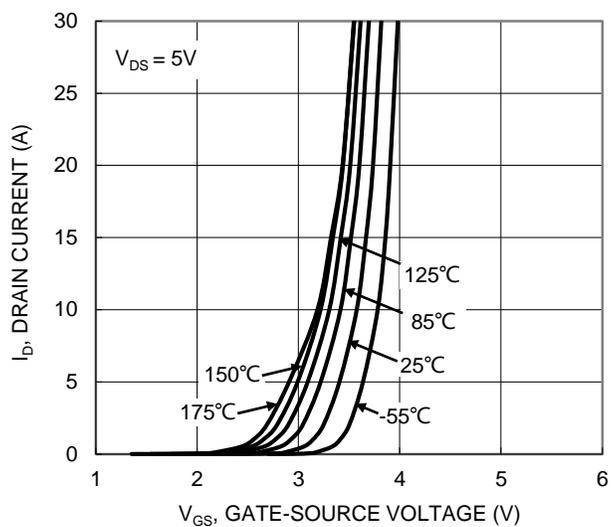


Figure 2. Typical Transfer Characteristic

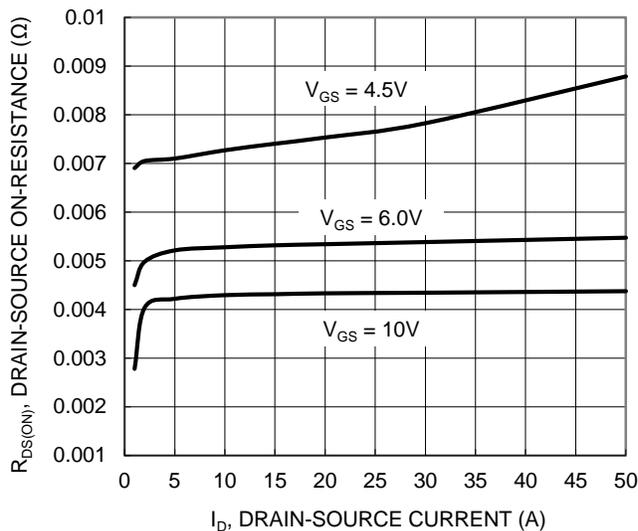


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

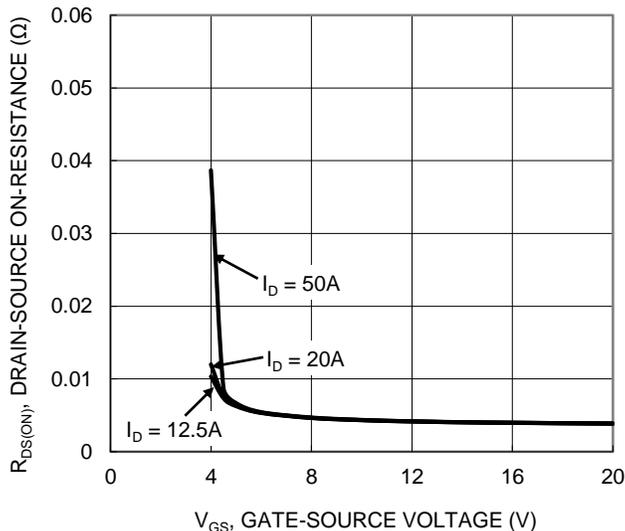


Figure 4. Typical Transfer Characteristic

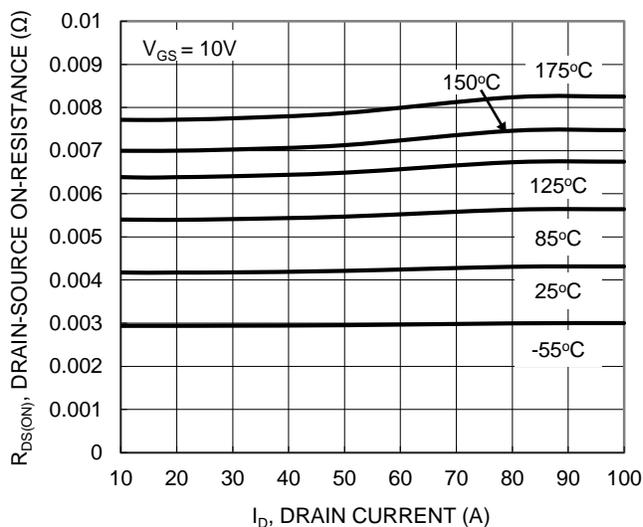


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

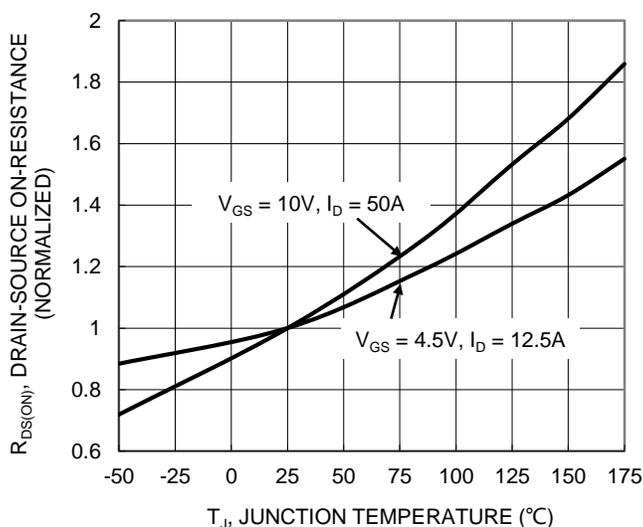


Figure 6. On-Resistance Variation with Junction Temperature

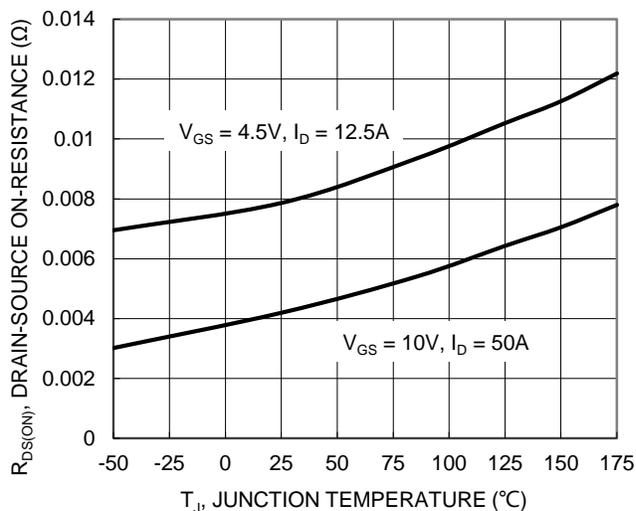


Figure 7. On-Resistance Variation with Junction Temperature

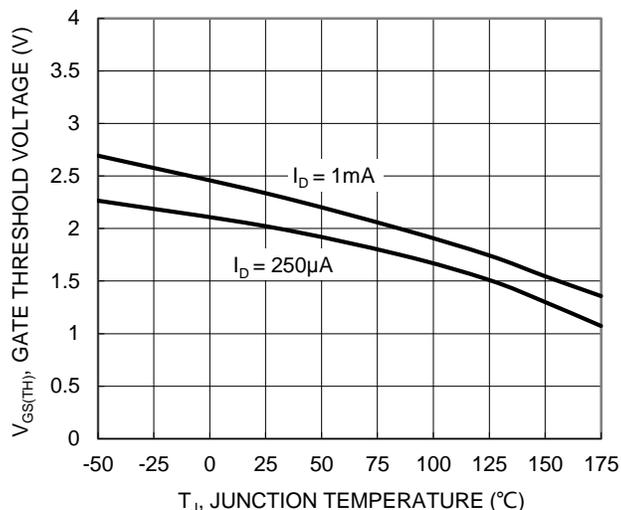


Figure 8. Gate Threshold Variation vs. Junction Temperature

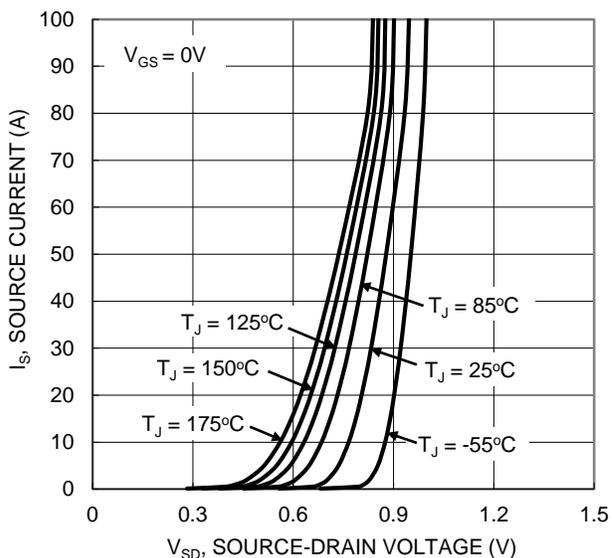


Figure 9. Diode Forward Voltage vs. Current

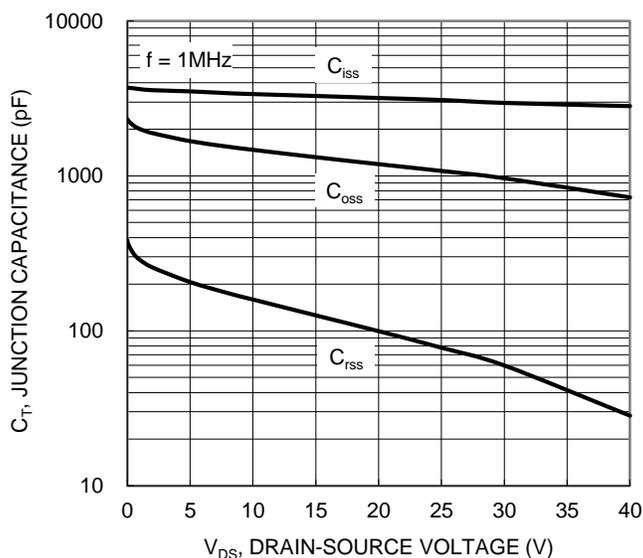


Figure 10. Typical Junction Capacitance

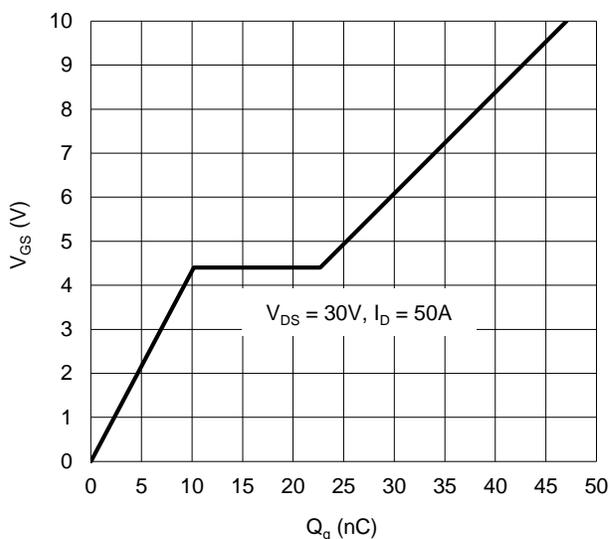


Figure 11. Gate Charge

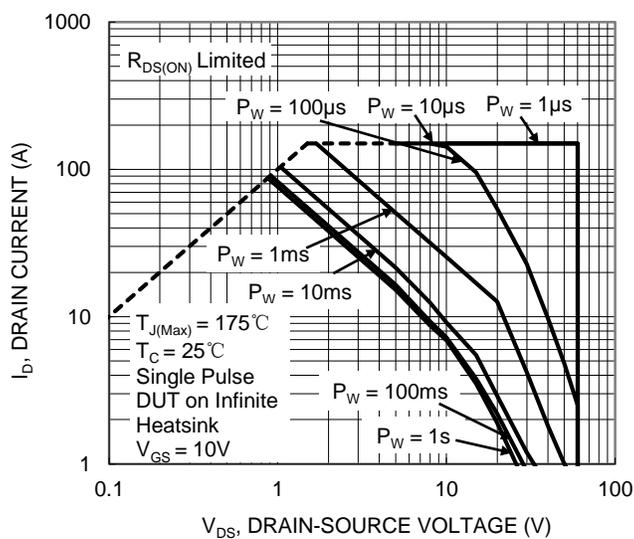


Figure 12. SOA, Safe Operation Area

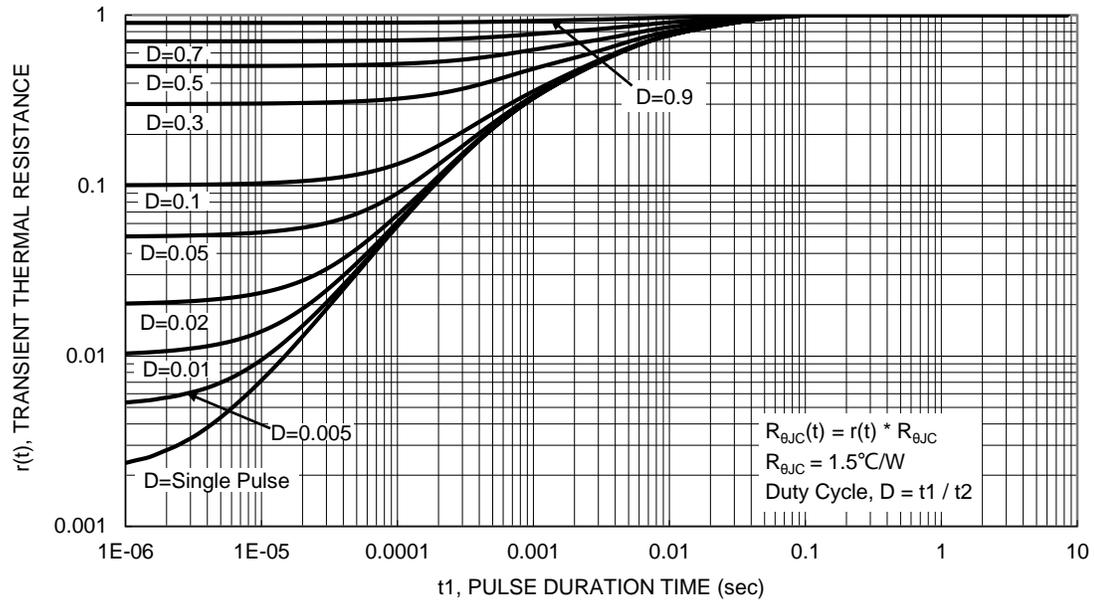
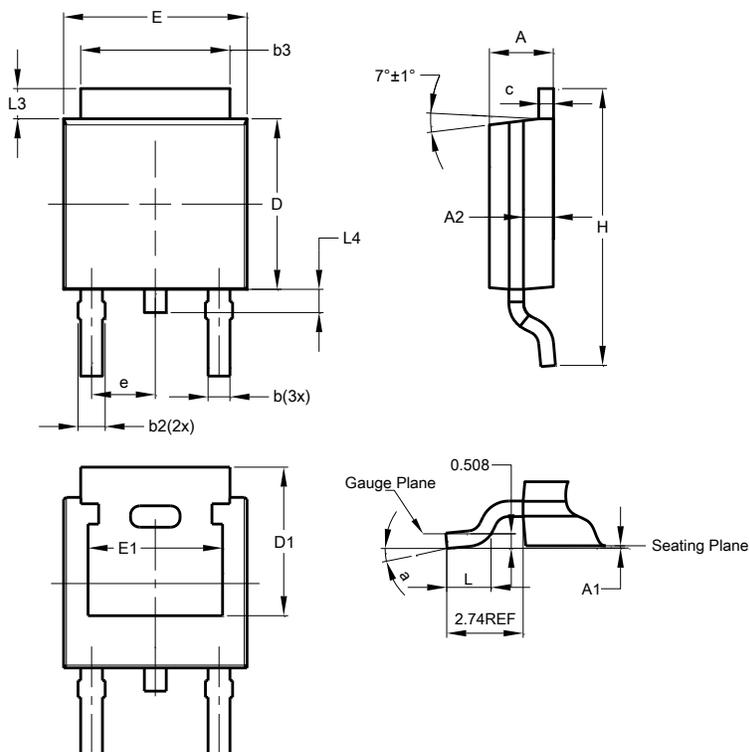


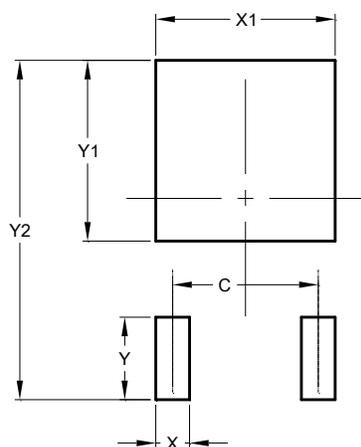
Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

**TO252 (DPAK)**


TO252 (DPAK)			
Dim	Min	Max	Typ
<b>A</b>	2.19	2.39	2.29
<b>A1</b>	0.00	0.13	0.08
<b>A2</b>	0.97	1.17	1.07
<b>b</b>	0.64	0.88	0.783
<b>b2</b>	0.76	1.14	0.95
<b>b3</b>	5.21	5.50	5.33
<b>c</b>	0.45	0.58	0.531
<b>D</b>	6.00	6.20	6.10
<b>D1</b>	5.21	--	--
<b>e</b>	2.286 BSC		
<b>E</b>	6.45	6.70	6.58
<b>E1</b>	4.32	--	--
<b>H</b>	9.40	10.41	9.91
<b>L</b>	1.40	1.78	1.59
<b>L3</b>	0.88	1.27	1.08
<b>L4</b>	0.64	1.02	0.83
<b>a</b>	0°	10°	--
<b>All Dimensions in mm</b>			

## Suggested Pad Layout

**TO252 (DPAK)**


Dimensions	Value (in mm)
<b>C</b>	4.572
<b>X</b>	1.060
<b>X1</b>	5.632
<b>Y</b>	2.600
<b>Y1</b>	5.700
<b>Y2</b>	10.700