



YOUSHANG SEMICONDUCTOR

设计研发新型功率器件

各类小信号开关

中低压及高压大电流等场效应管

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企业微信二维码



企业QQ二维码

Product Summary

BV _{DSS}	Max R _{DS(ON)}	Max I _D T _A = +25°C
250V	8.5Ω @ V _{GS} = 10V	230mA

Description and Applications

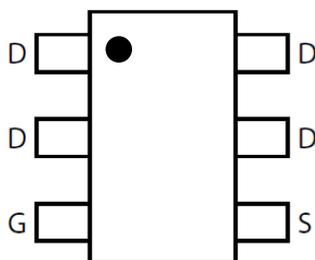
This 250V enhancement mode N-channel MOSFET provides users with a competitive specification. It offers efficient power handling capability, high impedance and is free from thermal runaway and thermally induced secondary breakdowns. Applications benefiting from this device include a variety of telecom and general high-voltage circuits.

SOT89 and SOT223 versions are also available.

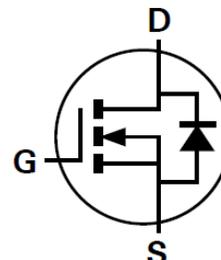
- Earth recall and dialing switches
- Electronic hook switches
- High-voltage power MOSFET drivers
- Telecom call routers
- Solid-state relays



Top View



Pinout Top-View



Device Symbol

Features and Benefits

- High Voltage
- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Complementary P-Channel Type ZVP4525E6

Mechanical Data

- Package: SOT26
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208. ^{Ⓔ3}
- Weight: 0.015 grams (Approximate)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	250	V	
Gate-Source Voltage		V _{GS}	±40	V	
Continuous Drain Current	V _{GS} = 10V	I _D	T _A = +25°C (Note 5)	230	mA
			T _A = +70°C (Note 5)	183	
Pulsed Drain Current (Note 7)		I _{DM}	1.44	A	
Continuous Source Current (Body Diode)		I _S	1.1	A	
Pulsed Source Current (Body Diode)		I _{SM}	1.44	A	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

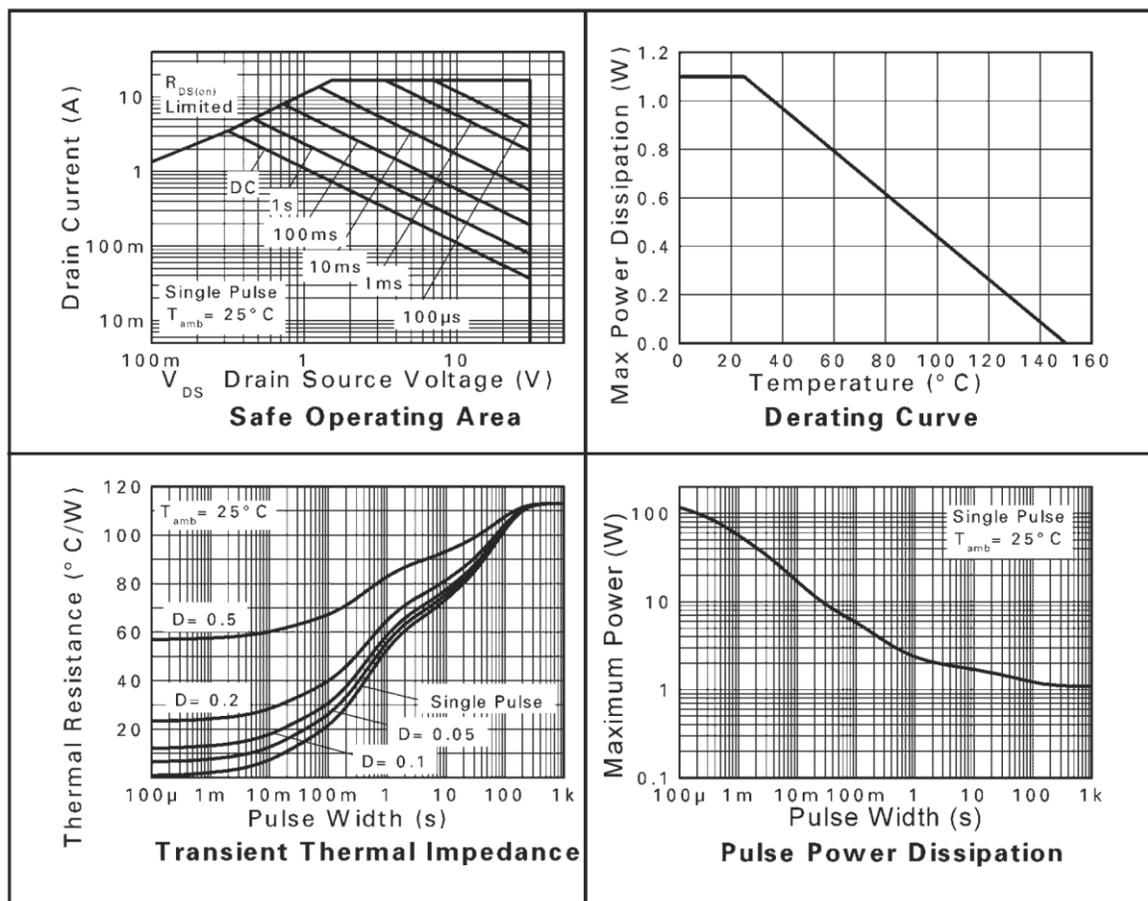
Characteristic	Symbol	Value	Unit
Power Dissipation at T _A = +25°C (Note 5)	P _D	1.1	W
Linear Derating Factor		8.8	mW/°C
Junction to Ambient (Note 5)	R _{θJA}	113	°C/W
Junction to Ambient (Note 6)	R _{θJA}	65	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
 6. For a device surface mounted on FR4 PCB measured at t ≤ 5 secs.
 7. Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal.

NB High Voltage Applications

For high voltage applications, the appropriate industry sector guidelines should be considered with regards to voltage spacing between conductors.

Thermal Characteristics

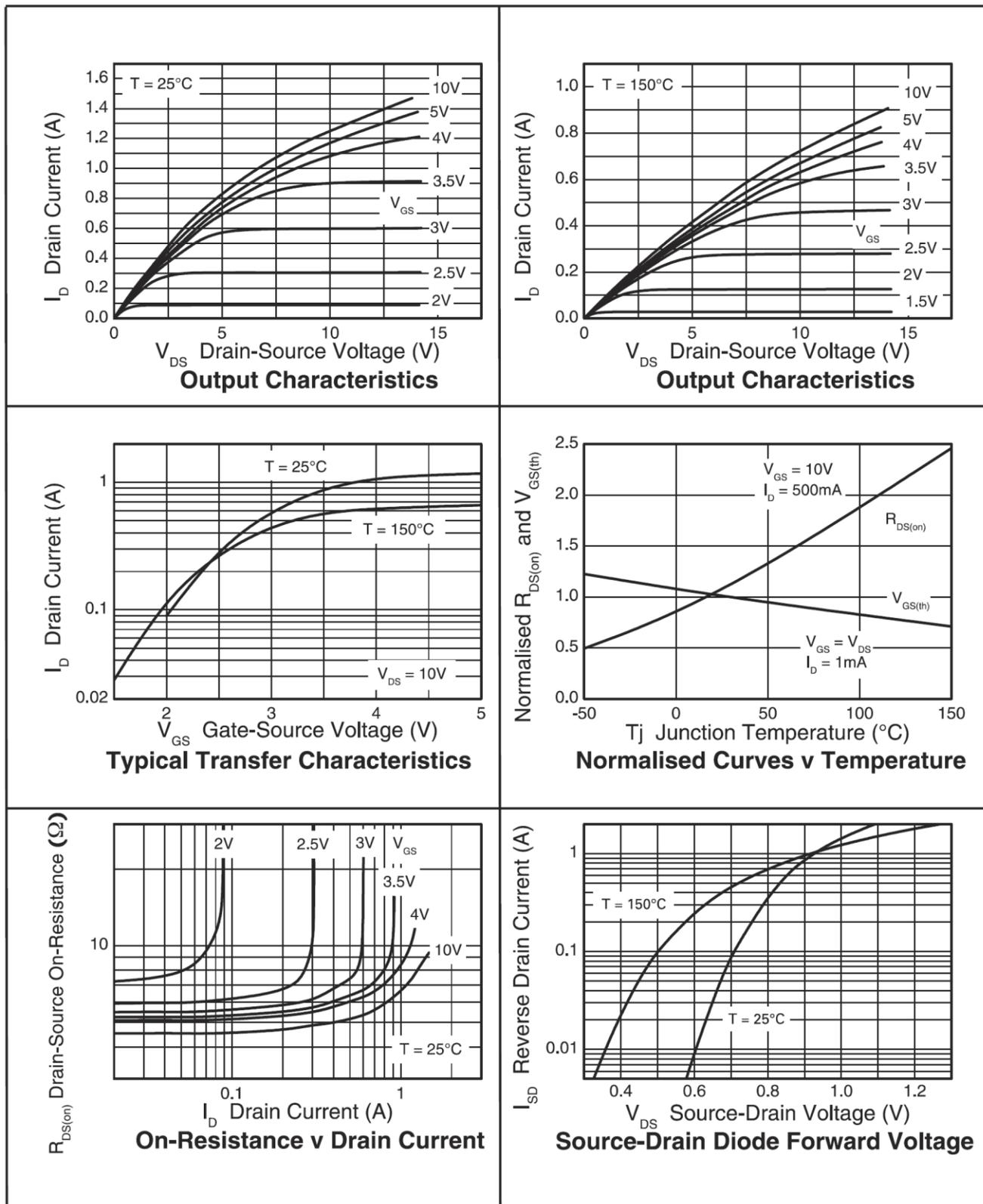


Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

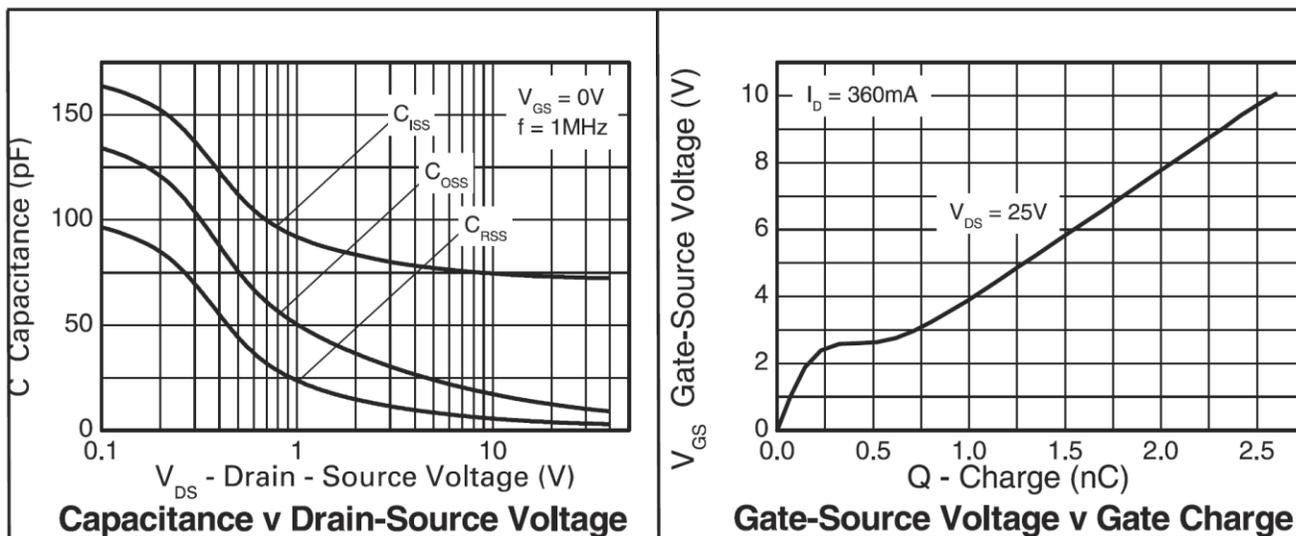
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	250	285	—	V	$I_D = 1\text{mA}$, $V_{GS} = 0$
Zero Gate Voltage Drain Current	I_{DSS}	—	35	500	nA	$V_{DS} = 250\text{V}$, $V_{GS} = 0$
Gate-Body Leakage	I_{GSS}	—	± 1	100	nA	$V_{GS} = \pm 40\text{V}$, $V_{DS} = 0$
ON CHARACTERISTICS						
Gate-Source Threshold Voltage	$V_{GS(th)}$	0.8	1.4	1.8	V	$I_D = 1\text{mA}$, $V_{DS} = V_{GS}$
Static Drain-Source On-State Resistance (Note 8)	$R_{DS(on)}$	—	5.6	8.5	Ω	$V_{GS} = 10\text{V}$, $I_D = 500\text{mA}$
			5.9	9.0		$V_{GS} = 4.5\text{V}$, $I_D = 360\text{mA}$
			6.4	9.5		$V_{GS} = 2.4\text{V}$, $I_D = 20\text{mA}$
Forward Transconductance (Note 10)	g_{fs}	0.3	0.475	—	S	$V_{DS} = 10\text{V}$, $I_D = 0.3\text{A}$
Diode Forward Voltage (Note 8)	V_{SD}	—	—	0.97	V	$T_J = +25^\circ\text{C}$, $I_S = 360\text{mA}$, $V_{GS} = 0$
DYNAMIC CHARACTERISTICS (Notes 9 & 10)						
Input Capacitance	C_{iss}	—	72	—	pF	$V_{DS} = 25\text{V}$, $V_{GS} = 0$, $f = 1\text{MHz}$
Output Capacitance	C_{oss}	—	11	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	3.6	—	pF	
Total Gate Charge	Q_g	—	2.6	3.65	nC	$V_{GS} = 10\text{V}$, $V_{DS} = 25\text{V}$, $I_D = 360\text{mA}$ (refer to test circuit)
Gate-Source Charge	Q_{gs}	—	0.2	0.28	nC	
Gate-Drain Charge	Q_{gd}	—	0.5	0.7	nC	
Reverse-Recovery Time (Note 10)	t_{rr}	—	186	260	ns	$T_J = +25^\circ\text{C}$, $I_F = 360\text{A}$, $di/dt = 100\text{A}/\mu\text{s}$
Reverse-Recovery Charge (Note 10)	Q_{rr}	—	34	48	nC	
Turn-On Delay Time	$t_{d(on)}$	—	1.25	—	ns	$V_{DD} = 30\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 360\text{mA}$, $R_G = 50\Omega$ (refer to test circuit)
Turn-On Rise Time	t_r	—	1.7	—	ns	
Turn-Off Delay Time	$t_{d(off)}$	—	11.40	—	ns	
Turn-Off Fall Time	t_f	—	3.5	—	ns	

- Notes:
8. Measured under pulsed conditions. Width = 300 μs . Duty cycle $\leq 2\%$.
 9. Switching characteristics are independent of operating junction temperature.
 10. For design aid only, not subject to production testing.

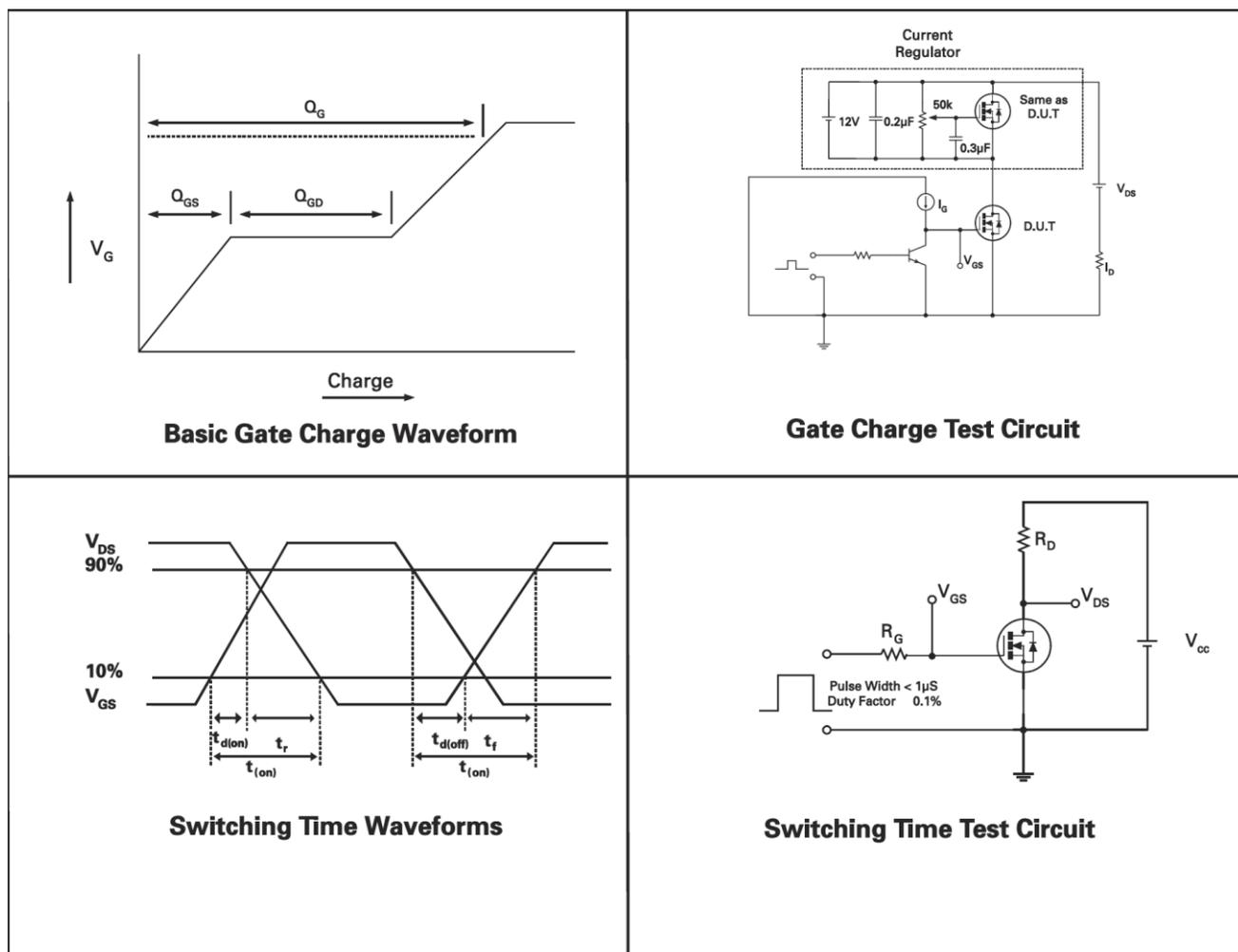
Typical Characteristics



Typical Characteristics (continued)

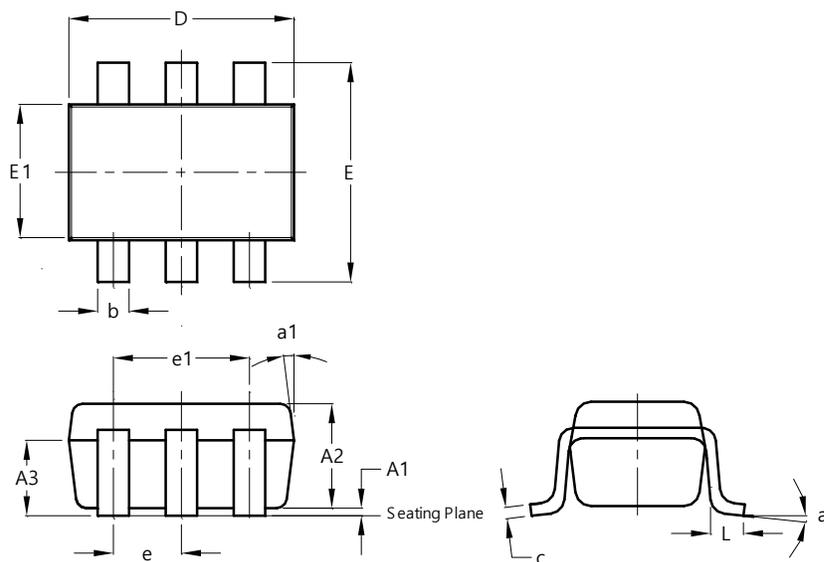


Test Circuits



Package Outline Dimensions

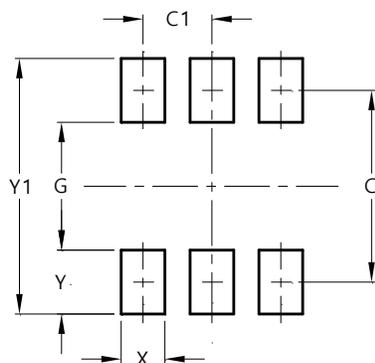
SOT26



SOT26			
Dim	Min	Max	
A1	0.013	0.10	0.05
A2	1.00	1.30	1.10
A3	0.70	0.80	0.75
b	0.35	0.50	0.38
c	0.10	0.20	0.15
D	2.90	3.10	3.00
e	-	-	0.95
e1	-	-	1.90
E	2.70	3.00	2.80
E1	1.50	1.70	1.60
L	0.35	0.55	0.40
a	-	-	8°
a1	-	-	7°
All Dimensions in mm			

Suggested Pad Layout

SOT26



Dimensions	
C	2.40
C1	0.95
G	1.60
X	0.55
Y	0.80
Y1	3.20