



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

0755-83047638

ysbdt@szyoushang.cn

www.szyoushang.cn



企业微信二维码



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## Product Summary

$V_{(BR)DSS}$	Max $R_{DS(ON)}$	Max $I_D$ $T_A = +25^{\circ}C$
20V	0.040 $\Omega$ @ $V_{GS} = 4.5V$	5.4A
	0.055 $\Omega$ @ $V_{GS} = 2.5V$	4.6A
	0.075 $\Omega$ @ $V_{GS} = 1.8V$	4.0A

## Description and Applications

This new generation trench MOSFET from Zetex features low on-resistance achievable with low gate drive.

- DC - DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

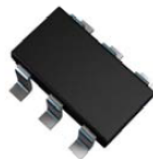
## Features and Benefits

- Low On-resistance
- Fast Switching Speed
- Low Gate Drive Capability

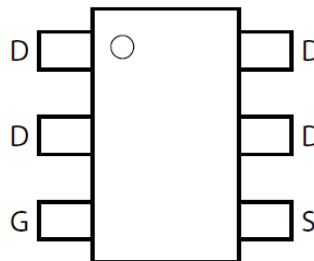
## Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208  $\text{E3}$
- Weight: 0.015 grams (Approximate)

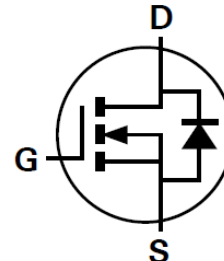
SOT26



Top View



Pinout Top-view



Device Symbol

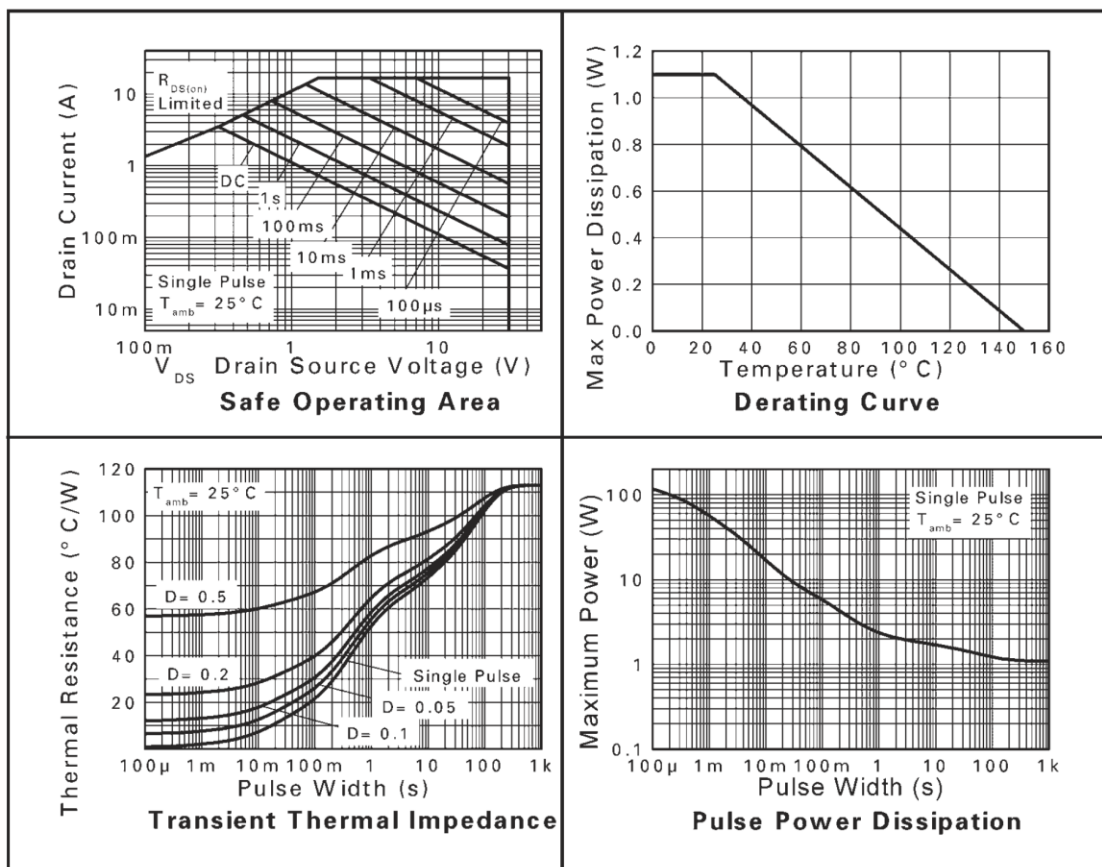
**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		$V_{DS}$	20	V	
Gate-Source Voltage		$V_{GS}$	$\pm 8$	V	
Continuous Drain Current	$V_{GS} = 4.5\text{V}$	$I_D$	$T_A = +25^\circ\text{C}$ (Note 6)	5.4	A
			$T_A = +70^\circ\text{C}$ (Note 6)	4.3	
			$T_A = +25^\circ\text{C}$ (Note 5)	4.3	
Pulsed Drain Current (Note 7)		$I_{DM}$	26	A	
Continuous Source Current (Body Diode) (Note 6)		$I_S$	2.8	A	
Pulsed Source Current (Body Diode) (Note 7)		$I_{SM}$	26	A	

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation at $T_A = +25^\circ\text{C}$ (Note 5)	$P_D$	1.1	W
Linear derating factor (Note 5)		8.8	mW/ $^\circ\text{C}$
Power Dissipation at $T_A = +25^\circ\text{C}$ (Note 6)	$P_D$	1.7	W
Linear derating factor (Note 6)		13.7	mW/ $^\circ\text{C}$
Junction to Ambient (Note 5)	$R_{\theta JA}$	113	$^\circ\text{C}/\text{W}$
Junction to Ambient (Note 6)	$R_{\theta JA}$	73	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

- Notes:
- For a device surface mounted on 25mm x 25mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions.
  - For a device surface mounted on FR-4 PCB measured at  $t \leq 10$  secs.
  - Repetitive rating 25mm x 25mm FR-4 PCB,  $D = 0.02$ , pulse width 300 $\mu\text{s}$  - pulse width limited by maximum junction temperature.

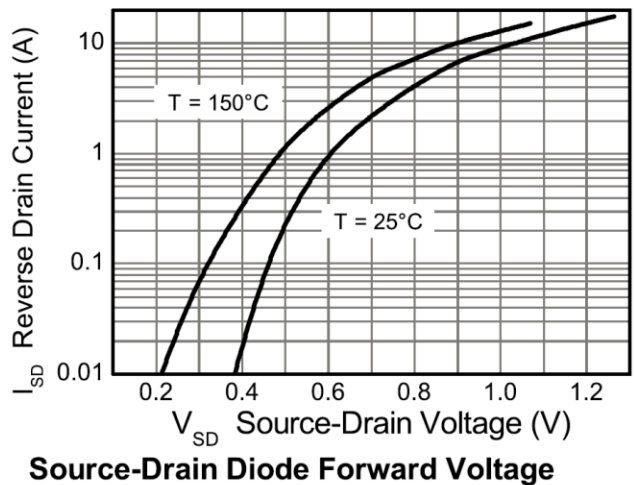
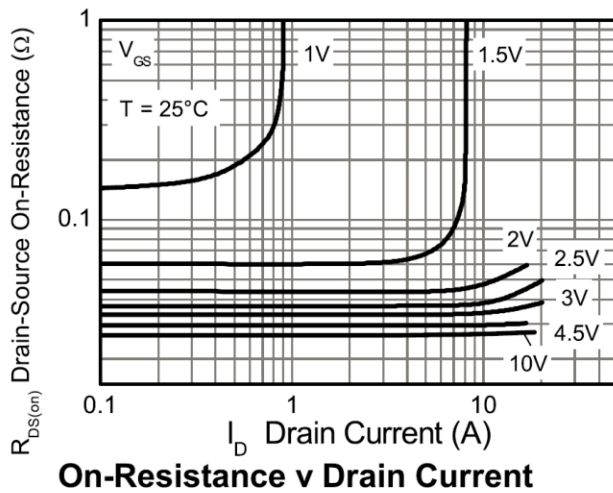
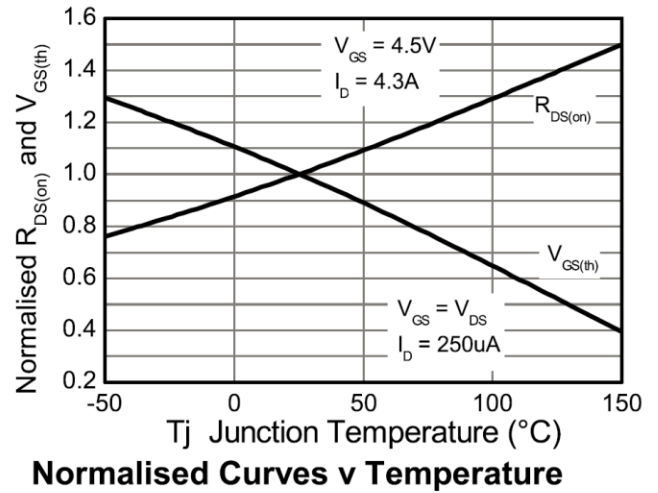
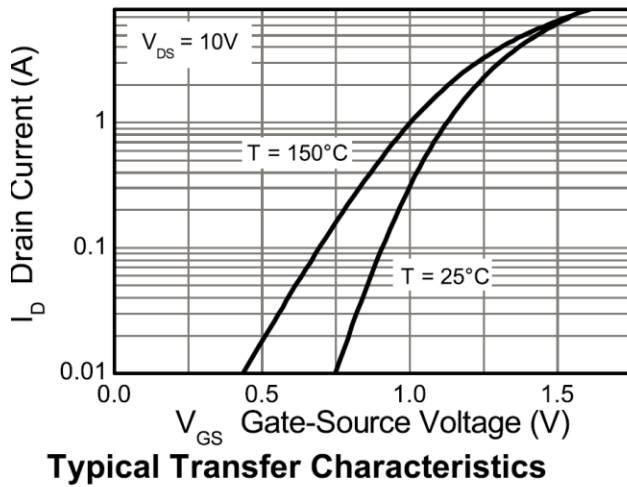
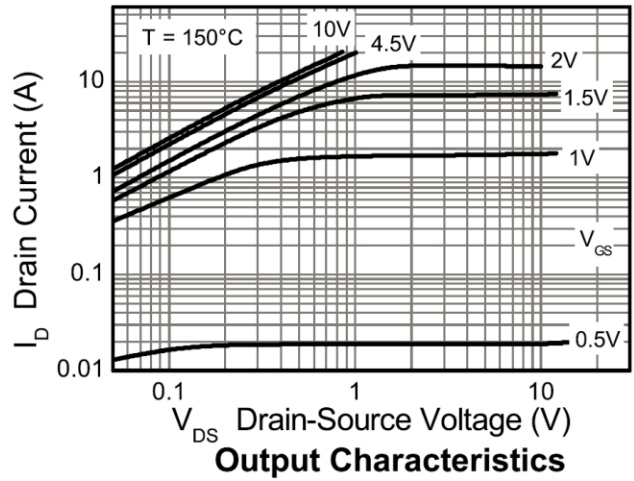
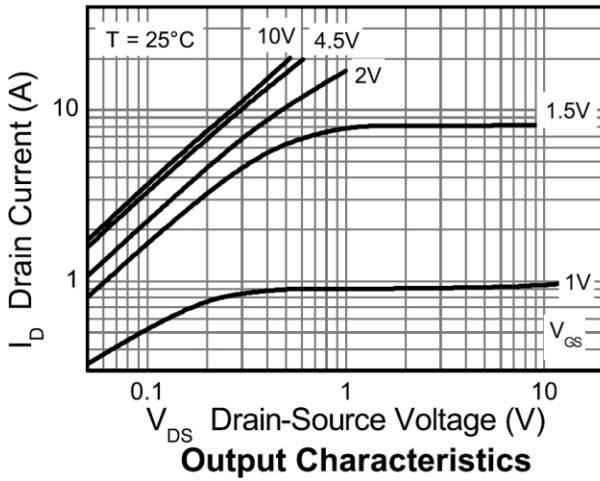
**Thermal Characteristics**


**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

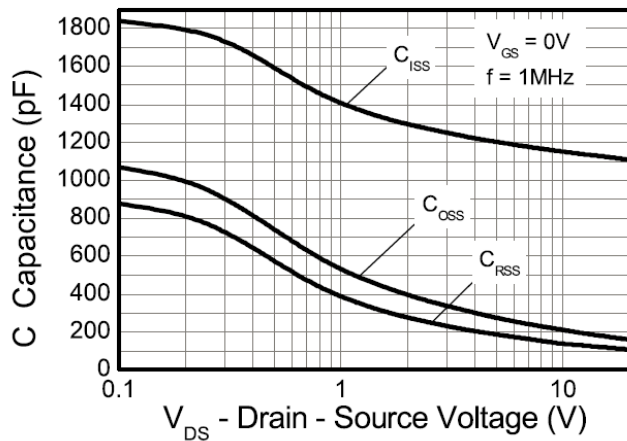
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b>						
Drain-source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-body Leakage	I <sub>GSS</sub>	—	—	100	nA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
Diode Forward Voltage (Note 8)	V <sub>SD</sub>	—	0.67	0.95	V	T <sub>J</sub> = +25°C, I <sub>S</sub> = 1.8A, V <sub>GS</sub> = 0V
<b>ON CHARACTERISTICS</b>						
Gate-source Threshold Voltage	V <sub>GS(th)</sub>	0.4	—	1.0	V	I <sub>D</sub> = 250μA, V <sub>DS</sub> = V <sub>GS</sub>
Static Drain-source On-state Resistance (Note 8)	R <sub>DS(on)</sub>	—	—	0.040	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.3A
				0.055		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3.7A
				0.075		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 3.2A
Forward Transconductance (Notes 8 & 10)	g <sub>fs</sub>	—	13.5	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4.3A
<b>DYNAMIC CHARACTERISTICS (Notes 9 &amp; 10)</b>						
Input Capacitance	C <sub>ISS</sub>	—	1160	—	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1MHz
Output Capacitance	C <sub>OSS</sub>	—	210	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	136	—	pF	
Total Gate Charge	Q <sub>g</sub>	—	14.5	—	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V I <sub>D</sub> = 4.3A
Gate-source Charge	Q <sub>gs</sub>	—	2.0	—	nC	
Gate-drain Charge	Q <sub>gd</sub>	—	2.8	—	nC	
Reverse Recovery Time (Note 10)	t <sub>rr</sub>	—	10.8	—	ns	T <sub>J</sub> = +25°C, I <sub>F</sub> = 2.8A, di/dt = 100A/μs
Reverse Recovery Charge (Note 10)	Q <sub>rr</sub>	—	3.4	—	nC	
Turn-on Delay Time	t <sub>d(on)</sub>	—	2.9	—	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V I <sub>D</sub> = 1A, R <sub>G</sub> = 6.0Ω
Turn-on Rise Time	t <sub>r</sub>	—	6.4	—	ns	
Turn-off Delay Time	t <sub>d(off)</sub>	—	16.0	—	ns	
Turn-off Fall Time	t <sub>f</sub>	—	11.2	—	ns	

- Notes:
8. Measured under pulsed conditions. Width=300μs. Duty cycle ≤ 2%.
  9. Switching characteristics are independent of operating junction temperature.
  10. For design aid only, not subject to production testing.

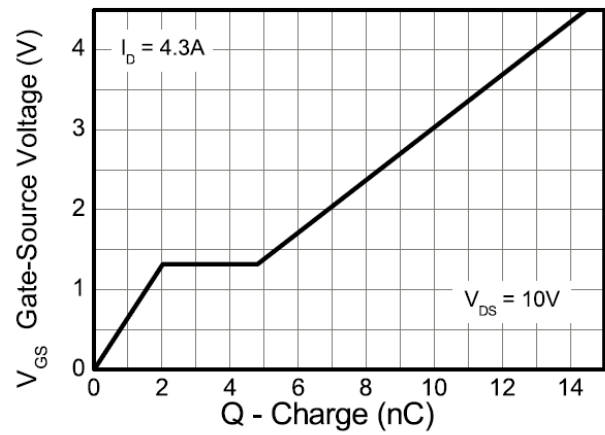
**Typical Characteristics**



**Typical Characteristics** (Cont.)

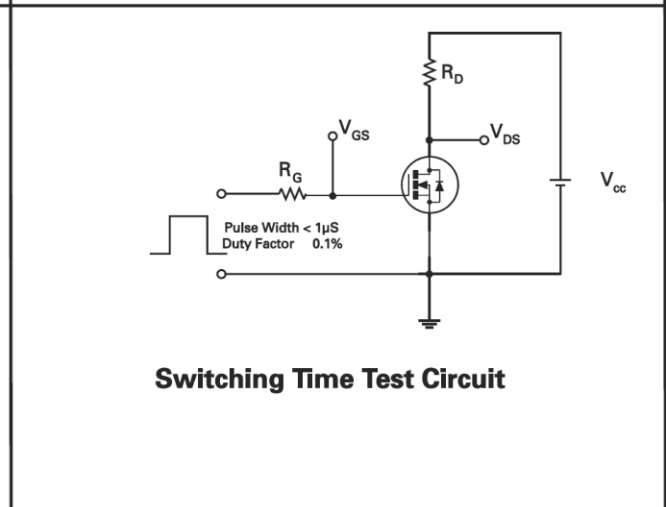
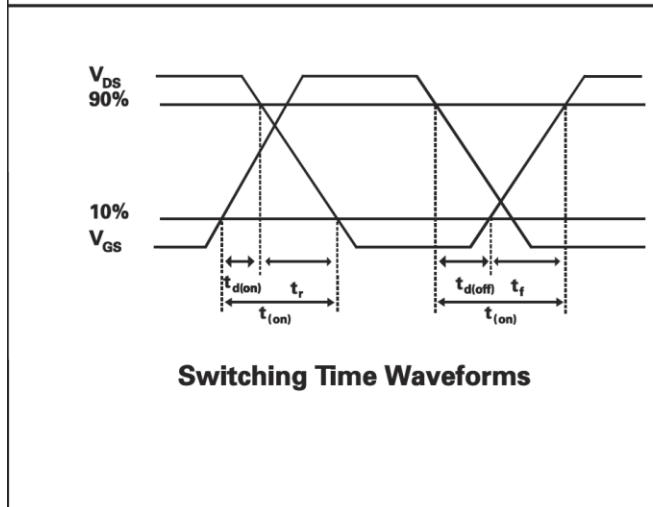
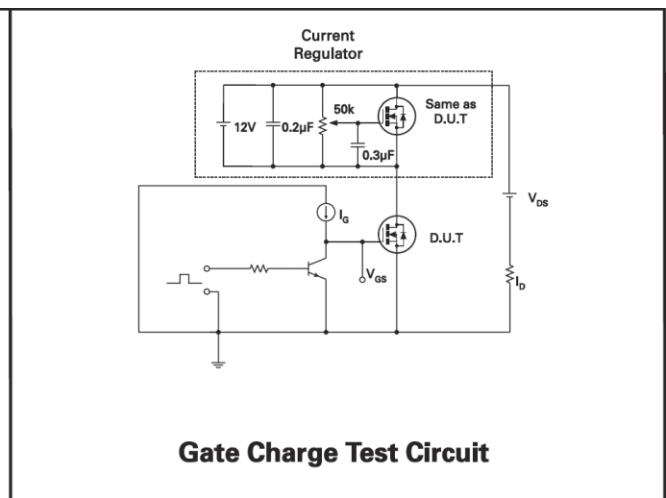
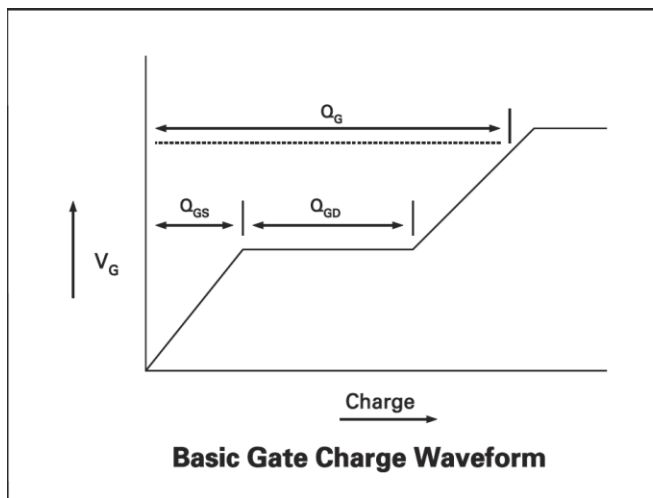


**Capacitance v Drain-Source Voltage**

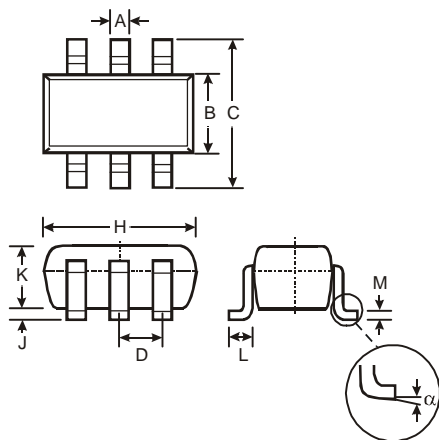


**Gate-Source Voltage v Gate Charge**

**Test Circuits**

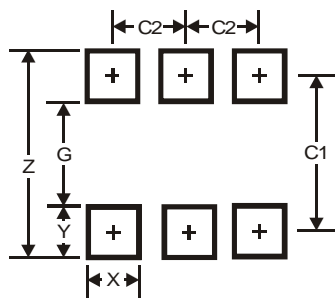


### Package Outline Dimensions



SOT26			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	—	—	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
α	0°	8°	—
All Dimensions in mm			

### Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95