



YOUSHANG SEMICONDUCTOR

**设计研发新型功率器件**

**各类小信号开关**

**中低压及高压大电流等场效应管**

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## Features

- Epitaxial Planar Die Construction
- Built-In Biasing Resistors, R<sub>1</sub> Only

<b>R<sub>1</sub> (NOM)</b>
4.7kΩ

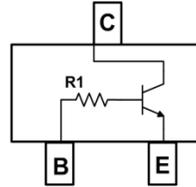
SOT23



Top View

## Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.  
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per  
MIL-STD-202, Method 208 ③
- Weight: 0.008 grams (Approximate)



Device Schematic – Top View

**Absolute Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

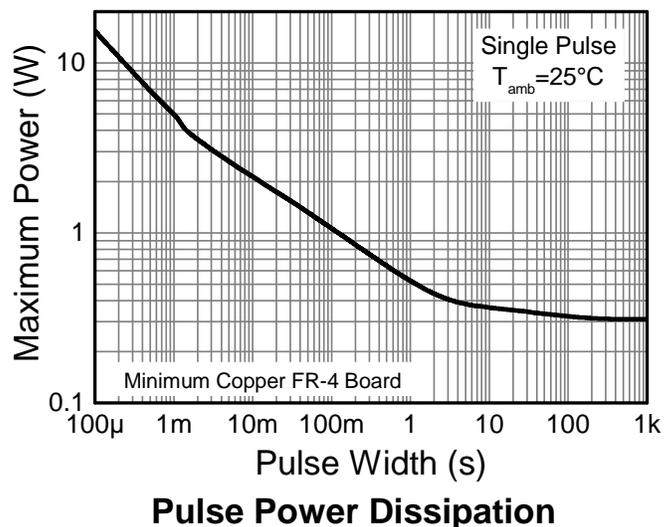
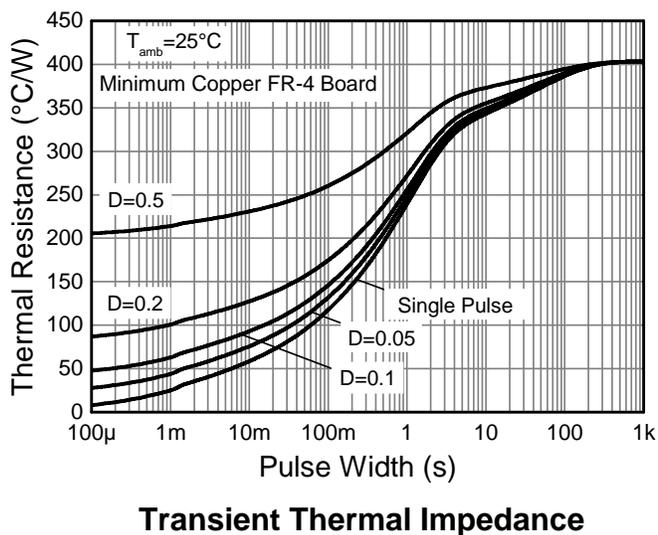
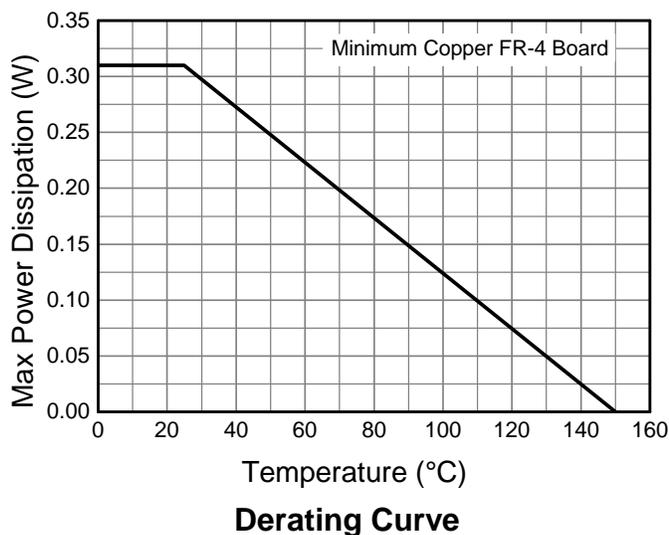
Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	50	V
Collector-Emitter Voltage	$V_{CEO}$	50	V
Emitter-Base Voltage	$V_{EBO}$	5	V
Collector Current	$I_C$ (Max)	100	mA

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	$P_D$	310	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{\theta JA}$	403	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

Note: 6. Mounted on FR-4 PC Board with minimum recommended pad layout.

**Thermal Characteristics and Derating Information**

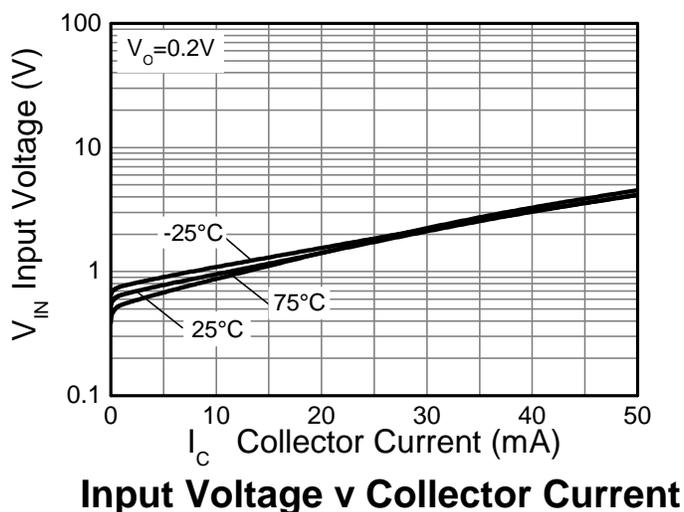
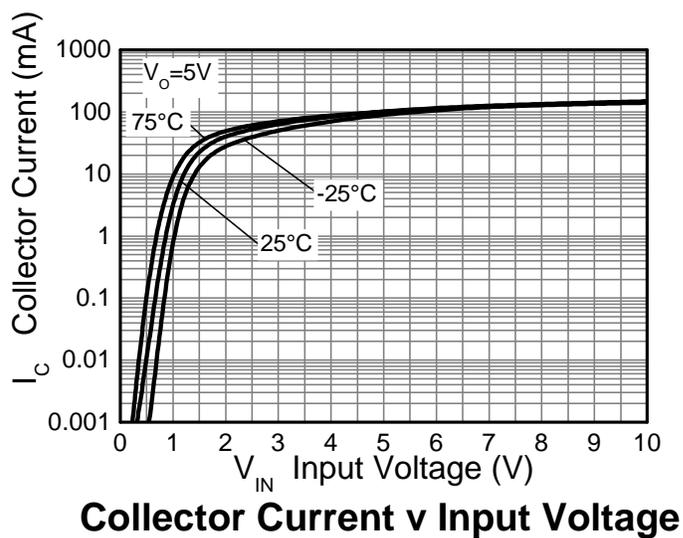
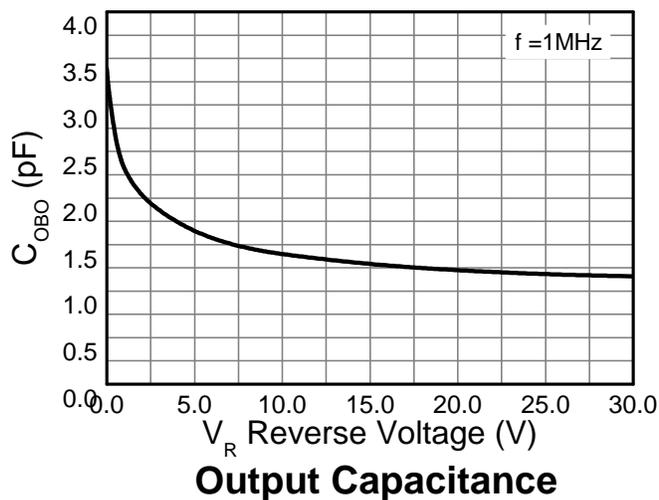
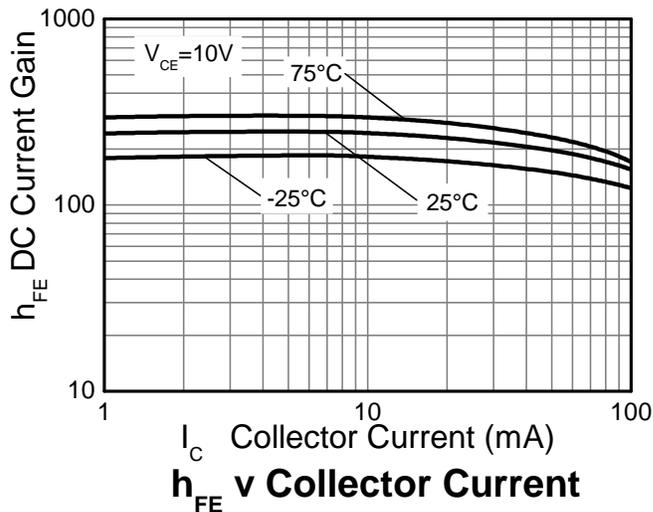
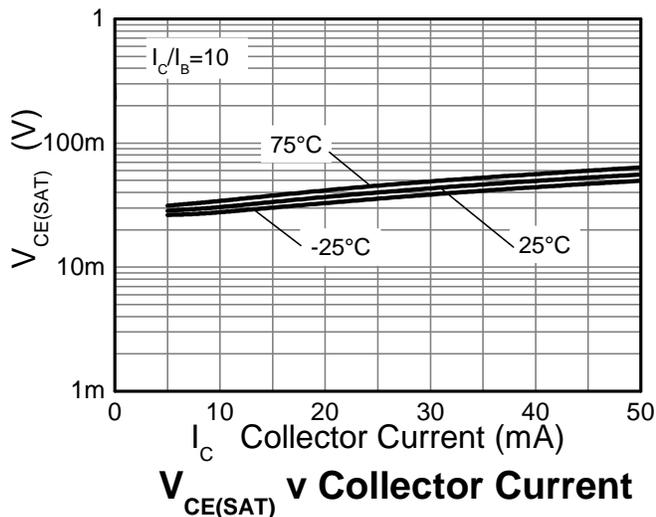


**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	50	—	—	V	I <sub>C</sub> = 50μA
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	50	—	—	V	I <sub>C</sub> = 1mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	5	—	—	V	I <sub>E</sub> = 50μA
Collector Cutoff Current	I <sub>CBO</sub>	—	—	0.5	μA	V <sub>CB</sub> = 50V
Emitter Cutoff Current	I <sub>EBO</sub>	—	—	0.5	μA	V <sub>EB</sub> = 4V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	—	—	0.3	V	I <sub>C</sub> /I <sub>B</sub> = 2.5mA/0.25mA
DC Current Transfer Ratio	h <sub>FE</sub>	100 120	250 —	600 630	—	I <sub>C</sub> = 1mA, V <sub>CE</sub> = 5V I <sub>C</sub> = 5mA, V <sub>CE</sub> = 5V
Input Resistor (R <sub>1</sub> ) Tolerance	ΔR <sub>1</sub>	-30	—	+30	%	—
Gain-Bandwidth Product (Note 7)	f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = 5mA, f = 100MHz

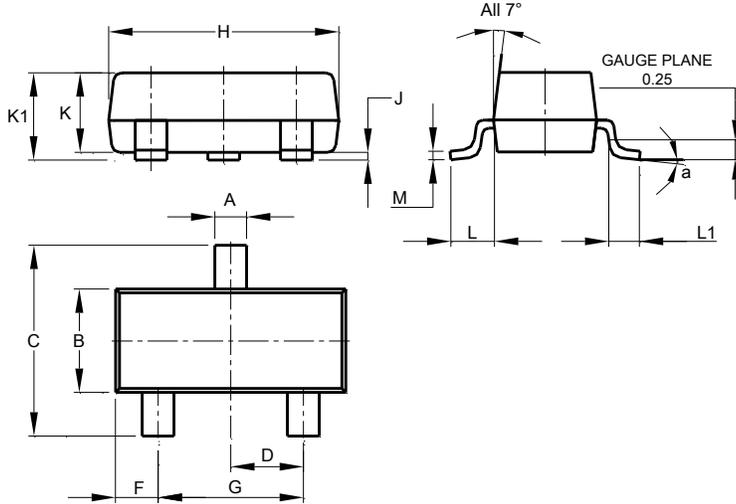
Note: 7. Transistor - For Reference Only.

**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)



**Package Outline Dimensions**

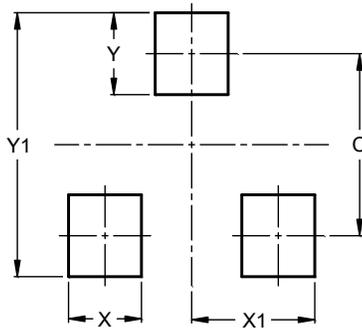
SOT23



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

**Suggested Pad Layout**

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9